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Orderable: TPS25830Q1EVM-040	Designed for: Public Release	Mod. Date: 12/16/2017
TID #: TID	Project Title: TPS25830Q1EVM-040	
Number: PSIL040	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: PSIL040E1_Sch.SchDoc	Size: B
Engineer: Bob Ma	Contact: http://www.ti.com/support	



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DNP FID1 DNP FID2 DNP FID3 DNP FID4 DNP FID5 DNP FID6

PCB LOGO LOGO1 PCB LOGO Pb-Free Symbol PCB LOGO FCC disclaimer

ZZ2 Assembly Note These assemblies are ESD sensitive, ESD precautions shall be observed.
ZZ3 Assembly Note These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
ZZ4 Assembly Note These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Table with 3 columns: Orderable, Project Title, Mod. Date, etc. Includes fields for TID #, Number, SVN Rev, Drawn By, Engineer, Sheet 2 of 2, File, Contact, and Texas Instruments logo.