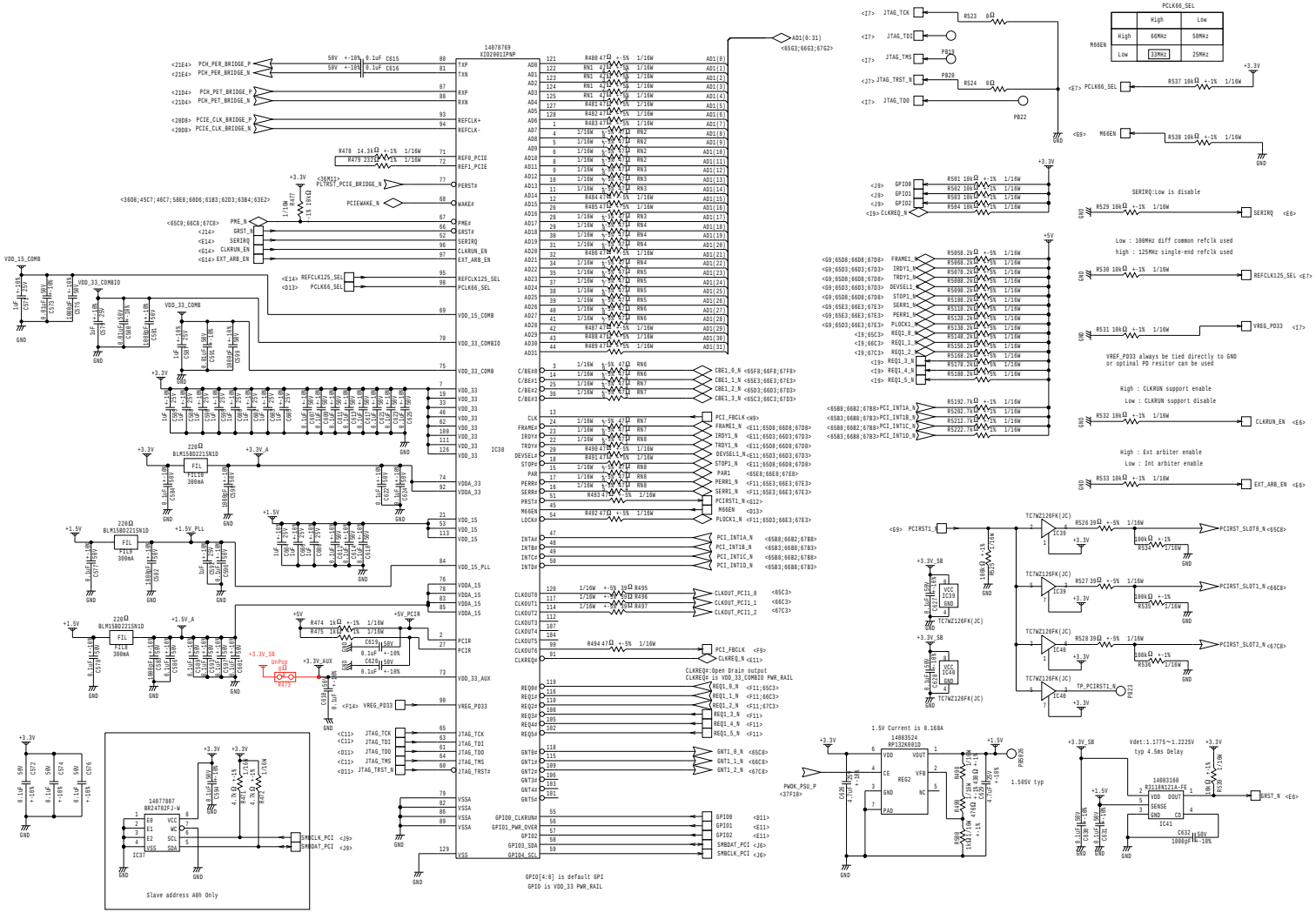
14078769
XIO2001PWP
IC38
PCLK66_SEL
High
Low
M66EN
66MHz
50MHz
33MHz
25MHz
SERIRQ:Low is disable
Low : 100MHz diff common refclk used
high : 125MHz single-end refclk used
VREF_PD33 always be tied directly to GND
or optinal PD resitor can be used
High : CLKRUN support enable
Low : CLKRUN support disable
High : Ext arbiter enable
Low : Int arbiter enable
1.5V Current is 0.168A
1.505V typ
Vdet:1.1775~1.2225V
typ 4.5ms Delay
Slave address A8h Only
GPIO[4:0] is default GPI
GPIO is VDD_33 PWR_RAIL
CLKREQF:Open Drain output
CLKREQE is VDD_33_COMBIO PWR_RAIL

PCIe to PCI