VDDIO
1.8V   1.8V 1.1V   *FPD-Link III*   3.3V 1.2V   VDDIO
                        2 Lane   (3.3V / 1.8V)   *FPD-Link*
                                                                      (Open LDI)

HDMI

Graphics Processor

IN_CLK-/+
IN_D0-/+
IN_D1-/+
IN_D2-/+

CEC
DDC
HPD

DS90UH949-Q1
Serializer

DOUT0+
DOUT0-

DOUT1+
DOUT1-

RIN0+
RIN0-

RIN1+
RIN1-

DS90UH948-Q1
Deserializer

CLK+/-
D0+/-
D1+/-
D2+/-
D3+/-
CLK2+/-
D4+/-
D5+/-
D6+/-
D7+/-

LVDS Display 1080p60 or Graphic Processor

I2C
IDx

D_GPIO
(SPI)

I2C

MCU needed?

I2C
IDx

D_GPIO
(SPI)

Touch Screen Controller

I2C

Audio Codec

I2S

Headphone

Headphone Jack detection

Q3: Do we need an additional MCU on serializer side to configure 949?

Q4: Can we achieve Serializer side I2C transparent transmission to deserializer side to configure other I2C devices like touch screen controller on LCD?

Q1: Can we connect this signal to DS90UH948 GPIO pins for insert detection? Will 948 automatically report GPIO status to 949?

Q2: Do we need an additional MCU on deserializer side to configure 948? Can we achieve Serializer side I2C transparent transmission to deserializer side to configure other I2C devices like touch screen controller on LCD?