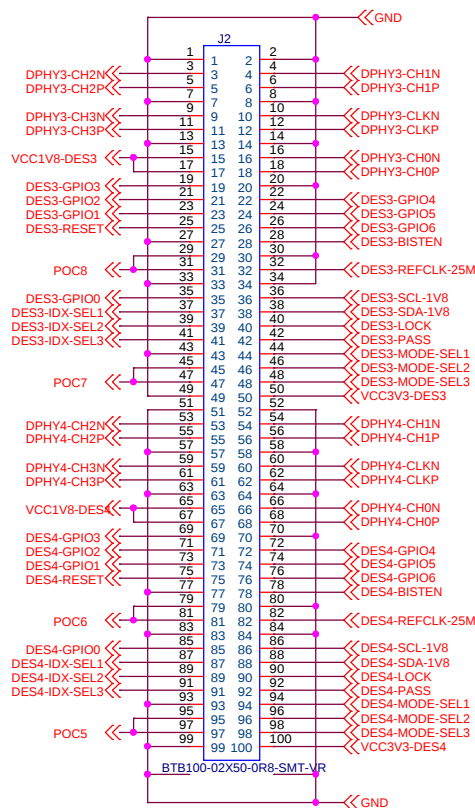
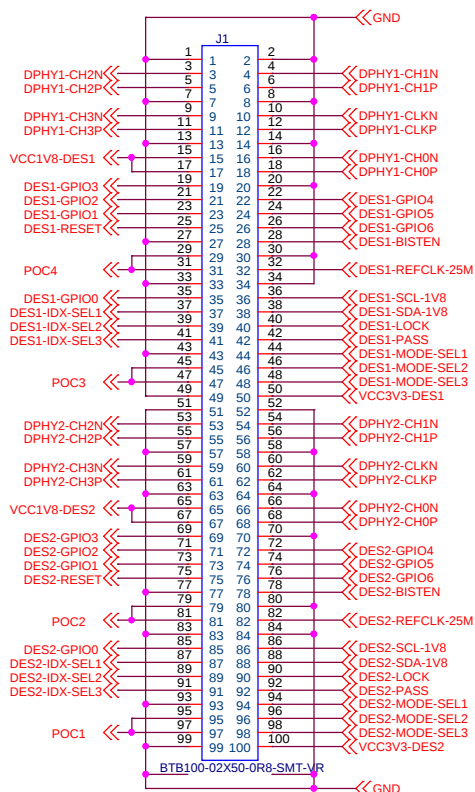
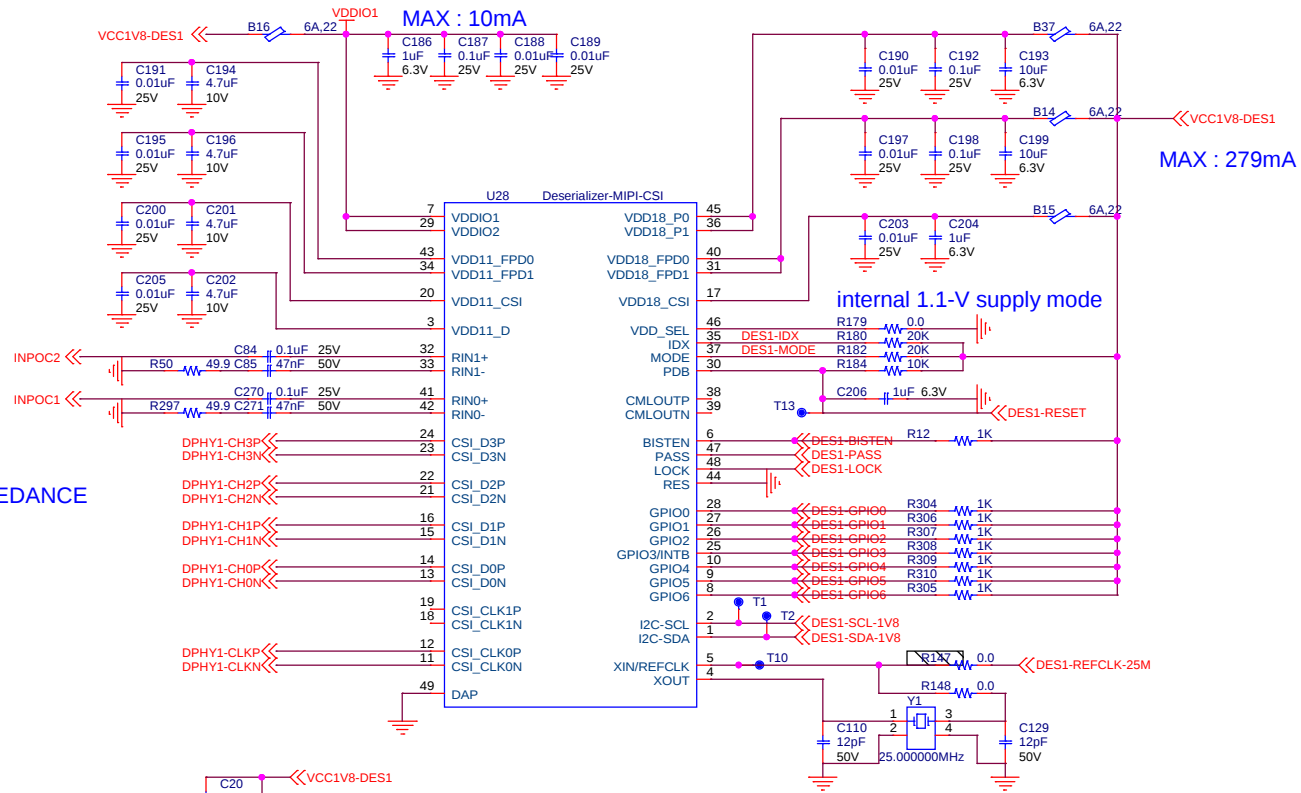


100 OHM DIFFERENTIAL IMPEDANCE

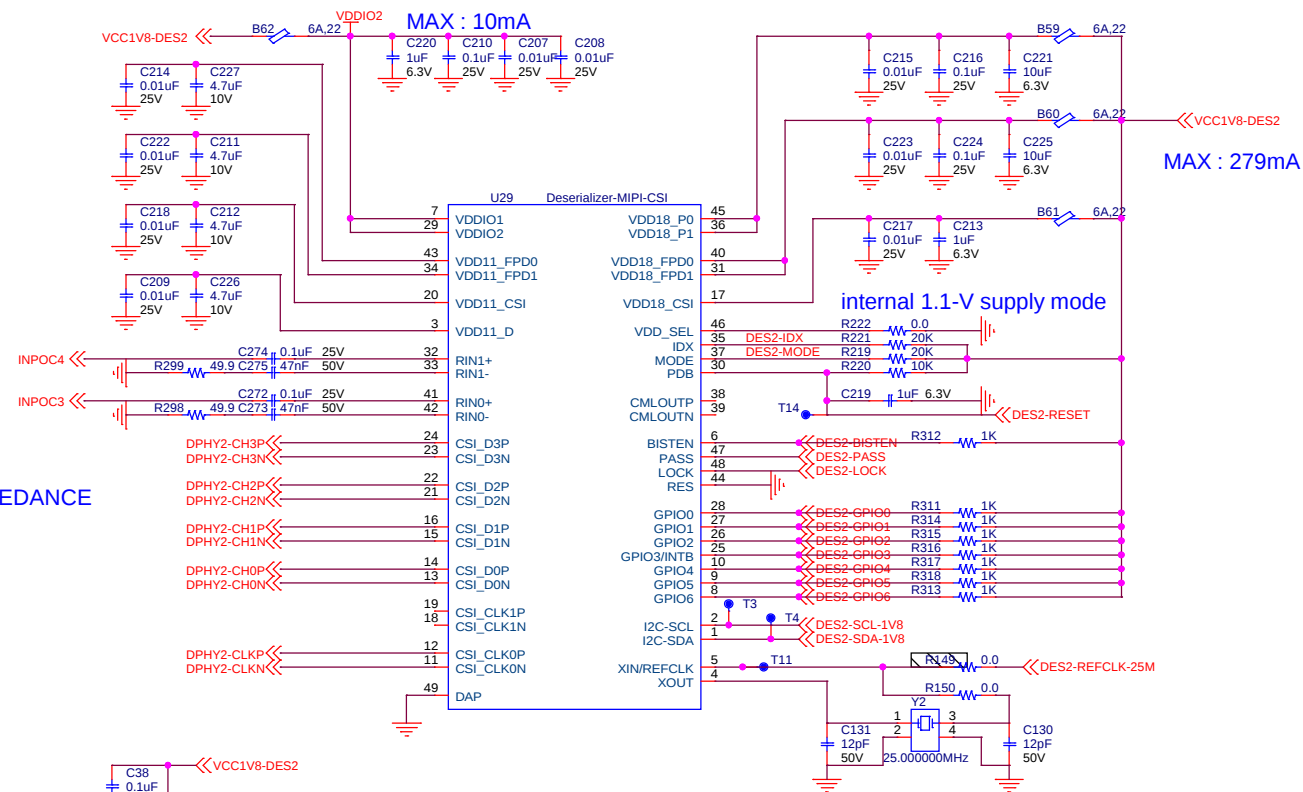


Deserializer SLAVE ADDRESS

SEL[3:1]	Vtype	MIN	MAX	7-BIT	8-BIT
000	0	0	0.131	0x30	0x60
001	0.213	0.179	0.247	0x32	0x64
010	0.330	0.296	0.362	0x34	0x68
011	0.443	0.412	0.474	0x36	0x6C
100	0.559	0.525	0.592	0x38	0x70
101	0.673	0.642	0.704	0x3A	0x74
110	0.792	0.761	0.823	0x3C	0x78
111	VDD	0.876	VDD	0x3D	0x7A

Deserializer MODE

SEL[3:1]	Vtype	MIN	MAX	COAX/STP	RX MODE
000	0	0	0.131	STP	CSI-2 Asynchronous Back Channel
001	0.213	0.179	0.247	STP	RAW12 LF
010	0.330	0.296	0.362	STP	RAW12 HF
011	0.443	0.412	0.474	STP	RAW10
100	0.559	0.525	0.592	COAX	CSI-2 Synchronous Back Channel
101	0.673	0.642	0.704	COAX	RAW12 LF
110	0.792	0.761	0.823	COAX	RAW12 HF
111	VDD	0.876	VDD	COAX	RAW10

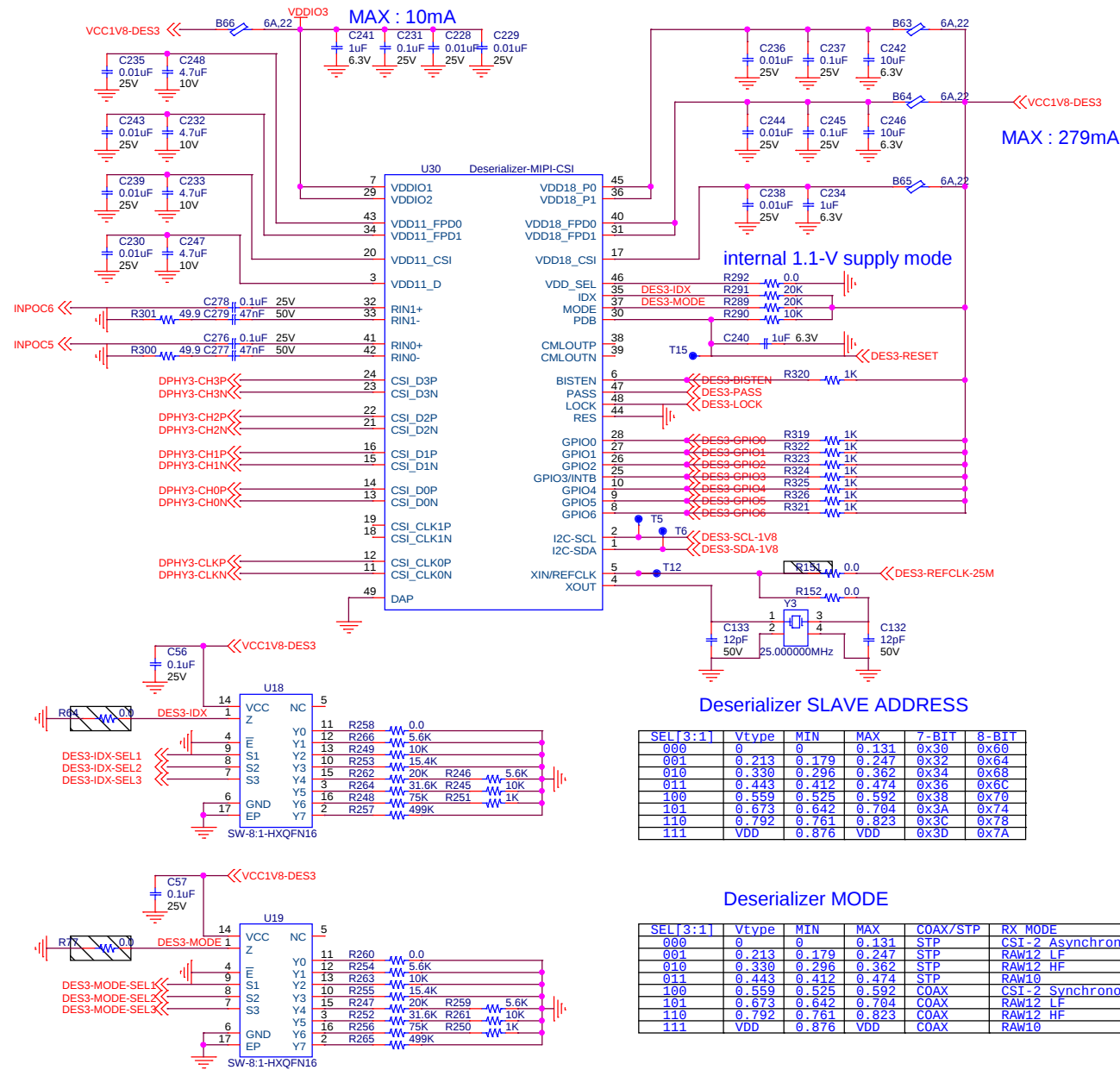


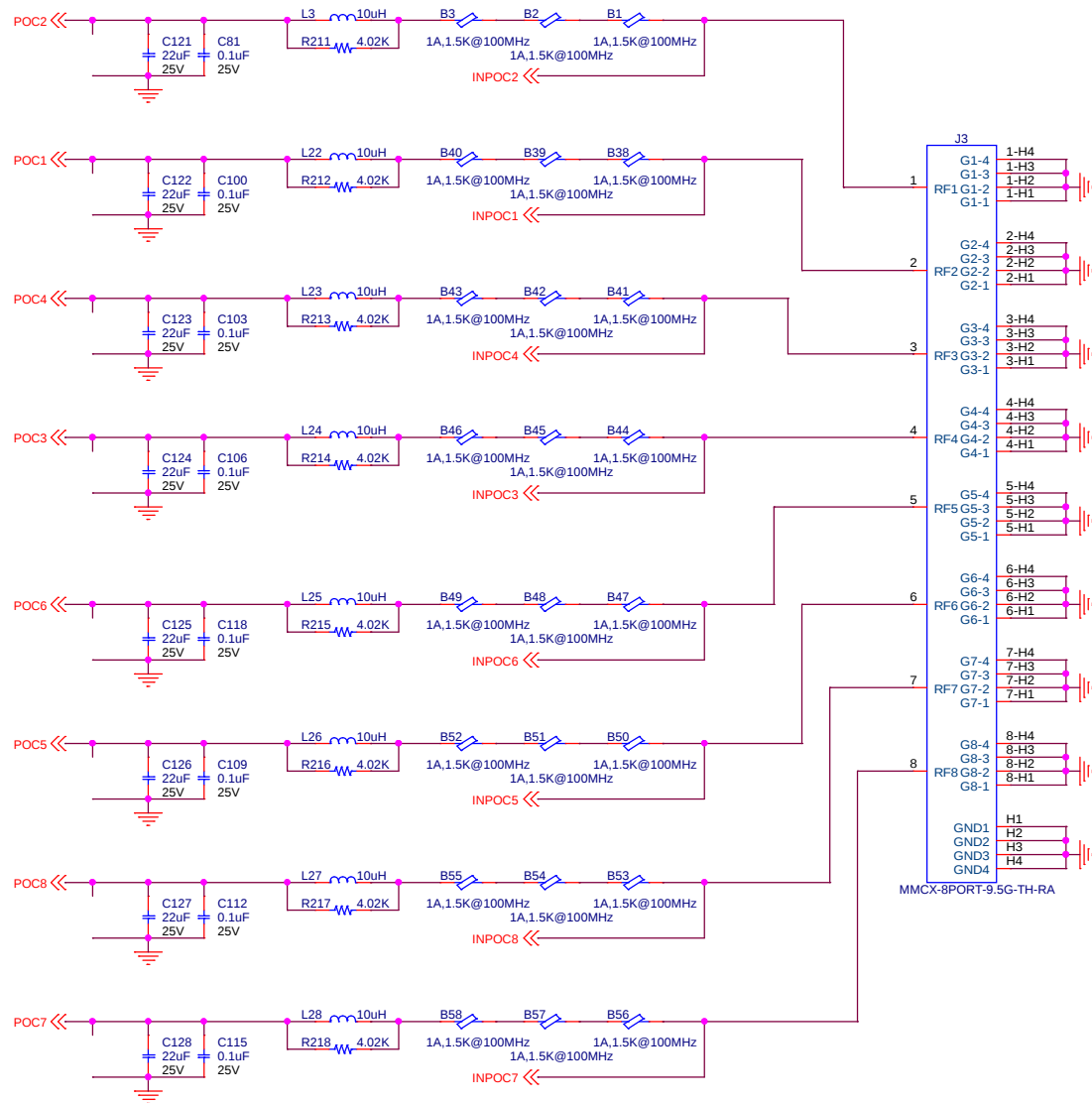
SEL[3:1]	Vtype	MIN	MAX	7-BIT	8-BIT
000	0	0	0.131	0x30	0x60
001	0.213	0.179	0.247	0x32	0x64
010	0.330	0.296	0.362	0x34	0x68
011	0.443	0.412	0.474	0x36	0x6C
100	0.559	0.525	0.592	0x38	0x70
101	0.673	0.642	0.704	0x3A	0x74
110	0.792	0.761	0.823	0x3C	0x78
111	VDD	0.876	VDD	0x3D	0x7A

SEL[3:1]	Vtype	MIN	MAX	COAX/STP	RX MODE
000	0	0	0.131	STP	CSI-2 Asynchronous Back Channel
001	0.213	0.179	0.247	STP	RAW12 LF
010	0.230	0.296	0.362	STP	RAW12 HF
011	0.443	0.412	0.474	STP	RAW10
100	0.559	0.525	0.592	COAX	CSI-2 Synchronous Back Channel
101	0.673	0.642	0.704	COAX	RAW12 LF
110	0.792	0.761	0.823	COAX	RAW12 HF
VDD	0.876	VDD	COAX	RAW10	

DES2-GPIO5 << R39 1K >> VCC1V8-DES2
DES2-GPIO6 << R41 1K >> VCC1V8-DES2

DES1-GPIO5:UART-TX
DES1-GPIO6:UART-RX





SW1-DES1-CONFIG0

NO. ON	MODE	8-BIT
1	I2C	0x90
2		0x94
3		0xD0
4		0xD8
5	UART	0xD8
6		0xD0
7		0x94
8		0x90

SW2-DES1-CONFIG1

NO. ON	CX/TP	GMSL	HIM/RATE
1	COAX	GMSL2	6Gbps
2		GMSL1	HIM enabled
3		GMSL1	HIM disabled
4		GMSL2	6Gbps
5	STP	GMSL2	3Gbps
6		GMSL1	HIM enabled
7		GMSL1	HIM disabled
8		GMSL2	3Gbps

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