

Hi Hao,
Thanks for review the schematic, we have not checked the TVS diode for clamping the Bus signal.
As it is mentioned in the datasheet section 10.2

Link- in Datasheet Part no- SN65HVD30MDREP

10.2 Typical Application

A full-duplex RS-485 network consists of multiple transceivers connecting in parallel to two bus cables. On one signal pair, a master driver transmits data to multiple slave receivers. The master driver and slave receivers can remain fully enabled at all times. On the other signal pair, multiple slave drivers transmit data to the master receiver. To avoid bus contention, the slave drivers must be intermittently enabled and disabled such that only one driver is enabled at any time, as in half-duplex communication. The master receiver can remain fully enabled at all times.

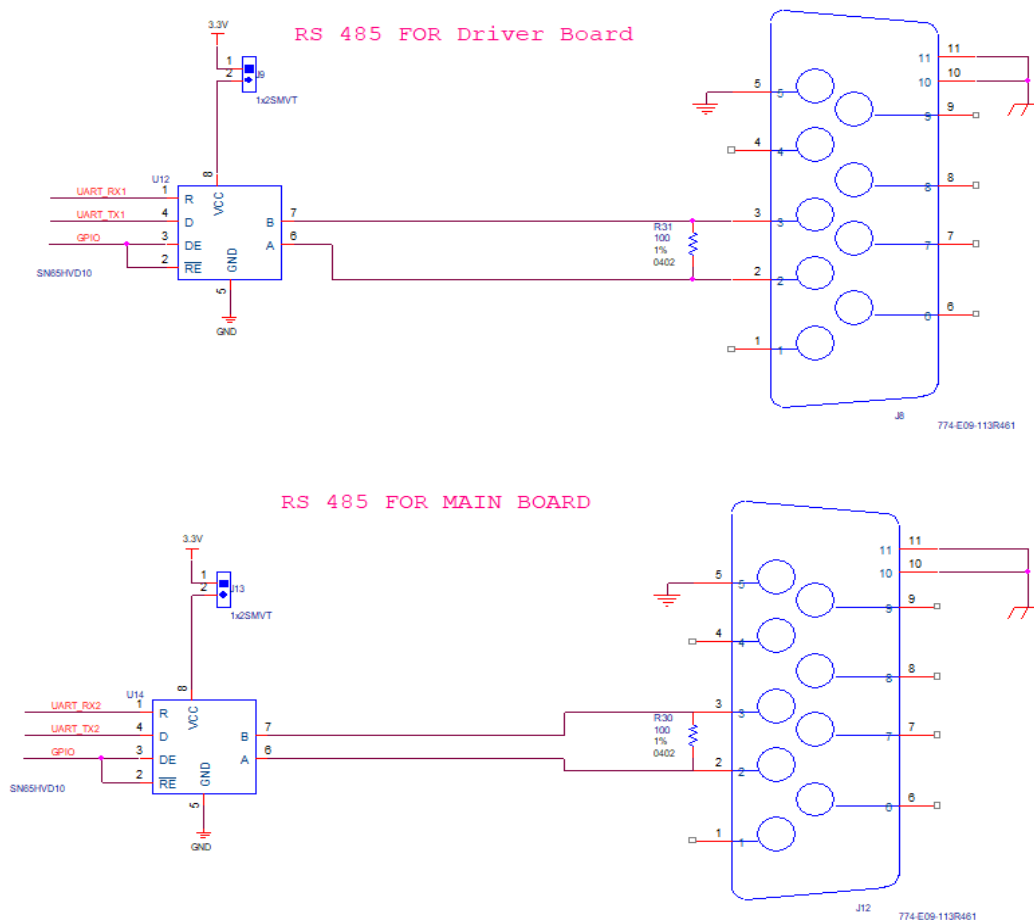
Because the driver cannot be disabled, only connect one driver to the bus when using the SN65HVD30, SN65HVD31, or SN65HVD32 devices.

Please check the above comments and let me know ie it is the only issue or not.

Due to that we are planning to use the different Part no - **SN65HVD10QDREP**

So we are updating the schematic as per below, Please review and let us know ie it is on or need any modification.

Termination resistor is 100 Ohm as mentioned in datasheet.



Yes, you can merge the Rolands post with this post.

Kindly review and let us know your feedback on this.