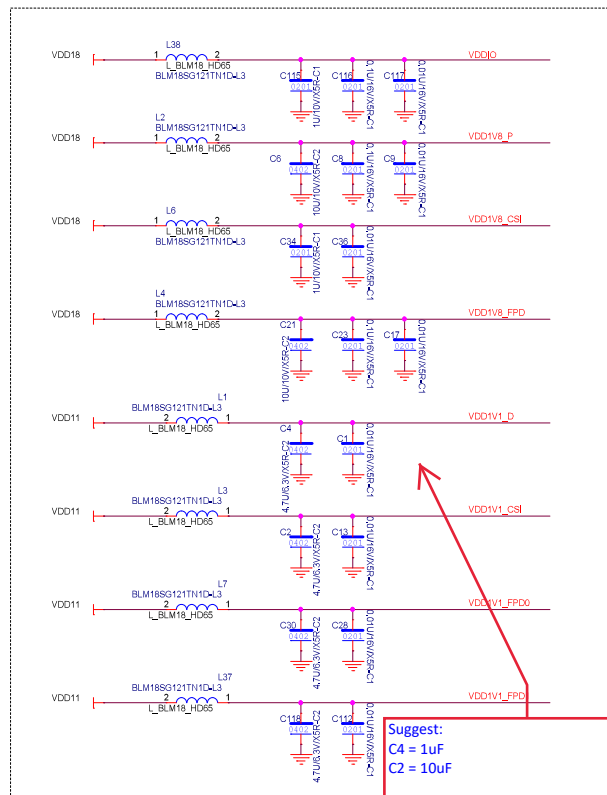
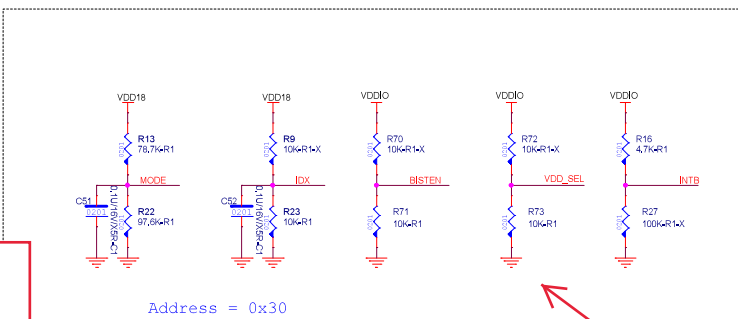
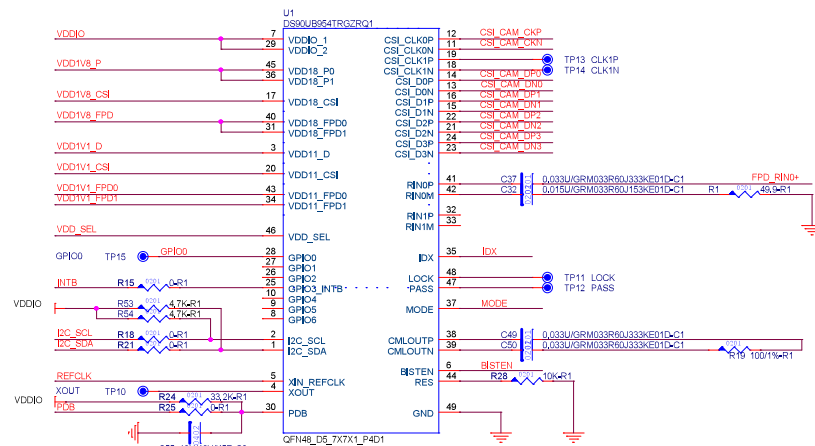


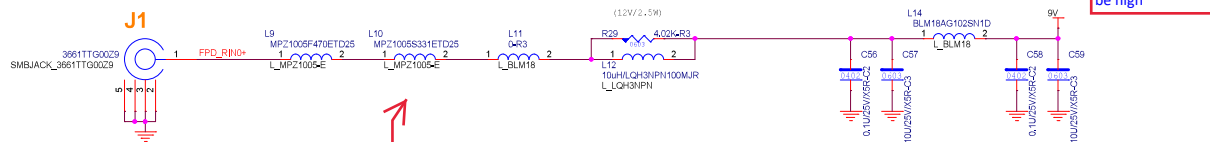
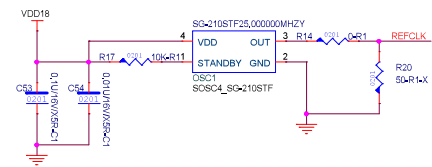
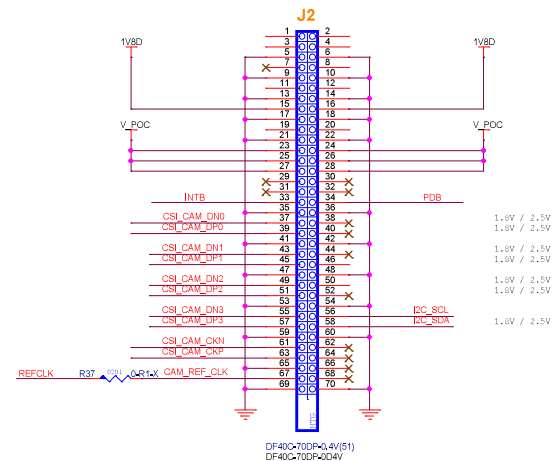
FPD LINK III - R

Suggest:
C4 = 1uF
C2 = 10uF

Group VDD1V1_FPD0 and VDD1V1_FPD1.
Use one 10uF cap for both nets and then
1x 0.01uF cap for each pin



Address = 0x30



PoC network does not match datasheet recommendation. Suggest to use PoC network exact components from the 954 datasheet

Schematic looks to be configured for external VDD11 mode so VDD_SEL should be high