FPD LINK III - R U1 DS90UB954TRGZRQ1 1 2 L BLM18 HD65 BLM18SG121TN1DH 3 VDDIO_1 VDDIO_2 VDD1V8_P VDD1V8_CSI L2 BLM18SG121TN1D+L3 VDD18_CS VDD1V8_FPD V_POC 40 31 VDD18_FPD0 VDD18_FPD1 V_POC L_BLM18_HD65 VDD1V1_D VDD11_D VDD1V1_CS VDD11_CS 43 34 VDD11_FPD0 VDD11_FPD1 VDD1V8_CS RIN1P 32 RIN1M 33 VDD_SEL 1.8V / 2.5V 1.8V / 2.5V 28 GPIO0 26 GPIO1 25 GPIO2 10 GPIO3_INTB-9 GPIO5 GPIO6 GPI00 TP15 GPI00 L4 BLM18SG121TN1D-L3 1 2 L_BLM18_HD65 1.8V / 2.5V 1.8V / 2.5V VDD1V8_FPD MODE 1.8V / 2.5V 2 1 |2C_SCL 12C SDA R19 100/1%-R1 REFCLK R37 0201 O.R1-X CAM_REF_CLK L1 BLM18SG121TN1D-L3 XOUT L_BLM18_HD65 30 PDB GND QFN48_D5_7X7X1_P4D1 L3 BLM18SG121TN1D-L3 L_BLM18_HD65 SG-210STF25,000000MHZY VDD OUT 3 R14 0001 OR1 R17 10K-R11 STANDBY GND 2 R20 50-R1-X BLM18SG121TN1D-L3 OSC1 SOSC4_SG-210STF L_BLM18_HD65 R70 10K-R1-X R72 10K-R1-X L37 BLM18SG121TN1D-L3 R23 10K-R1 R71 10K-R1 R73 10K-R1 R27 100K-R1-X L_BLM18_HD65 Suggest: C4 = 1uF C2 = 10uF Address = 0x30Group VDD1V1_FPD0 and VDD1V1_FPD1 Use one 10uF cap for both nets and then Schematic looks to be configured for 1x 0.01uF cap for each pin external VDD11 mode so VDD_SEL should be high (12V/2.5W) J1 R29 4.02K-R3 L9 L10 MPZ1005F470ETD25 MPZ1005S331ETD25 3661TTG00Z9 SMBJACK_3661TTG00Z9 1 2 C56 C58 L12 10uH/LQH3NPN100MJR L_LQH3NPN PoC network does not match datasheet recommendation. Suggest to use PoC network exact components from the 954 datasheet