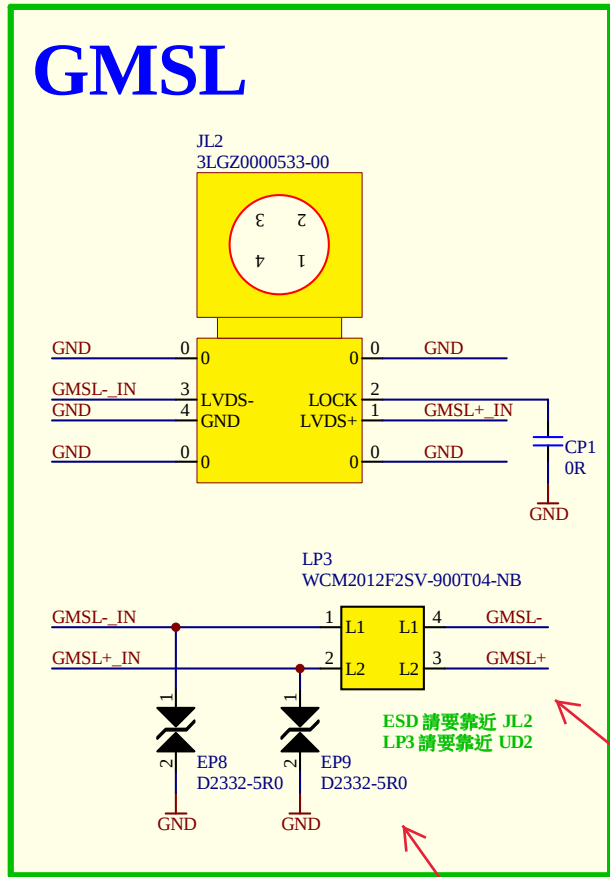
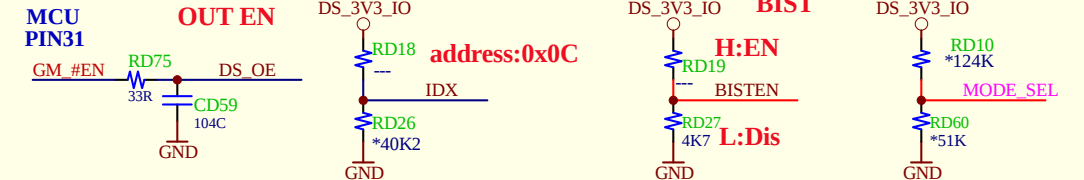
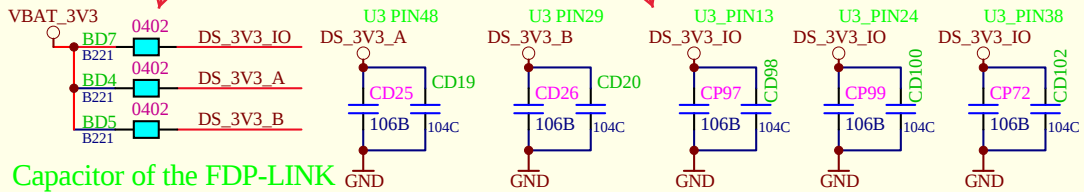




Can't see spec of FBs. Ensure DC and high frequency impedance meet specs of datasheet figure 24

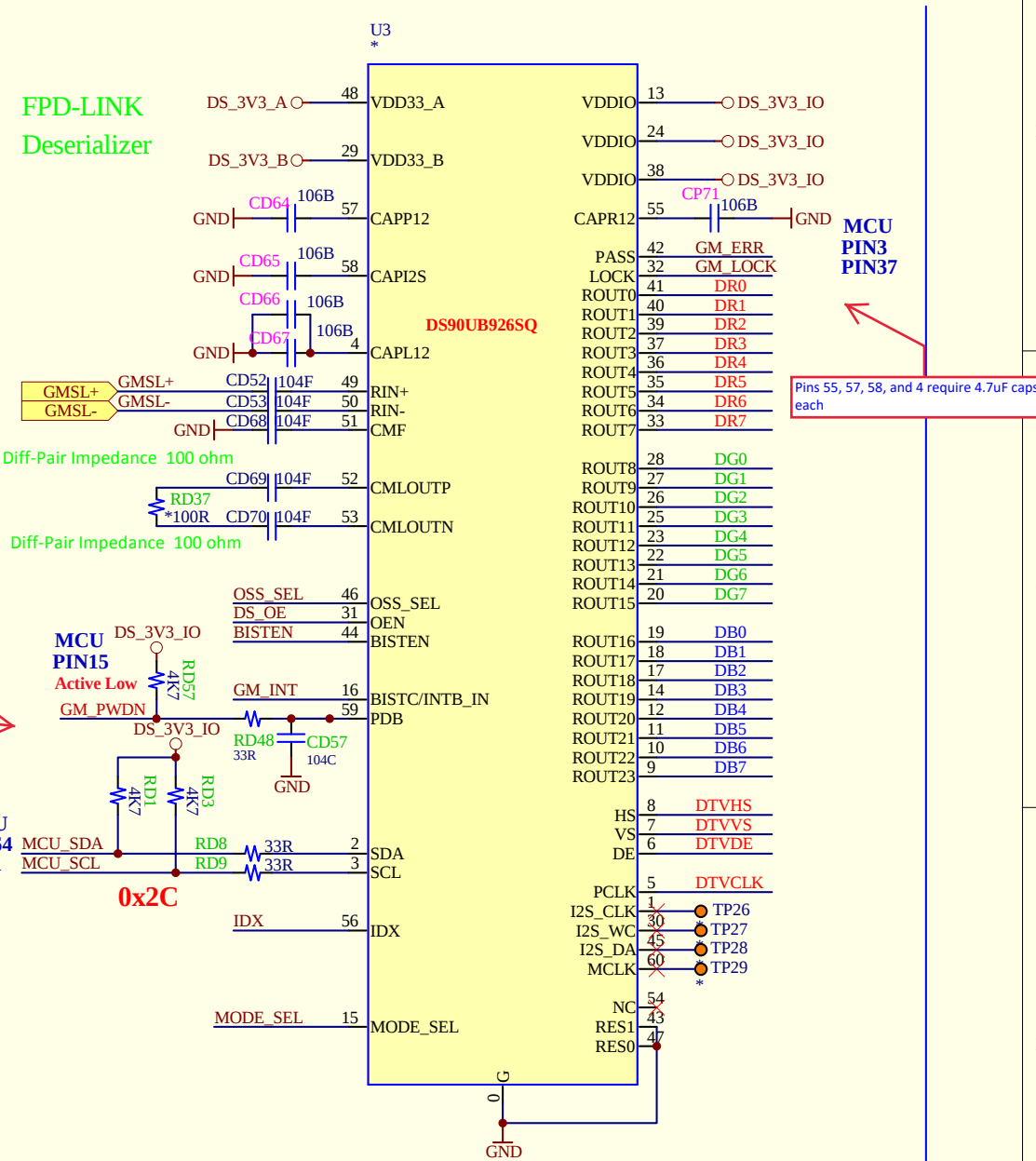
Pins 48, 29, 13, 24, 38 each require 4.7uF at each pin



0x0C is not a strappable address for 926. See datasheet table 10 for strap address options and recommended resistors

CD57 should be >=1uF to ensure PDB delay

FPD-LINK Deserializer



Can't find datasheet for ESD diodes. Make sure these are ultra low capacitance and small package to help with signal integrity. Similar spec to TPD1E01B04-Q1 would be ideal

Can't find differential insertion loss specs for this CMC. Suggest similar spec to DLW21S2900HQ2L to avoid insertion loss impact

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