

## DisplayPort 1.4 Test Report

**Overall result: Fail**

DUT: SA8155  
Comment: DSIO\_Lane 0  
Time of session start: 12/24/2019 04:15:21  
Operator: Albert  
Temperature: 25° C  
Standard in use: DisplayPort 1.4

Session ID: 85, Continuation #: 1:

Time of run: 2019/12/24 04:15:23  
Configuration in use: HBR2/HBR/RBR Tests, SSC Disabled (SA8155)  
Limits in use: Default  
Oscilloscope Name: LCRY0858N75425 Model: WM816ZI-B  
Oscilloscope Serial #: LCRY0858N75425  
Computer: LCRY0858N75425  
Oscilloscope firmware version: 8.9.0.3 (Build 263624)  
QualiPHY core version: 8.9.0.1 (Build 1000001)

QualiPHY script version: 8.9.0.1  
Stylesheet version: 1.2.0.7


# Summary Table

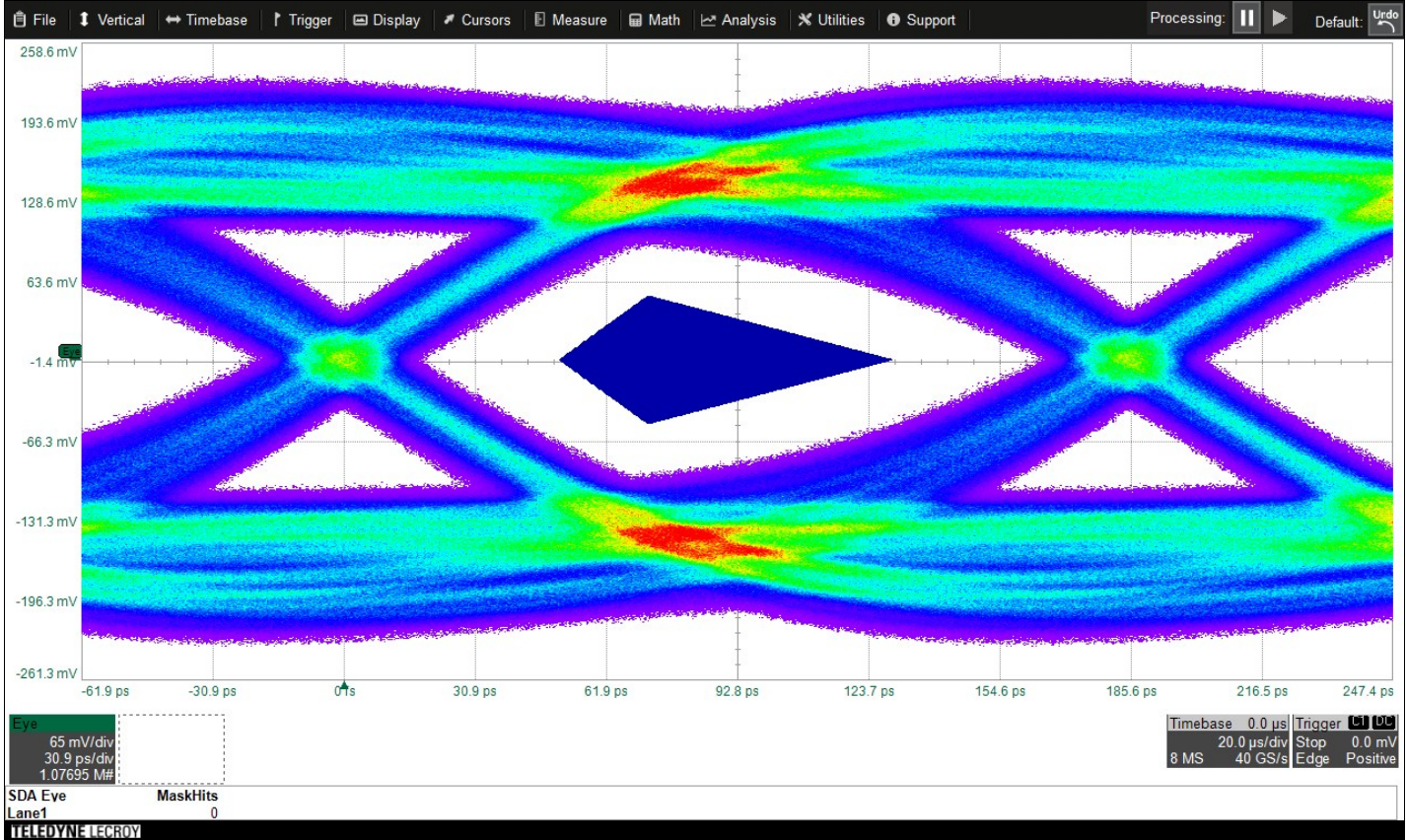
[Hide Table]

Pass#	Test	Measurement	Lane	Speed	SSC	Nom Output Level	Nom Preemp	Current Value	Test Criteria
✓	1 3.1	<a href="#">TP3_EQ Eye Diagram Testing, Worst Case Cable</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	0 hits	x = 0 hits
✓	1 3.11.1	<a href="#">HBR2 CPAT Worst Case Cable Total Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	267.1 mUI	x < 580.0 mUI
✓	1 3.11.1	<a href="#">HBR2 CPAT Worst Case Cable Deterministic Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	219.9 mUI	x < 490.0 mUI
✓	1 3.1	<a href="#">TP3_EQ, Zero Length Eye Mask Hits</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	0 hits	x = 0 hits
✓	1 3.11.1	<a href="#">HBR2 CPAT Zero Length Total Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	166.5 mUI	x < 580.0 mUI
✓	1 3.11.1	<a href="#">HBR2 CPAT Zero Length Deterministic Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	111.3 mUI	x < 490.0 mUI
✗	1 3.1	<a href="#">Eye Mask Hits</a>	Lane0	2.7Gb/s	Disabled	800mV	0.0dB	257.540 khits	x = 0 hits
✓	1 3.9	<a href="#">Non-ISI Jitter (UI)</a>	Lane0	2.7Gb/s	Disabled	800mV	0.0dB	44.3 mUI	x < 276.0 mUI
✓	1 3.11.1	<a href="#">Total Jitter (UI)</a>	Lane0	2.7Gb/s	Disabled	800mV	0.0dB	75.5 mUI	x < 420.0 mUI
i	1 3.2	<a href="#">Non-Preemp Pk-Pk, (400mV)</a>	Lane0	2.7Gb/s	Disabled	400mV	0.0dB	0.0mV	Informational Only
i	1 3.2	<a href="#">Non-Preemp Pk-Pk, (600mV)</a>	Lane0	2.7Gb/s	Disabled	600mV	0.0dB	0.0mV	Informational Only
✗	1 3.2	<a href="#">Non Pre-Emp Voltage, Measure 1 600mV to 400mV ratio</a>	Lane0	2.7Gb/s	Disabled	-	0.0dB	Invalid result: Overflow	800.0 mdB <= x <= 6.0000 dB
i	1 3.2	<a href="#">Non-Preemp Pk-Pk, (800mV)</a>	Lane0	2.7Gb/s	Disabled	800mV	0.0dB	0.0mV	Informational Only
✗	1 3.2	<a href="#">Non Pre-Emp Voltage Measure 2 800mV to 600mV ratio</a>	Lane0	2.7Gb/s	Disabled	-	0.0dB	Invalid Result: Overflow	100.0 mdB <= x <= 5.1000 dB
✗	1 3.3	<a href="#">PreEmp, 0dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	0.0dB	2.412 dB	x <= 250 mdB
✗	1 3.3	<a href="#">PreEmp, 0dB 600mV</a>	Lane0	2.7Gb/s	Disabled	600mV	0.0dB	2.412 dB	x <= 250 mdB
✗	1 3.3	<a href="#">PreEmp, 0dB 800mV</a>	Lane0	2.7Gb/s	Disabled	800mV	0.0dB	2.412 dB	x <= 250 mdB
✗	1 3.3	<a href="#">PreEmp, 3.5 dB - 0 dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	3.5dB	0 mdB	x >= 2.000 dB
✗	1 3.3	<a href="#">PreEmp, 3.5 dB - 0 dB 600mV</a>	Lane0	2.7Gb/s	Disabled	600mV	3.5dB	0 mdB	x >= 2.000 dB
i	1 3.3	<a href="#">PreEmp, 6.0 dB - 0 dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	6.0dB	0.0dB	Informational Only
✗	1 3.3	<a href="#">PreEmp, 6.0 dB - 3.5 dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	-	0 mdB	x >= 1.600 dB
i	1 3.3	<a href="#">PreEmp, 6.0 dB - 0 dB 600mV</a>	Lane0	2.7Gb/s	Disabled	600mV	6.0dB	0.0dB	Informational Only
✗	1 3.3	<a href="#">PreEmp, 6.0 dB - 3.5 dB 600mV</a>	Lane0	2.7Gb/s	Disabled	600mV	-	0 mdB	x >= 1.600 dB
i	1 3.3	<a href="#">PreEmp, 9.5 dB - 0 dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	9.5dB	0.0dB	Informational Only
✗	1 3.3	<a href="#">PreEmp, 9.5 dB - 6.0 dB 400mV</a>	Lane0	2.7Gb/s	Disabled	400mV	-	0 mdB	x >= 1.600 dB
✓	1 3.3	<a href="#">Non-Trans VRange (400mV)</a>	Lane0	2.7Gb/s	Disabled	400mV	-	1.0000	x > 850.0 m
✓	1 3.3	<a href="#">Non-Trans VRange (600mV)</a>	Lane0	2.7Gb/s	Disabled	600mV	-	1.0000	x > 708.0 m
✓	1 3.3	<a href="#">Vpp 400mV 0.0 dB</a>	Lane0	2.7Gb/s	Disabled	400mV	0.0dB	479 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 400mV 3.5 dB</a>	Lane0	2.7Gb/s	Disabled	400mV	3.5dB	479 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 400mV 6.0 dB</a>	Lane0	2.7Gb/s	Disabled	400mV	6.0dB	479 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 400mV 9.5 dB</a>	Lane0	2.7Gb/s	Disabled	400mV	9.5dB	479 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 600mV 0.0 dB</a>	Lane0	2.7Gb/s	Disabled	600mV	0.0dB	477 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 600mV 3.5 dB</a>	Lane0	2.7Gb/s	Disabled	600mV	3.5dB	479 mV	x < 1.380 V
✓	1 3.3	<a href="#">Vpp 600mV 6.0 dB</a>	Lane0	2.7Gb/s	Disabled	600mV	6.0dB	479 mV	x < 1.380 V
✓	1 3.11.3	<a href="#">D10.2 Worst case cable Total Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	268.2 mUI	x <= 400.0 mUI
✓	1 3.11.3	<a href="#">D10.2 Worst case cable Deterministic Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	219.9 mUI	x <= 250.0 mUI
✓	1 3.11.3	<a href="#">D10.2 Worst case cable Random Jitter (UI)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	47.4 mUI	x <= 230.0 mUI
✓	1 3.12	<a href="#">Main Link Frequency Min (ppm)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	-669.6 ppm	x >= -5.3000 kppm
✓	1 3.12	<a href="#">Main Link Frequency Max (ppm)</a>	Lane0	5.4Gb/s	Disabled	800mV	0.0dB	247.8 ppm	x <= 300.0 ppm


# Details


[\[Up\]](#)

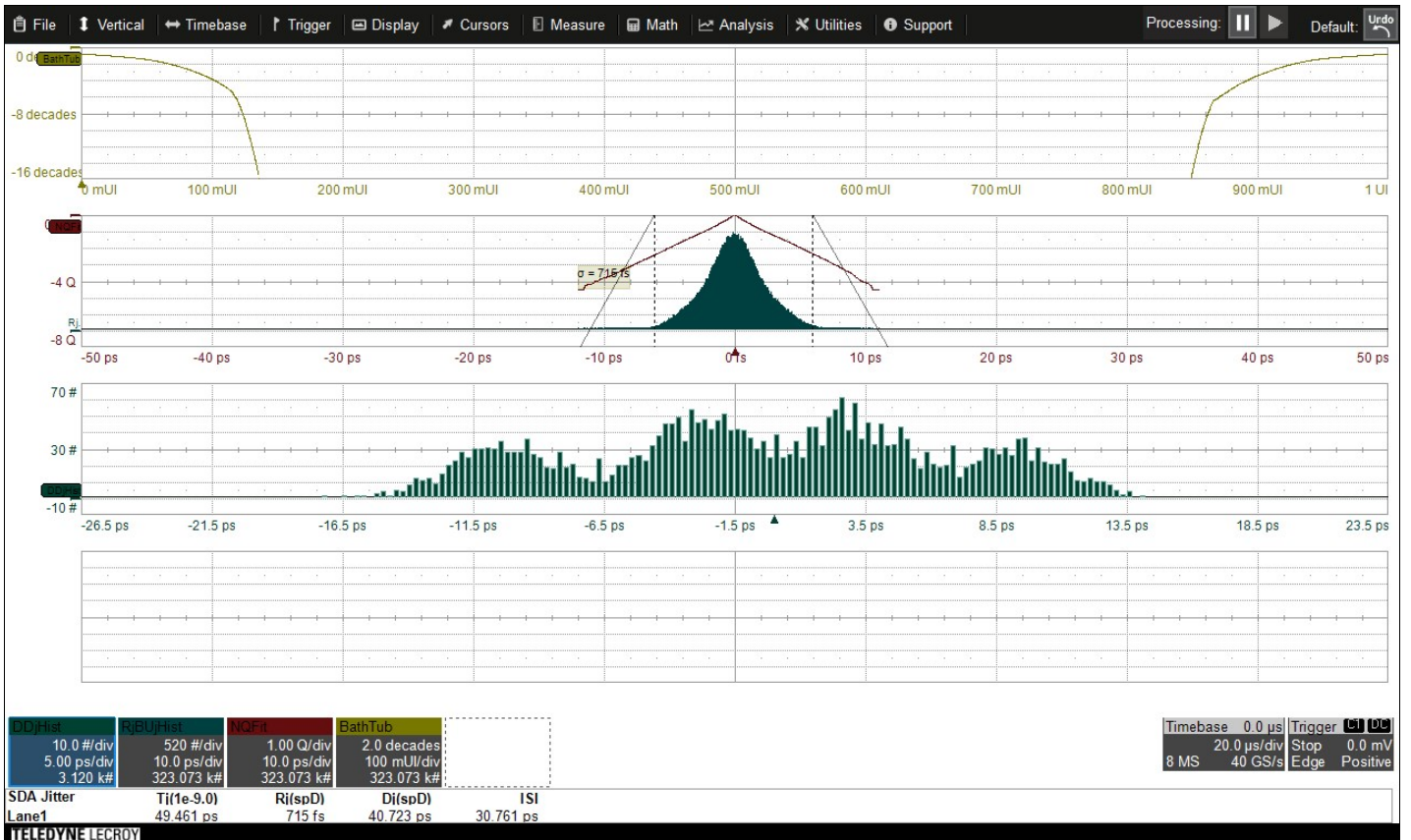
 <b>Pass</b>	Measurement: <b>TP3_EQ Eye Diagram Testing, Worst Case Cable</b>	
	Current Value: 0 hits	Test Criteria: x = 0 hits
	Timestamp: 12/24/2019 04:17:38	Limit Name: EyeMaskHits
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	



**Test 3.1 - TP3\_EQ Eye Diagram, Worst Case Cable Lane0 SSC Disabled 5.4 Gb/s**  
Timestamp: 12/24/2019 04:17:38


 <b>Pass</b>	Measurement: <b>HBR2 CPAT Worst Case Cable Total Jitter (UI)</b>	
	Current Value: 267.1 mUI	Test Criteria: $x < 580.0$ mUI
	Timestamp: 12/24/2019 04:18:35	Limit Name: TJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	

 <b>Pass</b>	Measurement: <b>HBR2 CPAT Worst Case Cable Deterministic Jitter (UI)</b>	
	Current Value: 219.9 mUI	Test Criteria: $x < 490.0$ mUI
	Timestamp: 12/24/2019 04:18:35	Limit Name: DJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	




**Tests 3.11.1 HBR2 CPAT Tj/Rj Worst Case Cable Lane0 SSC Disabled 5.4Gb/s , Output Level 2, Preemphasis Level 0**  
 Timestamp: 12/24/2019 04:18:36




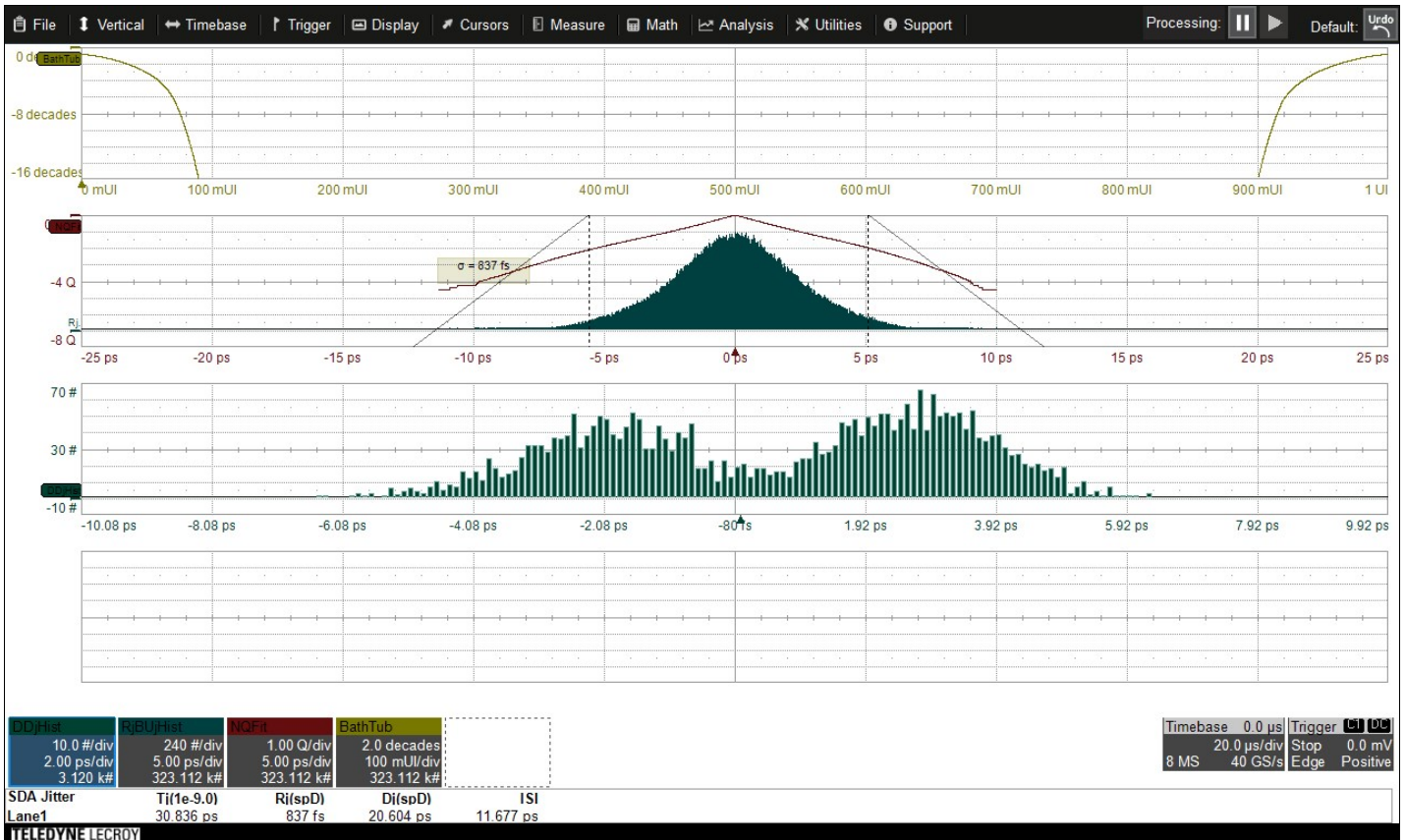
 <b>Pass</b>	Measurement: <b>TP3_EQ, Zero Length Eye Mask Hits</b>	
	Current Value: 0 hits	Test Criteria: x = 0 hits
	Timestamp: 12/24/2019 04:19:29	Limit Name: EyeMaskHits
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	



**Test 3.1 - TP3\_EQ Eye Diagram, Zero Length Lane0 SSC Disabled 5.4 Gb/s**  
Timestamp: 12/24/2019 04:19:29

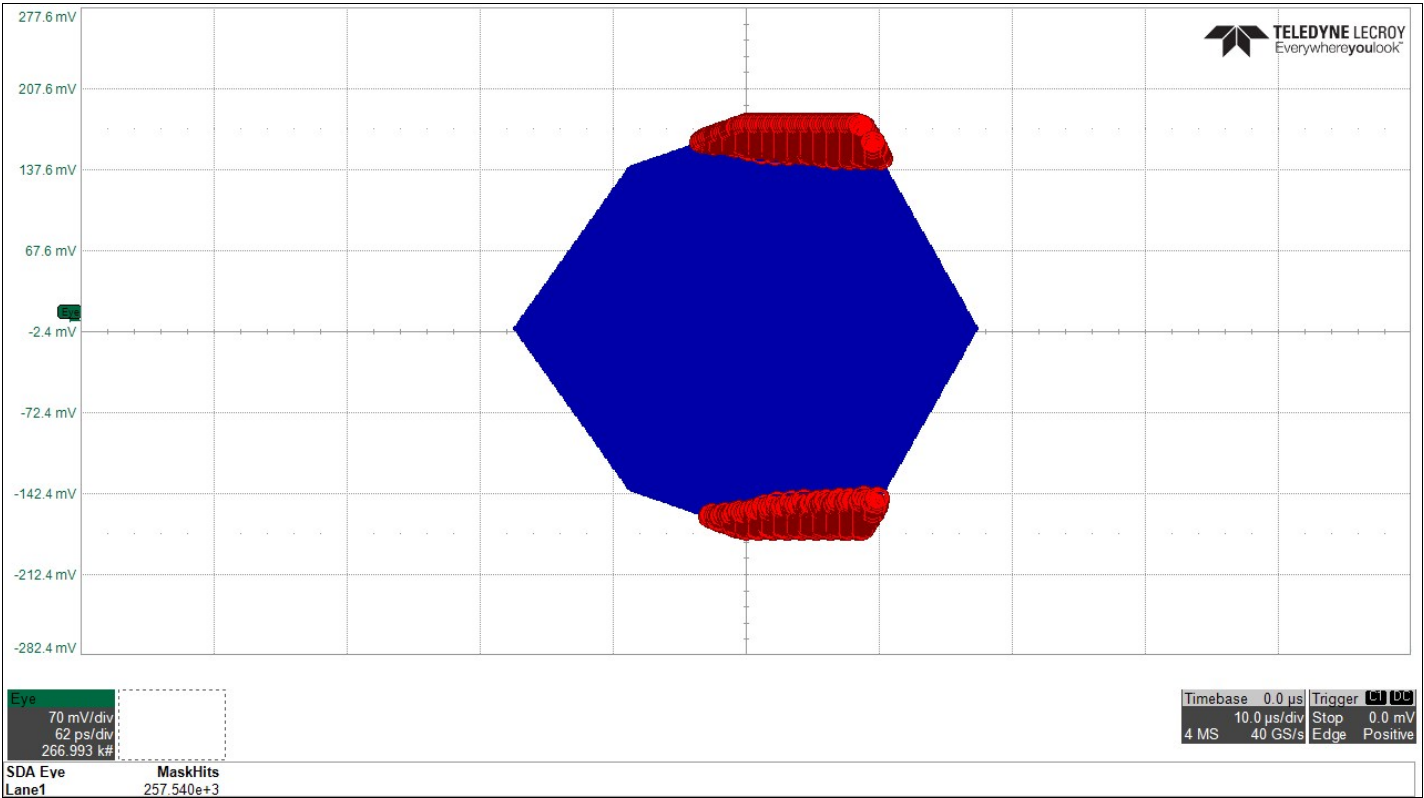
 Pass	Measurement: <b>HBR2 CPAT Zero Length Total Jitter (UI)</b>	
	Current Value: 166.5 mUI	Test Criteria: $x < 580.0$ mUI
	Timestamp: 12/24/2019 04:20:25	Limit Name: TJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	

 Pass	Measurement: <b>HBR2 CPAT Zero Length Deterministic Jitter (UI)</b>	
	Current Value: 111.3 mUI	Test Criteria: $x < 490.0$ mUI
	Timestamp: 12/24/2019 04:20:25	Limit Name: DJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	




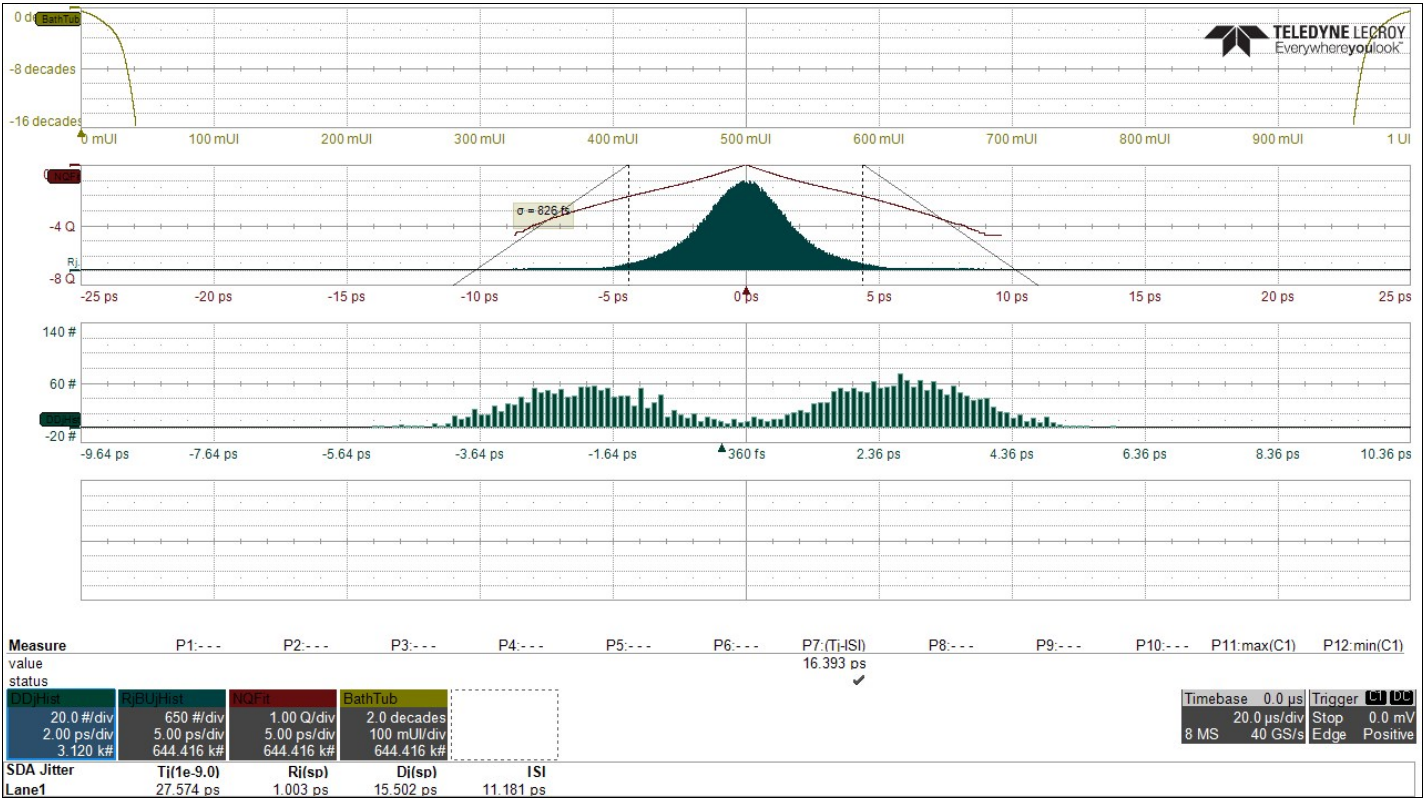
Tests 3.11.1 HBR2 CPAT Tj/Rj (Zero Length) Lane0 SSC Disabled 5.4Gb/s , Output Level 2, Preemphasis Level 0  
 Timestamp: 12/24/2019 04:20:25

<b>X</b> <b>Fail</b>	Measurement: <b>Eye Mask Hits</b>	
	Current Value: 257.540 khits	Test Criteria: x = 0 hits
	Timestamp: 12/24/2019 04:33:28	Limit Name: EyeMaskHits
	Configuration Lane0 : 800mV : 0.0dB : 2.7Gb/s : SSCDisabled	




**Test 3.1 - TP2 Eye Diagram Lane0 SSC Disabled 2.7 Gb/s**  
Timestamp: 12/24/2019 04:33:36

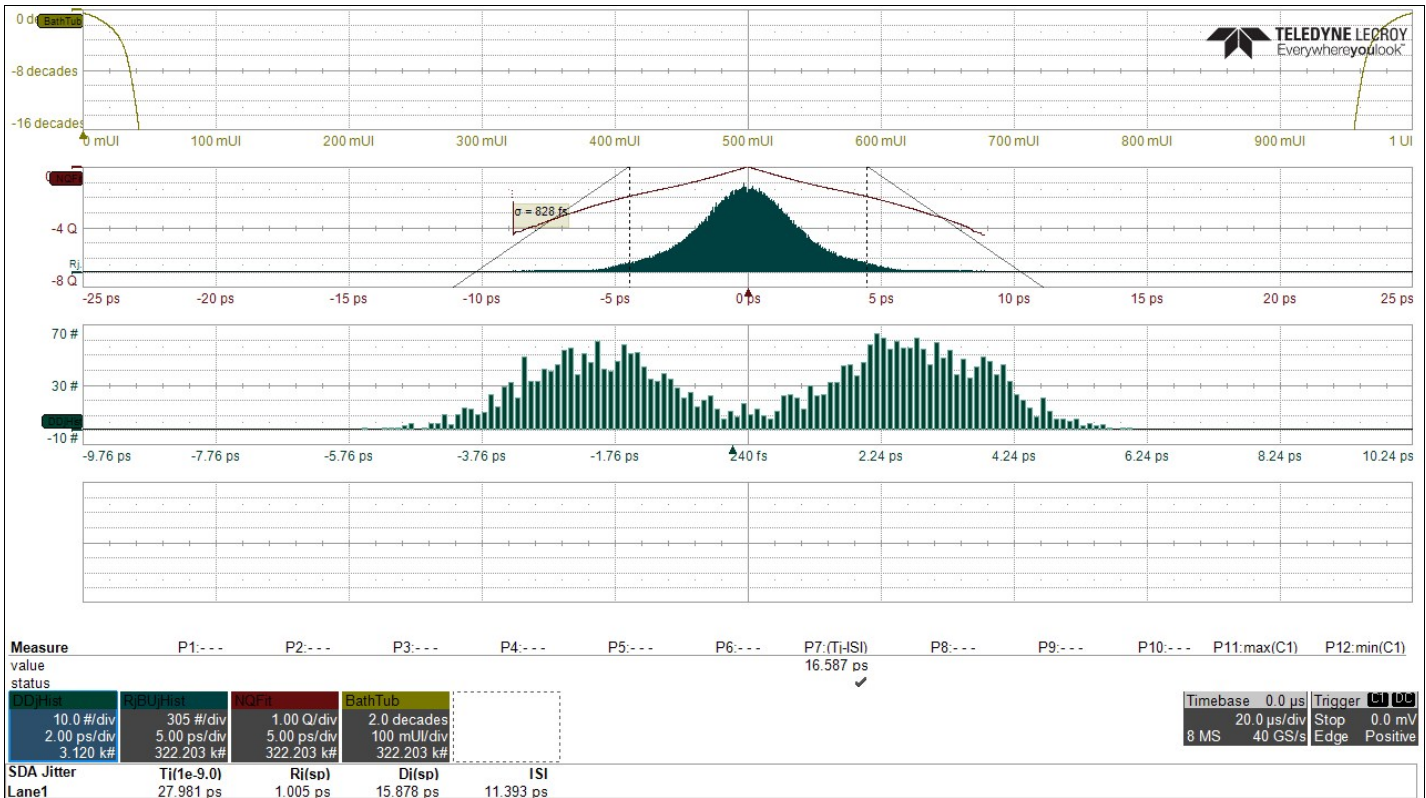
 <b>Pass</b>	Measurement: <b>Non-ISI Jitter (UI)</b>	
	Current Value: 44.3 mUI	Test Criteria: $x < 276.0$ mUI
	Timestamp: 12/24/2019 04:34:14	Limit Name: NonISI_HBR
	Configuration Lane0 : 800mV : 0.0dB : 2.7Gb/s : SSCDisabled	



**Test 3.9 Lane0 2.7Gb/s Non ISI, Output Level 2, Preemphasis Level 0**  
Timestamp: 12/24/2019 04:34:15




 <b>Pass</b>	Measurement: <b>Total Jitter (UI)</b>	
	Current Value: 75.5 mUI	Test Criteria: $x < 420.0$ mUI
	Timestamp: 12/24/2019 04:34:46	Limit Name: TJ_HBR
	Configuration Lane0 : 800mV : 0.0dB : 2.7Gb/s : SSCDisabled	




**Test 3.11.1 Lane0 2.7Gb/s Total Jitter, Output Level 2, Preemphasis Level 0**  
Timestamp: 12/24/2019 04:34:47


[\[Up\]](#)

	Measurement: <b>Non-Preemp Pk-Pk, (400mV)</b>	
	Current Value: 0.0mV	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:35:57	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Non-Preemp Pk-Pk, (600mV)</b>	
	Current Value: 0.0mV	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:36:44	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Non Pre-Emp Voltage, Measure 1 600mV to 400mV ratio</b>	
	Current Value: Invalid result: Overflow	Test Criteria: 800.0 mdB <= x <= 6.0000 dB
	Timestamp: 12/24/2019 04:36:44	Limit Name: NonPreEmphPkPk1
	Configuration Lane0 : - : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Non-Preemp Pk-Pk, (800mV)</b>	
	Current Value: 0.0mV	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:37:33	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Non Pre-EmpVoltage Measure 2 800mV to 600mV ratio</b>	
	Current Value: Invalid Result: Overflow	Test Criteria: 100.0 mdB <= x <= 5.1000 dB
	Timestamp: 12/24/2019 04:37:33	Limit Name: NonPreEmphPkPk2
	Configuration Lane0 : - : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 0dB 400mV</b>	
	Current Value: 2.412 dB	Test Criteria: x <= 250 mdB
	Timestamp: 12/24/2019 04:24:20	Limit Name: PreEmph0
	Configuration Lane0 : 400mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 0dB 600mV</b>	
	Current Value: 2.412 dB	Test Criteria: x <= 250 mdB
	Timestamp: 12/24/2019 04:28:59	Limit Name: PreEmph0
	Configuration Lane0 : 600mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 0dB 800mV</b>	
	Current Value: 2.412 dB	Test Criteria: x <= 250 mdB
	Timestamp: 12/24/2019 04:38:20	Limit Name: PreEmph0
	Configuration Lane0 : 800mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 3.5 dB - 0 dB 400mV</b>	
	Current Value: 0 mdB	Test Criteria: x >= 2.000 dB
	Timestamp: 12/24/2019 04:25:19	Limit Name: PreEmph3_5
	Configuration Lane0 : 400mV : 3.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 3.5 dB - 0 dB 600mV</b>	
	Current Value: 0 mdB	Test Criteria: x >= 2.000 dB
	Timestamp: 12/24/2019 04:30:14	Limit Name: PreEmph3_5
	Configuration Lane0 : 600mV : 3.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 6.0 dB - 0 dB 400mV</b>	
	Current Value: 0.0dB	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:26:18	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 6.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 6.0 dB - 3.5 dB 400mV</b>	
	Current Value: 0 mdB	Test Criteria: x >= 1.600 dB
	Timestamp: 12/24/2019 04:26:18	Limit Name: PreEmph6_0
	Configuration Lane0 : 400mV : - : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 6.0 dB - 0 dB 600mV</b>	
	Current Value: 0.0dB	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:32:12	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 6.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 6.0 dB - 3.5 dB 600mV</b>	
	Current Value: 0 mdB	Test Criteria: x >= 1.600 dB
	Timestamp: 12/24/2019 04:32:12	Limit Name: PreEmph6_0
	Configuration Lane0 : 600mV : - : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 9.5 dB - 0 dB 400mV</b>	
	Current Value: 0.0dB	Test Criteria: Informational Only
	Timestamp: 12/24/2019 04:27:35	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 9.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>PreEmp, 9.5 dB - 6.0 dB 400mV</b>	
	Current Value: 0 mdB	Test Criteria: x >= 1.600 dB
	Timestamp: 12/24/2019 04:27:35	Limit Name: PreEmph9_5
	Configuration Lane0 : 400mV : - : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Non-Trans VRange (400mV)</b>	
	Current Value: 1.0000	Test Criteria: x > 850.0 m
	Timestamp: 12/24/2019 04:27:35	Limit Name: NonTransRange400
	Configuration Lane0 : 400mV : - : 2.7Gb/s : SSCDisabled	

[\[Up\]](#)


	Measurement: <b>Non-Trans VRange (600mV)</b>	
	Current Value: 1.0000	Test Criteria: x > 708.0 m
	Timestamp: 12/24/2019 04:32:12	Limit Name: NonTransRange600
	Configuration Lane0 : 600mV : - : 2.7Gb/s : SSCDisabled	

[\[Up\]](#)


	Measurement: <b>Vpp 400mV 0.0 dB</b>	
	Current Value: 479 mV	Test Criteria: x < 1.380 V
	Timestamp: 12/24/2019 04:27:35	Limit Name: VppMax

Configuration Lane0 : 400mV : 0.0dB : 2.7Gb/s : SSCDisabled


[\[Up\]](#)

	Measurement: <b>Vpp 400mV 3.5 dB</b>	
	Current Value: 479 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:27:35	Limit Name: VppMax
	Configuration Lane0 : 400mV : 3.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Vpp 400mV 6.0 dB</b>	
	Current Value: 479 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:27:36	Limit Name: VppMax
	Configuration Lane0 : 400mV : 6.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Vpp 400mV 9.5 dB</b>	
	Current Value: 479 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:27:36	Limit Name: VppMax
	Configuration Lane0 : 400mV : 9.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Vpp 600mV 0.0 dB</b>	
	Current Value: 477 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:32:12	Limit Name: VppMax
	Configuration Lane0 : 600mV : 0.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Vpp 600mV 3.5 dB</b>	
	Current Value: 479 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:32:13	Limit Name: VppMax
	Configuration Lane0 : 600mV : 3.5dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

	Measurement: <b>Vpp 600mV 6.0 dB</b>	
	Current Value: 479 mV	Test Criteria: $x < 1.380 \text{ V}$
	Timestamp: 12/24/2019 04:32:13	Limit Name: VppMax
	Configuration Lane0 : 600mV : 6.0dB : 2.7Gb/s : SSCDisabled	


[\[Up\]](#)

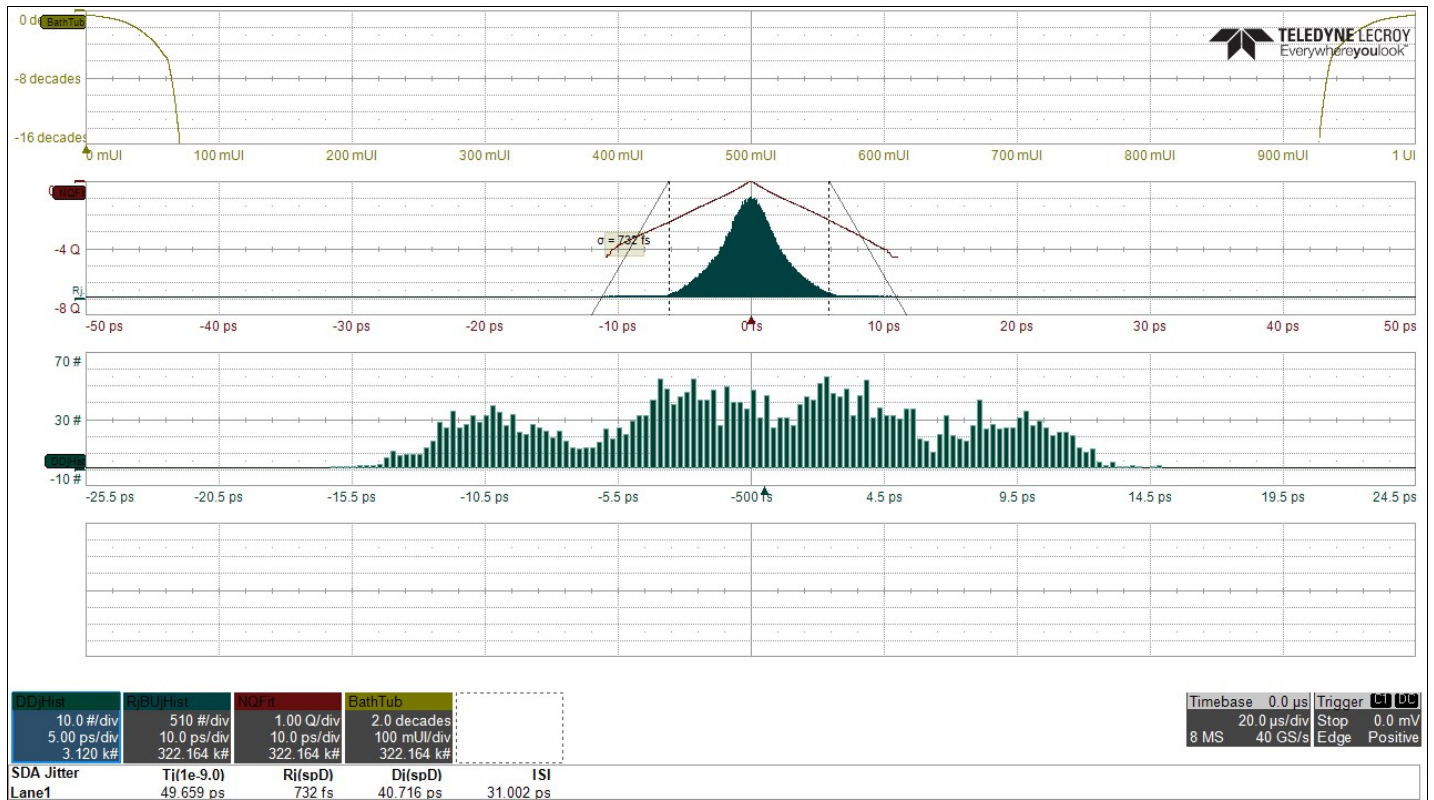
	Measurement: <b>D10.2 Worst case cable Total Jitter (UI)</b>	
	Current Value: 268.2 mUI	Test Criteria: $x \leq 400.0 \text{ mUI}$
	Timestamp: 12/24/2019 04:21:45	Limit Name: TJ_D10_2
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	

[\[Up\]](#)

	Measurement: <b>D10.2 Worst case cable Deterministic Jitter (UI)</b>	
	Current Value: 219.9 mUI	Test Criteria: $x \leq 250.0 \text{ mUI}$
	Timestamp: 12/24/2019 04:21:45	Limit Name: DJ_D10_2
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	

[\[Up\]](#)


	Measurement: <b>D10.2 Worst case cable Random Jitter (UI)</b>	
	Current Value: 47.4 mUI	Test Criteria: $x \leq 230.0 \text{ mUI}$
	Timestamp: 12/24/2019 04:21:45	Limit Name: RJ_D10_2
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	




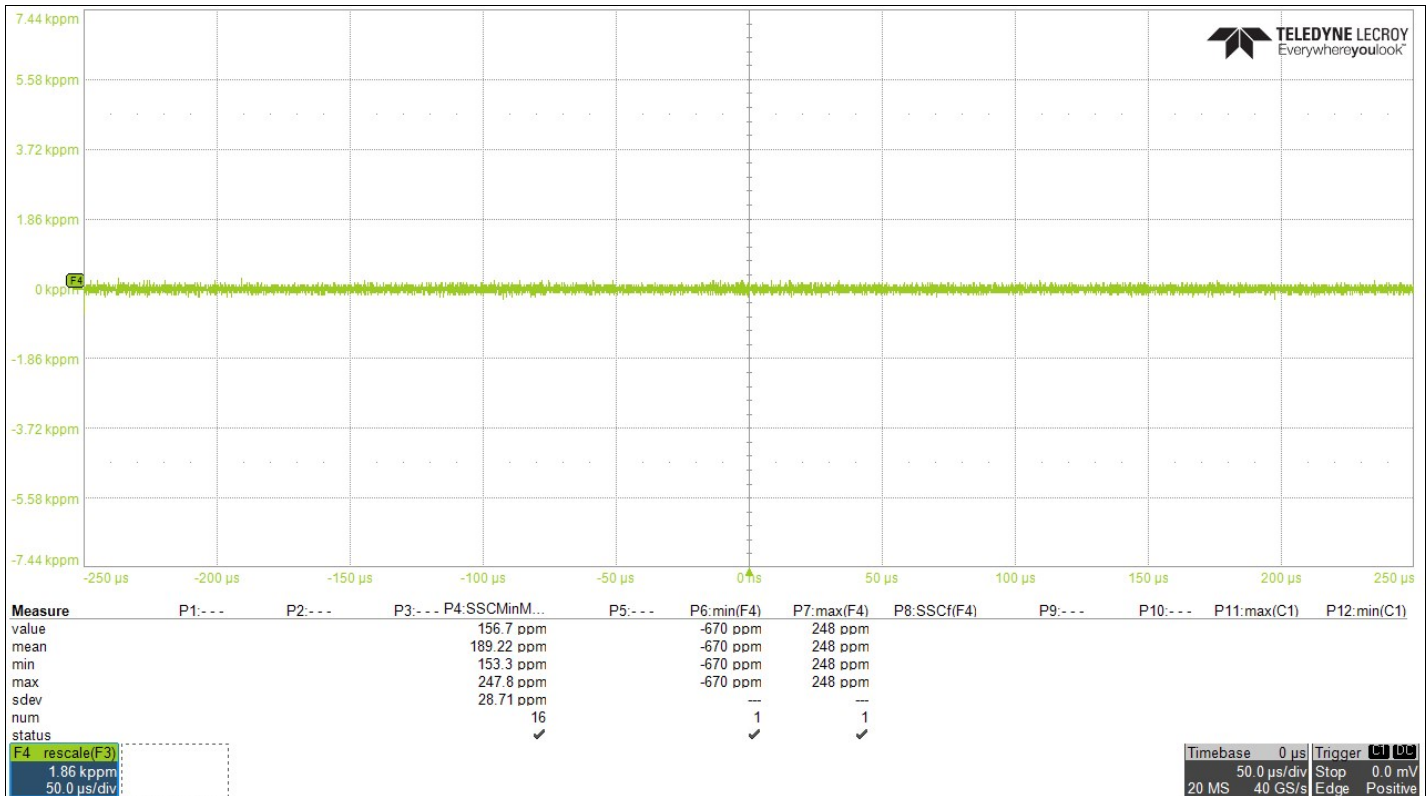
**Tests 3.11.3 D10.2 CPAT Tj/Rj/Dj (Worst Case Cable) Lane0 SSC Disabled 5.4Gb/s , Output Level 2, Preemphasis Level 0**

Timestamp: 12/24/2019 04:21:45



 Pass	Measurement: <b>Main Link Frequency Min (ppm)</b>	
	Current Value: -669.6 ppm	Test Criteria: $x \geq -5.3000$ kppm
	Timestamp: 12/24/2019 04:22:58	Limit Name: FreqPPMMin
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	

 Pass	Measurement: <b>Main Link Frequency Max (ppm)</b>	
	Current Value: 247.8 ppm	Test Criteria: $x \leq 300.0$ ppm
	Timestamp: 12/24/2019 04:22:58	Limit Name: FreqPPMMax
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCDisabled	



**Tests 3.13 - 3.15 Spread Spectrum Lane0 5.4Gb/s**  
Timestamp: 12/24/2019 04:22:58

--- End of report ---