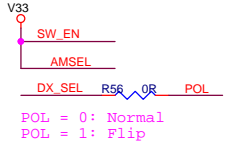


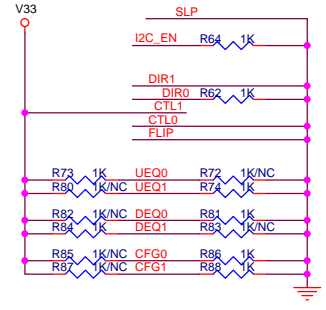
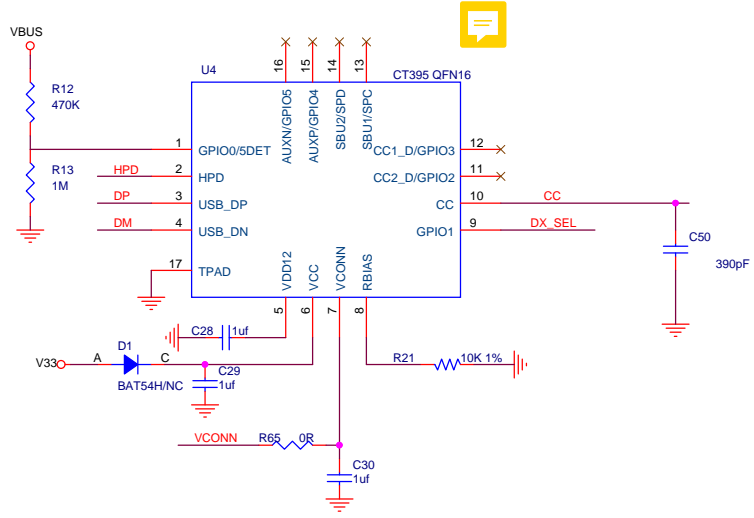
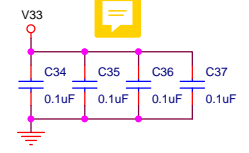
$V_{out} = 0.6V * (1 + 10/2.2) = 3.327V$



POL = 0: Normal
POL = 1: Flip



SWAP = 0: Upstream -> Downstream
SWAP = 1: Downstream -> Upstream



1. Disable RX detect (USB Function)
2. GPIO Configure Mode
- 4 Lane DP - No Flip (Source side: DP on Upstream side)
4 Lane DP - No Flip (Sink side: DP on Downstream side)
4. Upstream EQ = 3dB@4.05G
5. Downstream EQ = 12dB@4.05G
6. Upstream = 0dB, Downstream = 1dB, VOD=900mVpp

Title	<Title>	
Size	Document Number	Rev
A3	<Doc>	<Rev Code>
Date:	Saturday, June 20, 2020	Sheet 3 of 3