



Description	Signal Mapping	
	TCP	DP
Reth Link (Tx)	TCP_TX0	DP Lane_0
	TCP_TX1	DP Lane_2
	TCP_TX0X0	DP Lane_1
	TCP_TX0X1	DP Lane_3

Note: Note: apply to TCP ports only

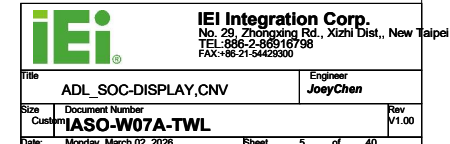


Description	Signal Mapping		
	DDI	HDMI (Default non-reversed)	HDMI (Reversed)
Main Link (Tx)	DDI_TX0	HDMI Data_2	HDMI CLK
	DDI_TX1	HDMI Data_1	HDMI Data_0
	DDI_TX2	HDMI Data_0	HDMI Data_1
	DDI_TX3	HDMI CLK	HDMI Data_2

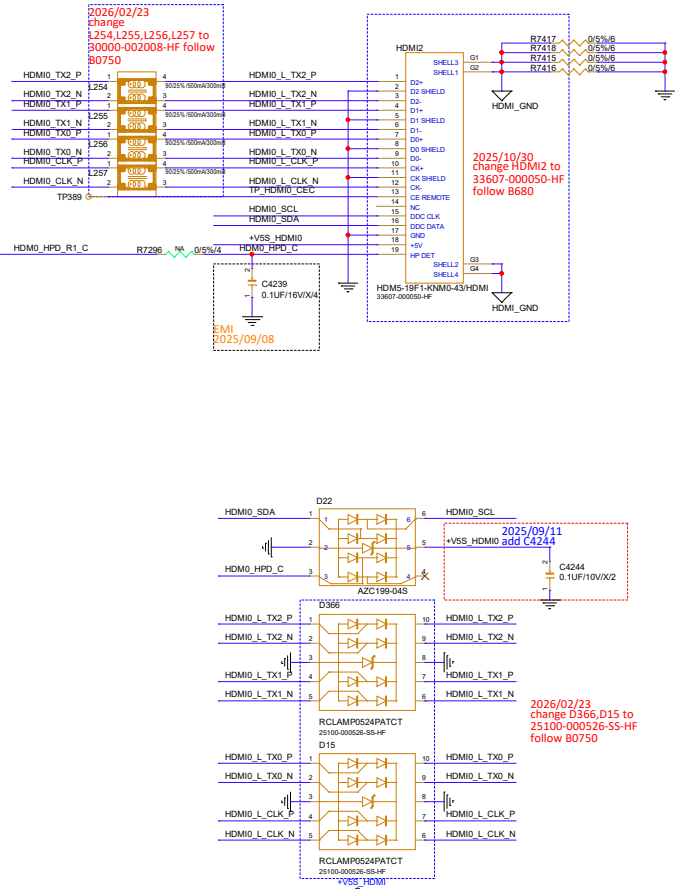
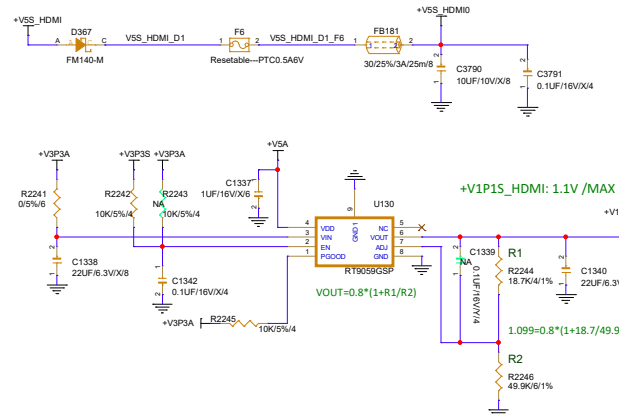
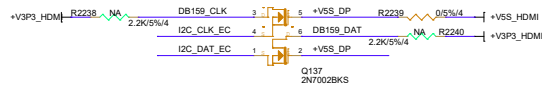
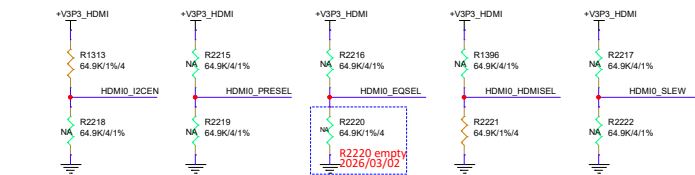
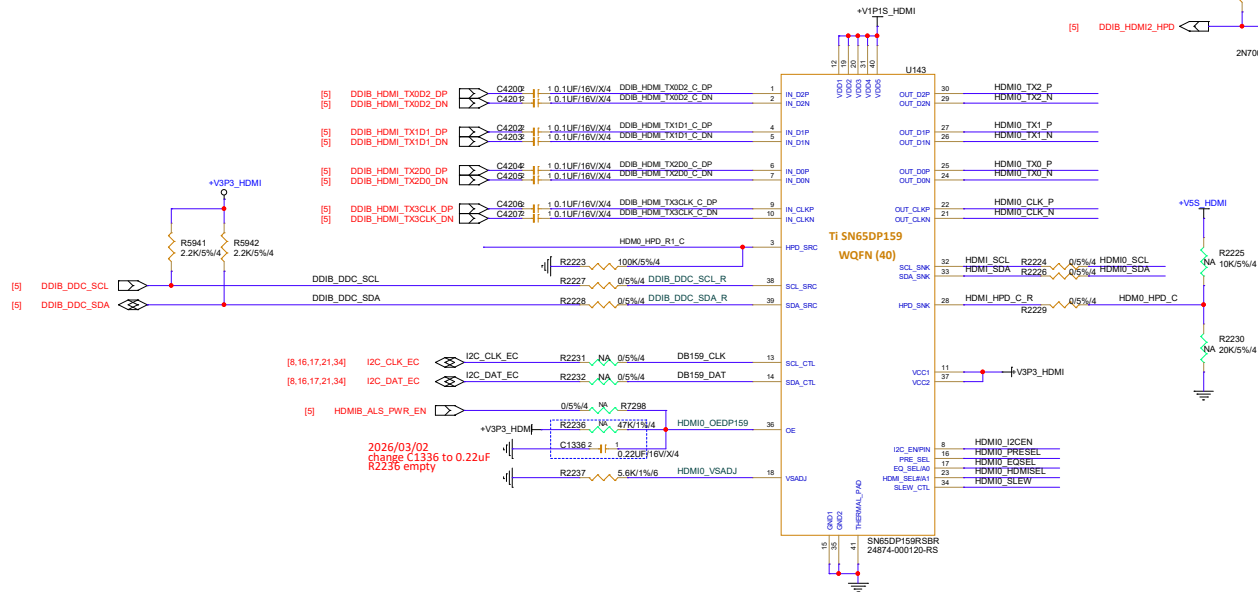
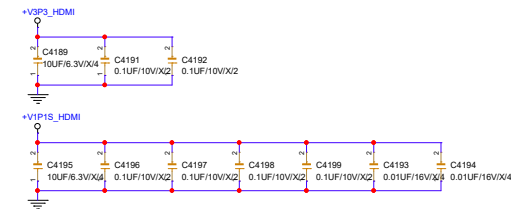
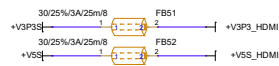
Note: Apply to DDI(A/B ports only

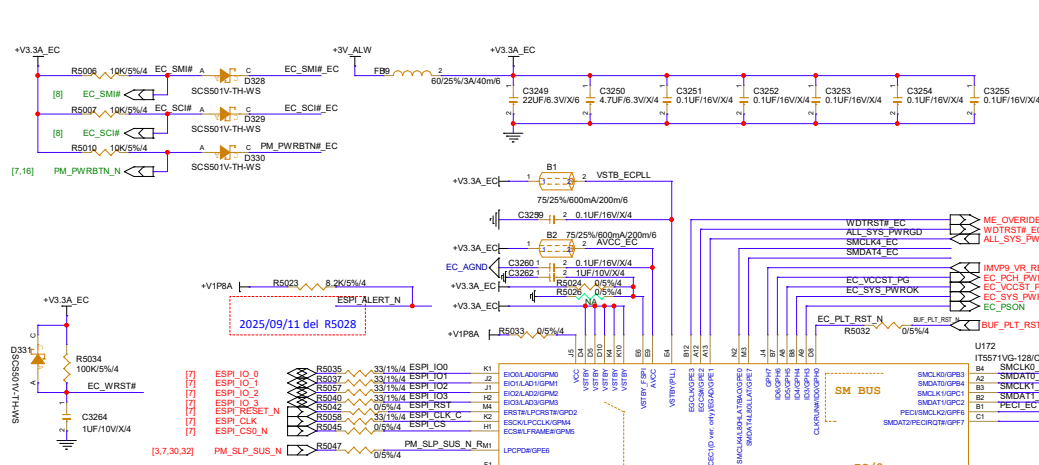
Note: For systems which implements reversed lane configuration it is required to set DDI Lane Reversal to enabled in VBT. DDI lane reversal option can be found under EFP1 configuration.

Note: Note: For systems which implements default signal configuration no additional configuration needed in VBT (default is disabled).



HDMI 2.0 ReTimer





IT5571
VFBGA

