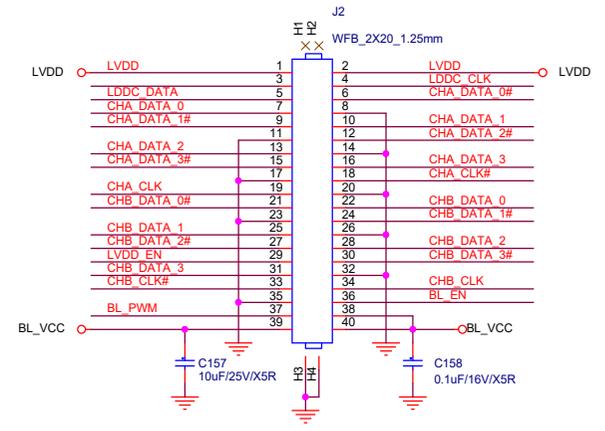
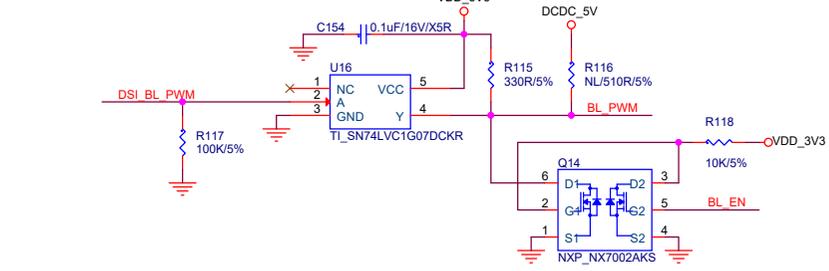
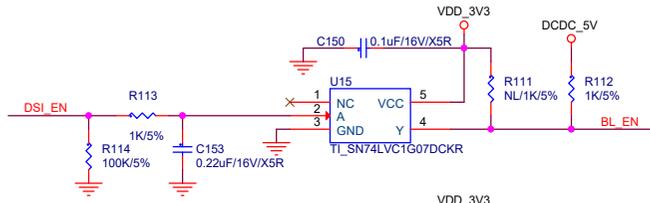
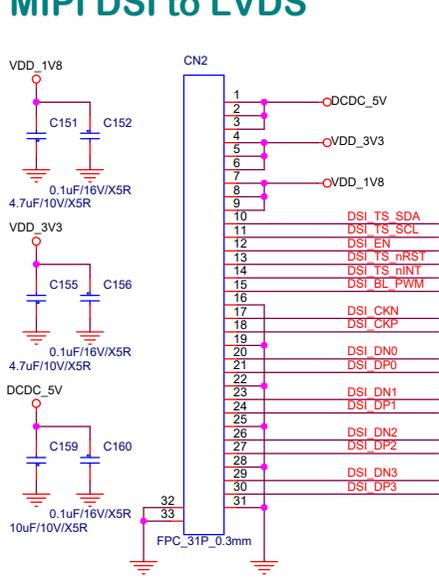
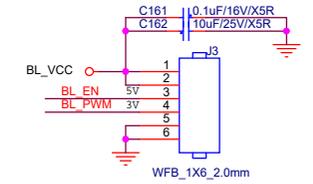


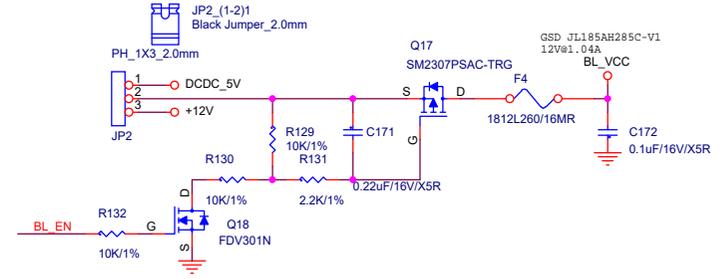
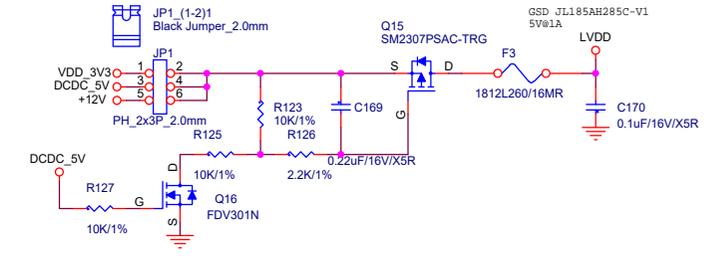
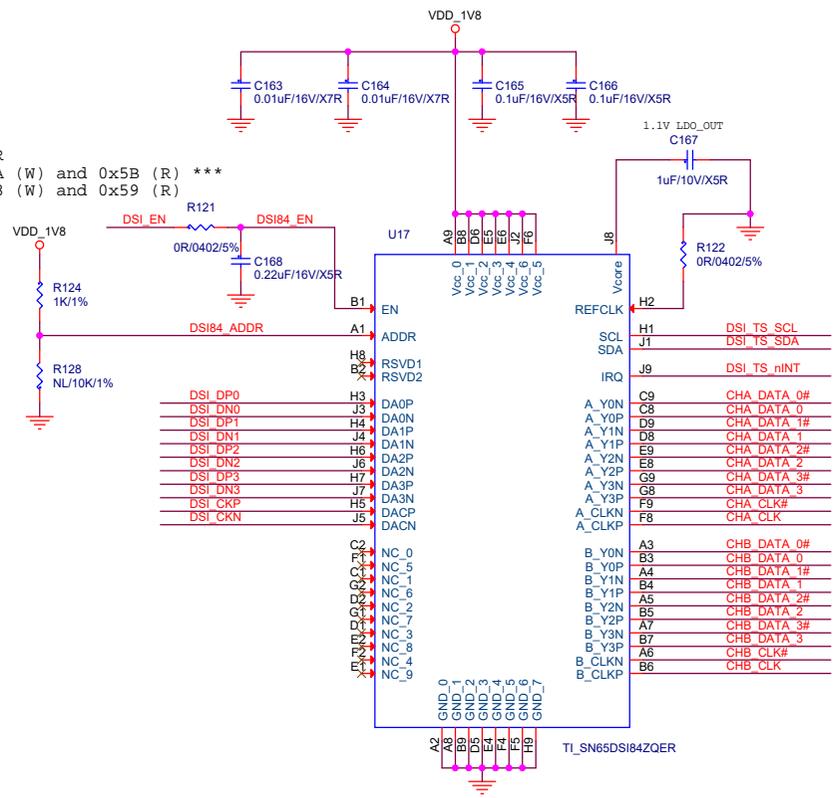
MIPI DSI to LVDS



8,15,16,18 I2C3_SCL <<< R119_NL/0R/0402/5% LDCC_CLK
 8,15,16,18 I2C3_SDA <<< R120_NL/0R/0402/5% LDCC_DATA



DSI I2C ADDR
 ADDR=1, 0x5A (W) and 0x5B (R) ***
 ADDR=0, 0x58 (W) and 0x59 (R)



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