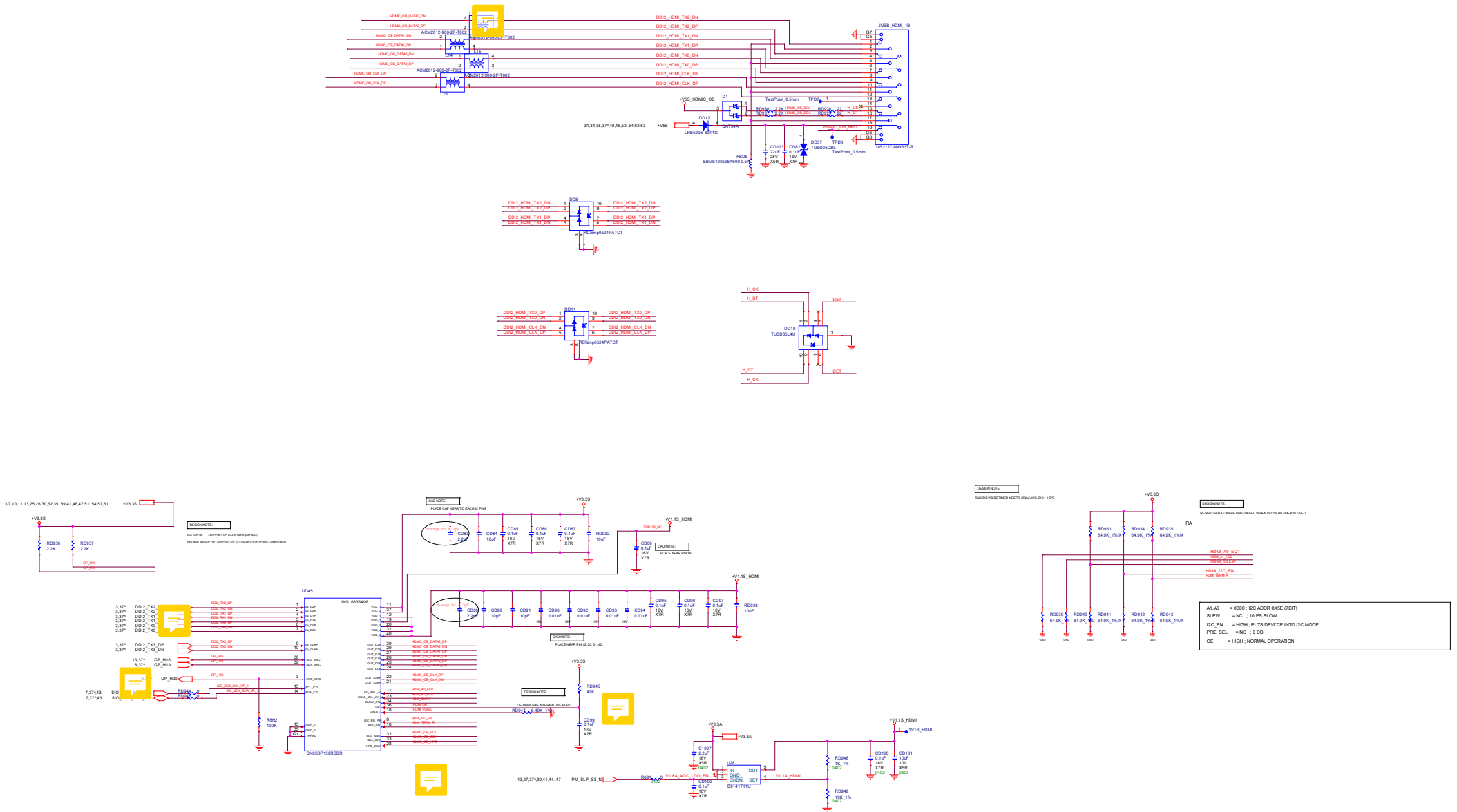
JUSB_HDMI_1B
1852127-0NY631-R
ACM2012-900-2P-T002
DDI2_HDMI_TX2_DN
DDI2_HDMI_TX2_DP
DDI2_HDMI_TX1_DN
DDI2_HDMI_TX1_DP
DDI2_HDMI_TX0_DN
DDI2_HDMI_TX0_DP
DDI2_HDMI_CLK_DN
DDI2_HDMI_CLK_DP
+VSS_HDMIC_OB
BAT54A
LRB520S-30T1G
EBMS100505A600 0.5
TUSD05CBL
TUSD05L4U
RClamp0524PATCT
SN65DP159RSBR
INS16835496
UD45
+V3.3S
+V1.1S_HDMI
+V3.3A
V1.8A_ADC_LDO_EN
PM_SLP_S3_N
V1.1A_HDMI
DESIGN NOTE:
CAD NOTE:
PLACE CAP NEAR TO EACH IC PINS
PLACE NEAR PIN 12, 22, 31, 40.
PLACE NEAR PIN 16.
CE PINS HAS INTERNAL WEAK PU
RESISTOR RA CAN BE UNSTUFFED WHEN DP159 RETIMER IS USED
A1,A0 = 0B00 ; I2C ADDR 0X5E (7BIT)
SLEW = NC ; 10 PS SLOW
I2C_EN = HIGH ; PUTS DEV CE INTO I2C MODE
PRE_SEL = NC ; 0 DB
OE = HIGH ; NORMAL OPERATION