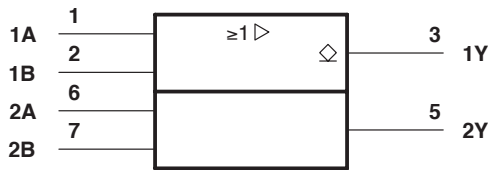


SN55451B, SN55452B, SN55453B, SN55454B  
SN75451B, SN75452B, SN75453B, SN75454B  
DUAL PERIPHERAL DRIVERS

SLRS021B – DECEMBER 1976 – REVISED SEPTEMBER 1999

logic symbol†

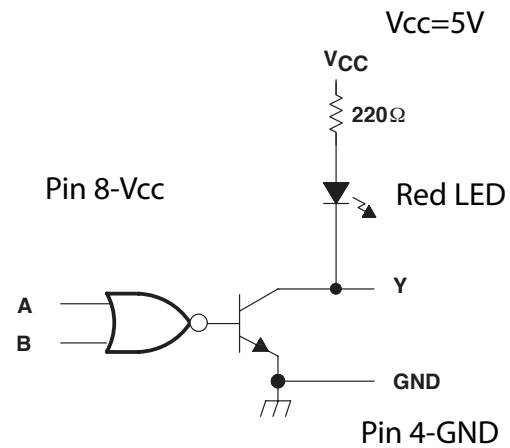


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC publication 617-12.  
Pin numbers shown are for the D, JG, and P packages.

FUNCTION TABLE  
(each driver)

A	B	Y
L	L	L (on state)
L	H	H (off state)
H	L	H (off state)
H	H	H (off state)

positive logic:  
 $Y = A+B$  or  $\overline{A}\overline{B}$



SN7545B is listed as NOR inputs.

My interpretation is when Y is low, the transistor is ON and pulling Y to ground.  
This is the "on state."

EXPECTED  
LED

A	B	Y	LED
L	L	L (on state)	ON
L	H	H (off state)	OFF
H	L	H (off state)	OFF
H	H	H (off state)	OFF

WHAT'S HAPPENING  
LED

A	B	Y	LED
L	L	L (on state)	OFF
L	H	H (off state)	ON
H	L	H (off state)	ON
H	H	H (off state)	ON

I have this part in a PDIP package.

I just bought 10 of these  
Date code: 32ADGJM