

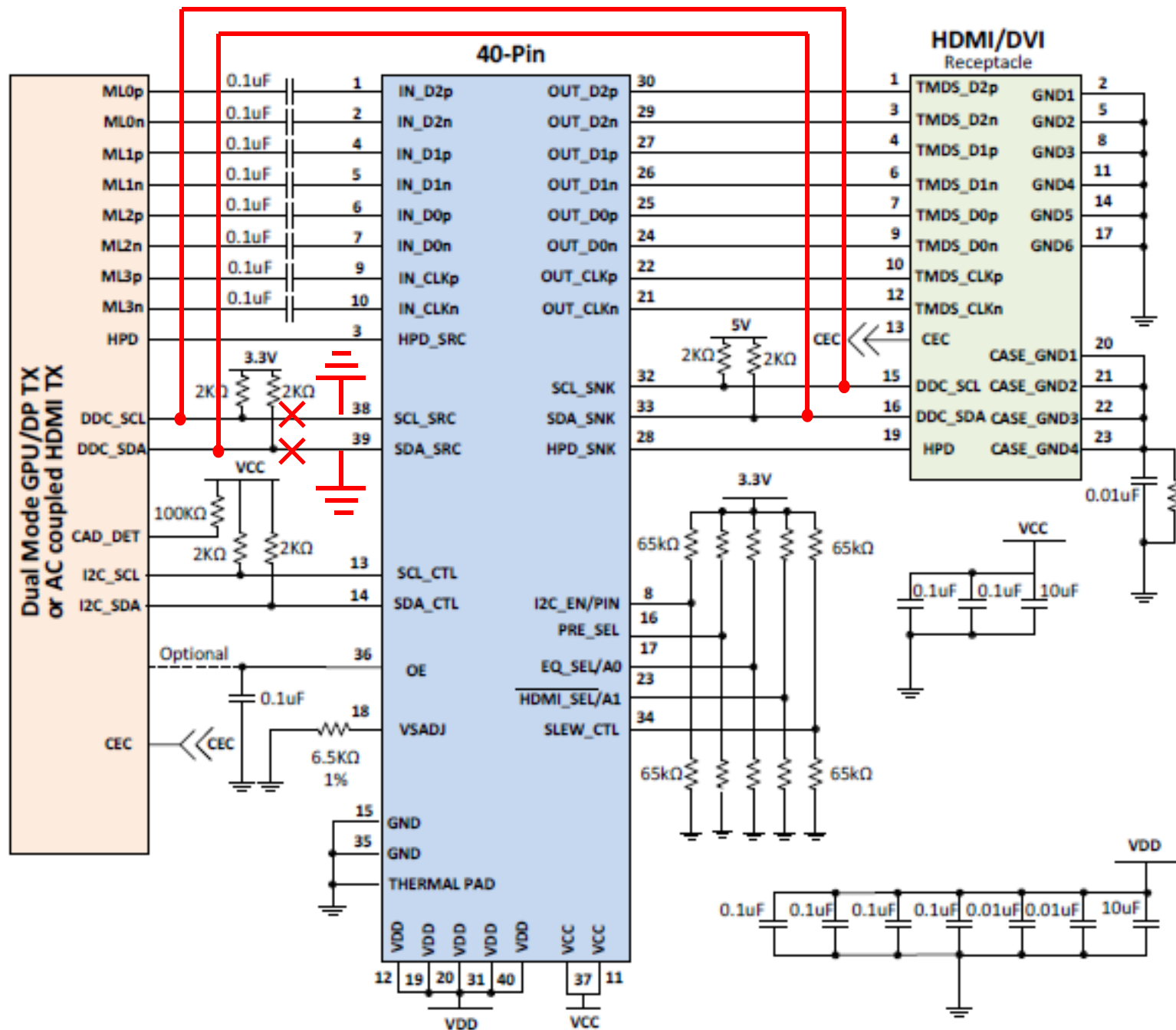


Figure 30. Implementation for Mother Board 1