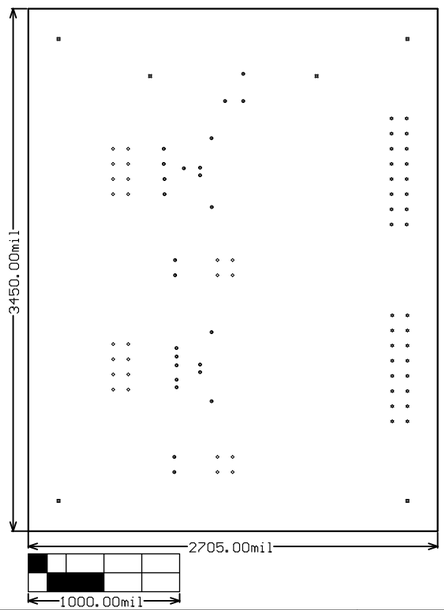
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.80mil	3.7	
3	Top Layer	Copper	1.40mil		
4	Dielectric1	FR-4	5.11mil	3.8	
5	GND	Copper	0.60mil		
6	Dielectric 3	FR-4	47.00mil	4.2	
7	VCC	Copper	0.60mil		
8	Dielectric 2	FR-4	5.11mil	3.8	
9	Bottom Layer	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.80mil	3.7	
11	Bottom Overlay				

CONTROLLED IMPEDANCE

1. 50 OHM CONTROLLED IMPEDANCE ON 9.25MIL SINGLE ENDED, +/-10%
2. 100 OHM DIFFERENTIAL CONTROLLED IMPEDANCE +/- 10% (U1-J1, U2-J4) W/S/W=8/12/8
3. STARTING CU WT .5 Oz TO 1 Oz.



Symbol	Quantity	Finished Hole Size	Plated	Hole Type
●	25	12.00mil (0.305mm)	PTH	Round
◇	24	40.00mil (1.016mm)	PTH	Round
✡	32	40.16mil (1.020mm)	PTH	Round
■	4	125.98mil (3.200mm)	PTH	Round
⊠	2	270.00mil (6.858mm)	PTH	Round
	87 Total			

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 0.2 mm
MIN. VIA PAD SIZE: 24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER _____
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER _____
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER _____

BOARD FINISH:
SILKSCREEN: [X] TOP [ ] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER _____
SOLDER RESIST COLOR: [X] GREEN [ ] OTHER _____
[X] MATTE [ ] SEMI-GLOSS

SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____

ARRAY/PANEL: [ ] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] UL 94V-0 [X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK

TEXAS INSTRUMENTS

PROJECT TITLE: DS90LV047-48AEVM
DESIGNED FOR: Public Release
FILE NAME: SV601246B.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SV601246	REV: B	SVN REV: Not In VersionControl
LAYER NAME = [illegible]nsions	TID #: N/A		
PLOT NAME = Fabrication Drawing	GENERATED : 31-12-2019 10:36:58		TEXAS INSTRUMENTS

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ENGINEER: T.K. Chin
LAYOUT BY: Ed Bonelli
SCALE: 1.00
ALTIUM DESIGNER VERSION: 18.1.9.240