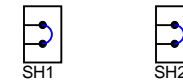


Label Table	
Variant	Label Text
001	UCC21520EVM-286
002	UCC20520EVM-286
003	UCC21521CEVM-286



ZZ5  
Assembly Note  
For -001: Shunt #1 (SH1) should be installed in PIN 2-3 for J-DIS/EN

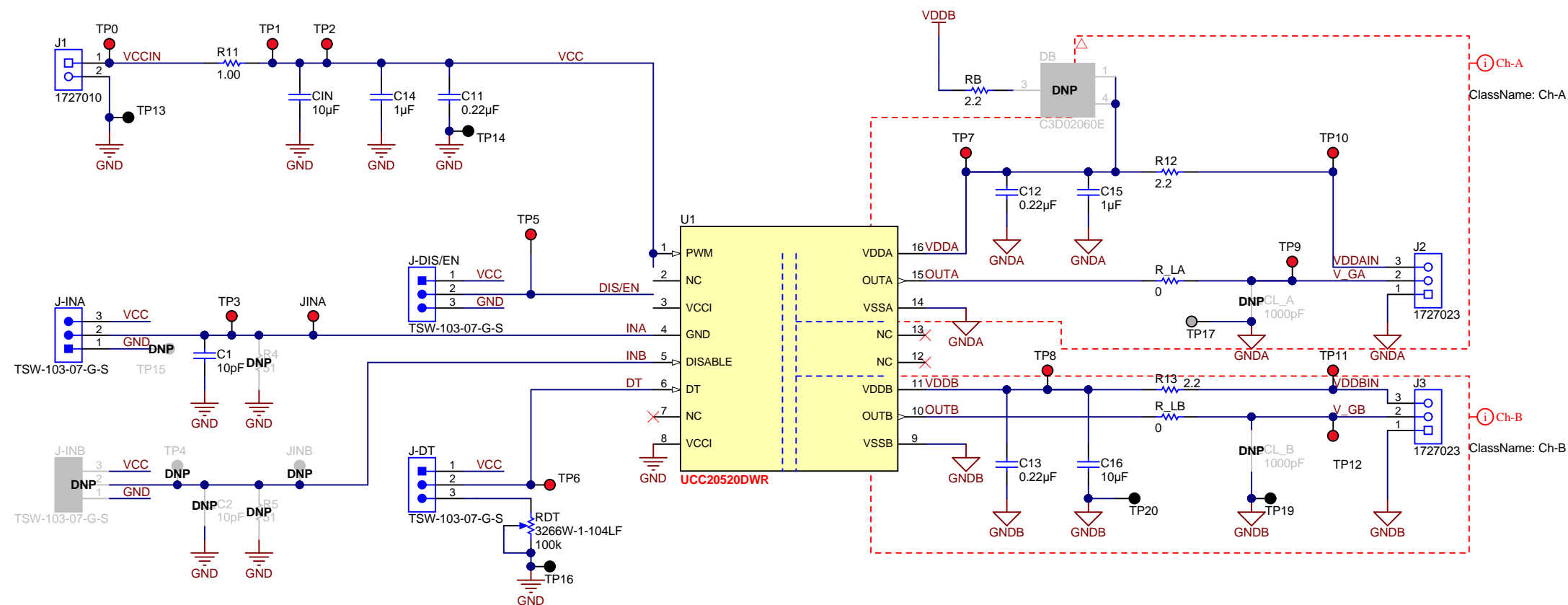
ZZ6  
**Assembly Note**  
 For -002: Shunt #1 (SH1) should be installed in PIN 2-3 for J-DIS/EN

**ZZ7**  
**Assembly Note**  
 For -003: Shunt #1 (SH1) should be installed in PIN 1-2 for J-DIS/EN

ZZ8

**Assembly Note**

Shunt #2 (SH2) should be installed in PIN 1-2 for J-DT



PCB Number: SV601286  
PCB Rev: B

PCB  
LOGO  
Texas Instruments

PCB  
LOGO  
Pb-Free Symbol

PCB  
LOGO  
FCC disclaimer

ZZ1

**Label Assembly Note**

This Assembly Note is for PCB labels only

ZZ2

**Assembly Note**

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

**Assembly Note**

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note
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These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.