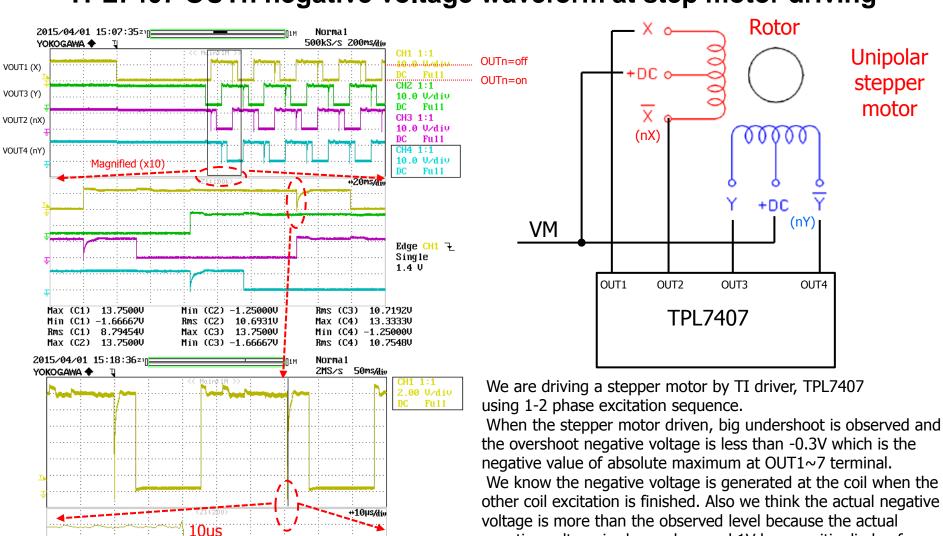
TPL7407 OUTn negative voltage waveform at step motor driving



Edge CH1 ₹ Single

1.40 V

Big undershoot

Magnified (x5000)

-1.0V

Max (C1) 13.1667V

Min (C1) -916.667mV Rms (C1) 9.35478V

We know the negative voltage is generated at the coil when the other coil excitation is finished. Also we think the actual negative negative voltage is clamped around 1V by parasitic diode of

NMOS of TPL7407 OUTn driver.

Our question is whether this negative voltage breaks TPL7407 or not and whether the voltage affects any problem on TPL7407. If the answer is yes, is there any solution to reduce the negative voltage to safety voltage level.

Anybody, could you please give us your any comment?