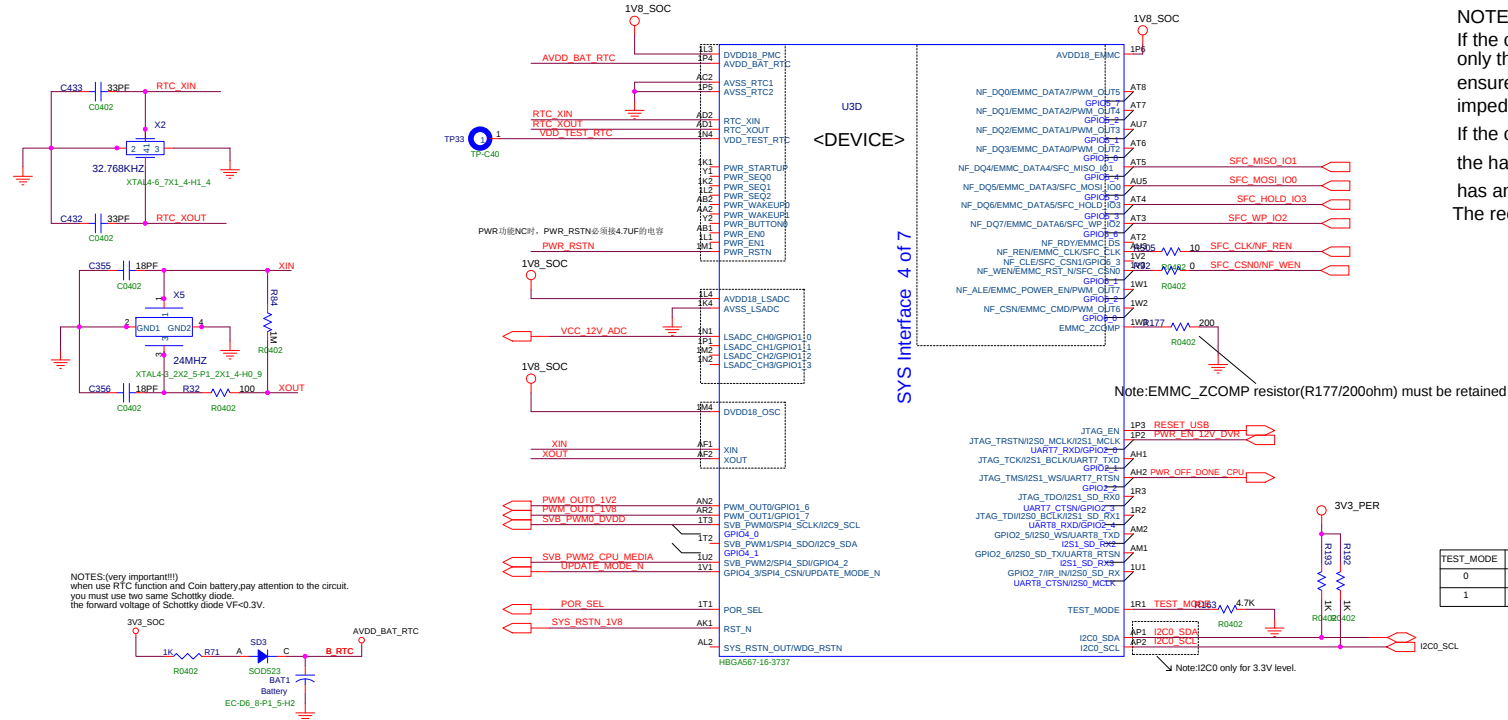


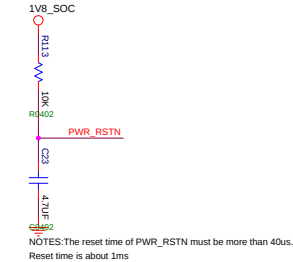
SYS of HI3519AV100



NOTES:(very important!!!)

If the customer does not use the UPDATE_MODE_N function, the customer can use only the GPIO output function or SPI function of the pin. The hardware design must ensure that the pin has an external pull-up resistor and the recommended impedance is 4.7k

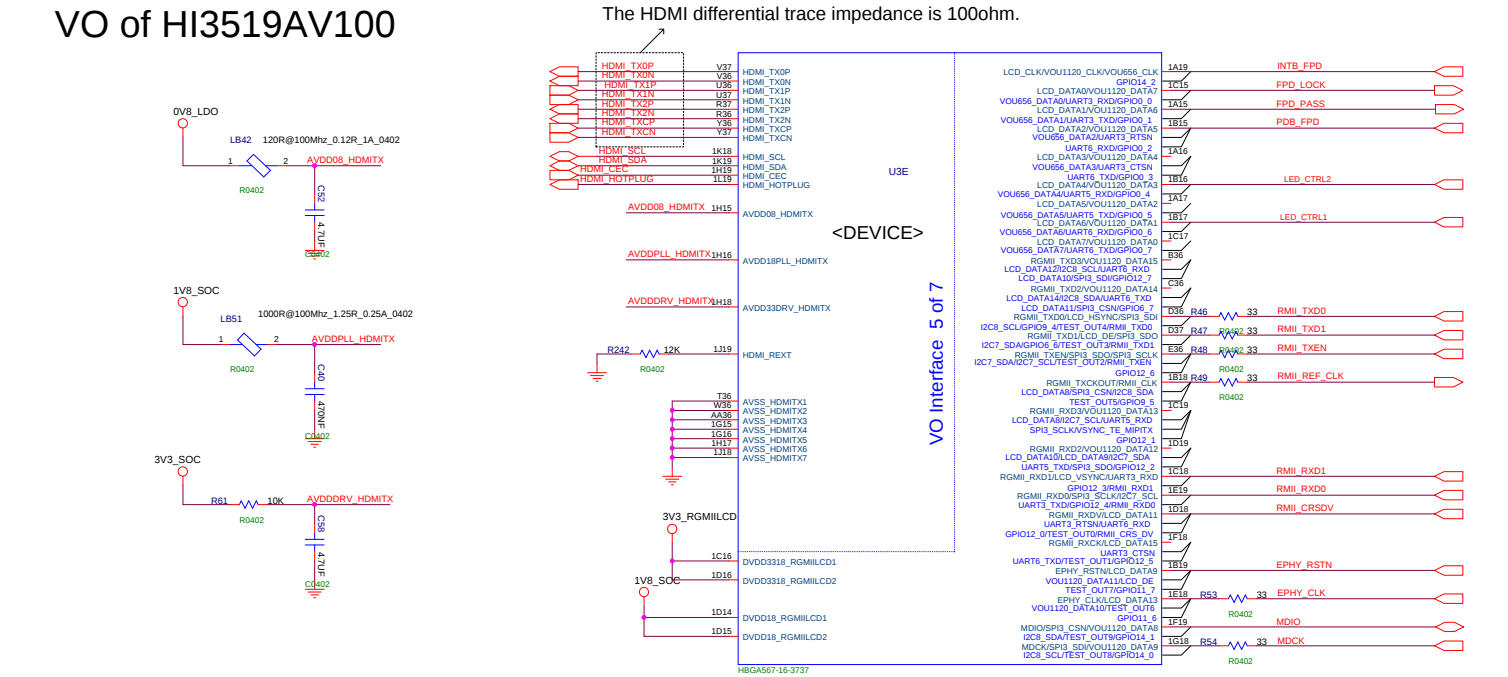
If the customer does not use any function of this pin, the hardware design must ensure that the pin has an external pull-up resistor. The recommended resistance is 4.7k



TEST_MODE	FUNCTION
0	normal mode
1	test mode

NOTES: The reset time of PWR_RSTN must be more than 40us.
Reset time is about 1ms

VO of HI3519AV100



NOTES:(very important!!!)

If DVDD3318_RGMII_LCD is connected to 3.3V

the schematic diagram of hardware design of DVDD3318_RGMII_LCD is referred to Hi3519AV100 hardware design user guide

Make sure that the voltage difference between DVDD3318_RGMII_LCD is within ± 1.98 V during power-on and power-off.

If DVDD3318_RGMII_LCD is connected to 3.3V

Set the output voltage of these pins to 3.3 V by referring to Hi3519AV100 3.3 V I/O Application Note.

If the voltage is set to 1.8 V, these I/O pins may be damaged.

