void TCANTrcv\_vidInit(void)

{

    uint16 Loc\_u16TimeCnt;

    uint8  Loc\_u8Status;

    uint8  Loc\_u8WakeupSt;

    uint8  Loc\_u8ConfigData;

    uint8  Loc\_u8WakeupSrc;

    uint32 Loc\_u32CANID;

    uint32 Loc\_u32CANIDMASK;

    uint16       Loc\_u16TempValue = 0;

    static uint8 Loc\_su8TempCount = 0;

    Loc\_su8TempCount++;

    TCANTrcv\_vidConfigReg(0x46, 0xFF);

    Loc\_u8ConfigData =

        (1 << 6) | (0 << 4) | (0 << 2) | (0);

    TCANTrcv\_vidConfigReg(0x11, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x11, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x11, Loc\_u8ConfigData);

    Loc\_u8ConfigData = 0;

    TCANTrcv\_vidConfigReg(0x51, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x51, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x51, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (1 << 1);

    TCANTrcv\_vidConfigReg(0x19, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x19, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x19, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (0 << 6) | (0 << 4) | (1 << 2) | (1);

    TCANTrcv\_vidConfigReg(0x13, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x13, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x13, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (0 << 7) | (5 << 4) | (0 << 2);

    TCANTrcv\_vidConfigReg(0x44, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x44, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x44, Loc\_u8ConfigData);

    Loc\_u32CANID = 0x359;

    Loc\_u8ConfigData = ((0) << 6);

    Loc\_u8ConfigData |= ((Loc\_u32CANID >> 6) & 0x1F);

    Loc\_u8ConfigData |= (0 << 5);

    TCANTrcv\_vidConfigReg(0x32, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x32, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x32, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (Loc\_u32CANID << 2) & 0xFC;

    TCANTrcv\_vidConfigReg(0x33, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x33, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x33, Loc\_u8ConfigData);

    Loc\_u32CANIDMASK = 0x000;

    Loc\_u8ConfigData = ((Loc\_u32CANIDMASK >> 3) & 0xff);

    TCANTrcv\_vidConfigReg(0x37, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x37, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x37, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (((Loc\_u32CANIDMASK) & 0x7) << 5) | (0 << 1) | (0);

    TCANTrcv\_vidConfigReg(0x38, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x38, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x38, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (1 << 7) | (1 << 2);

    TCANTrcv\_vidConfigReg(0x1C, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x1C, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x1C, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (0 << 6) | (0 << 5) | (1 << 4) | (0 << 2) | (1 << 1);

    TCANTrcv\_vidConfigReg(0x1A, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x1A, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x1A, Loc\_u8ConfigData);

    Loc\_u8ConfigData = (1 << 7) | (0 << 6) | (0 << 5) | (1 << 2) | (1 << 1) | (1 << 0) ;

    TCANTrcv\_vidConfigReg(0x10, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x10, Loc\_u8ConfigData);

    TCANTrcv\_vidConfigReg(0x10, Loc\_u8ConfigData);

}