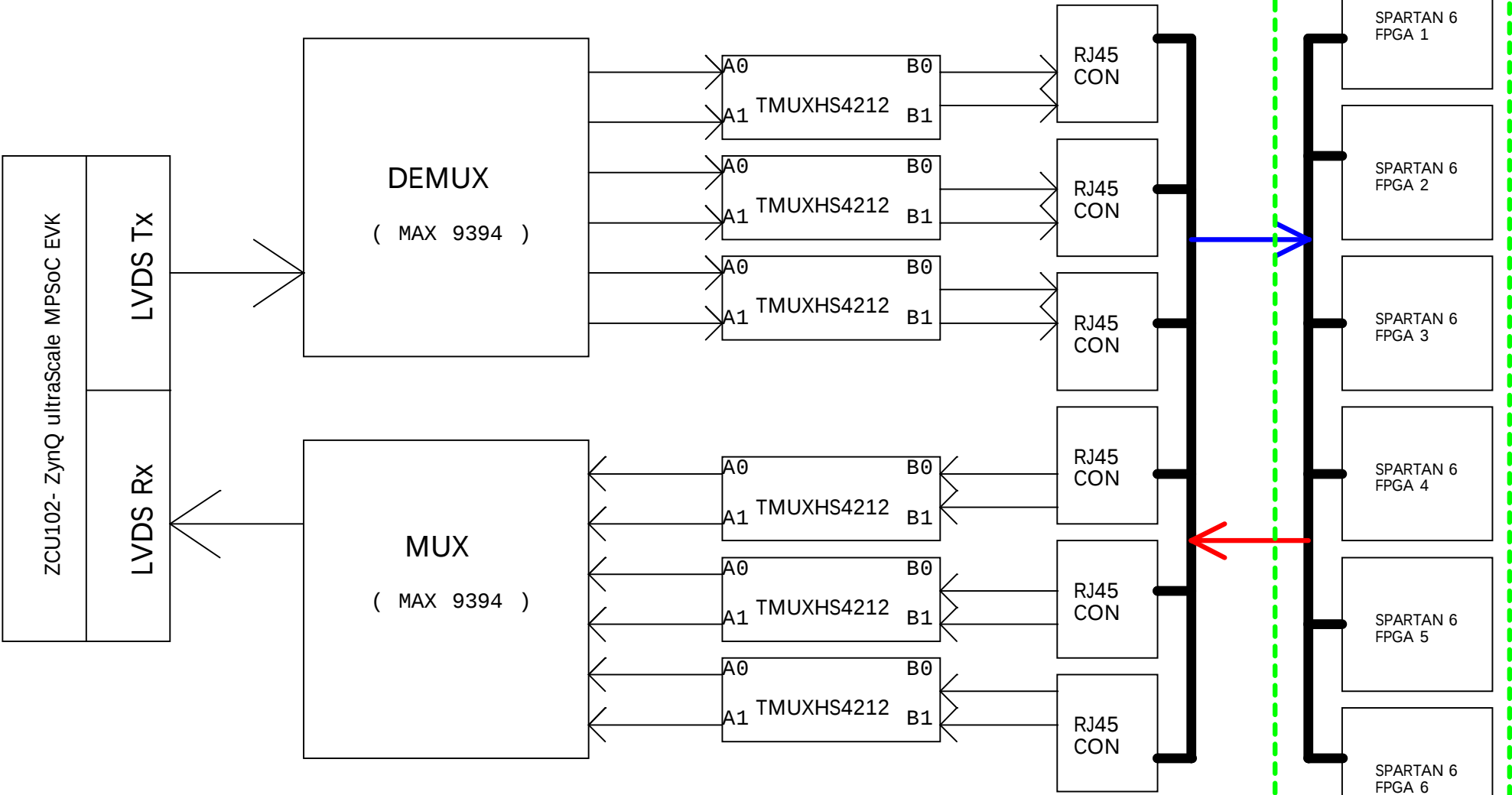


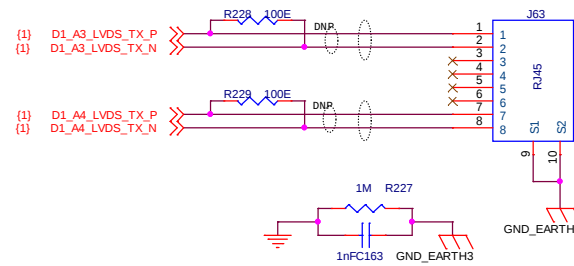
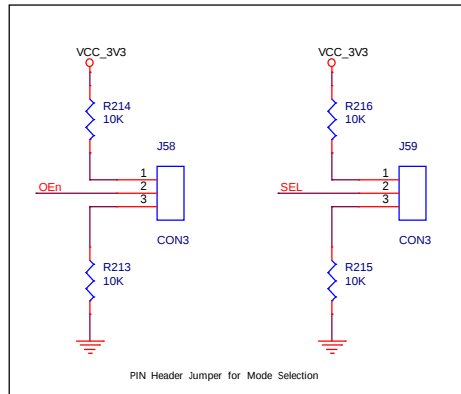
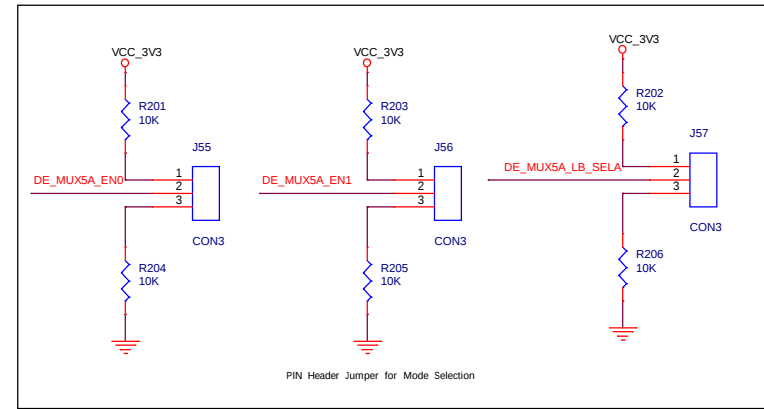
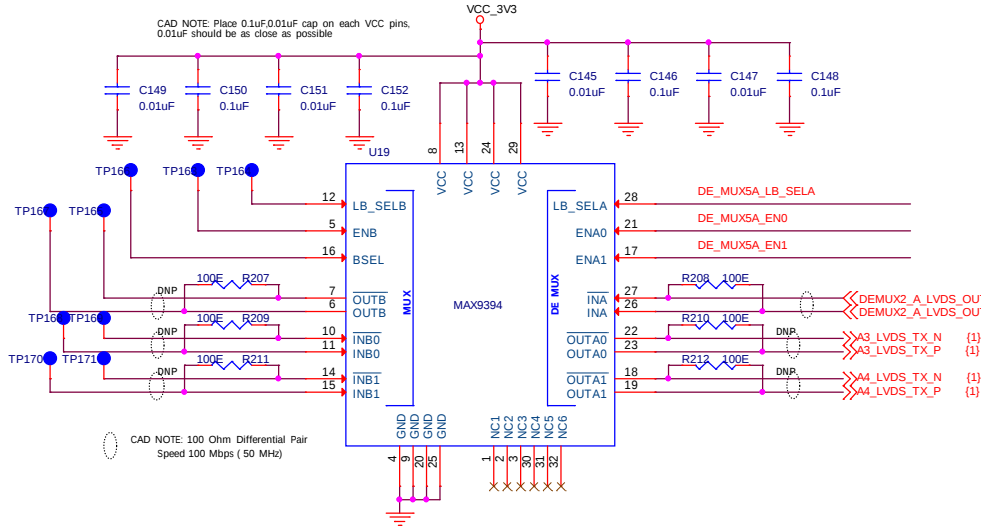
BLOCK DIAGRAM



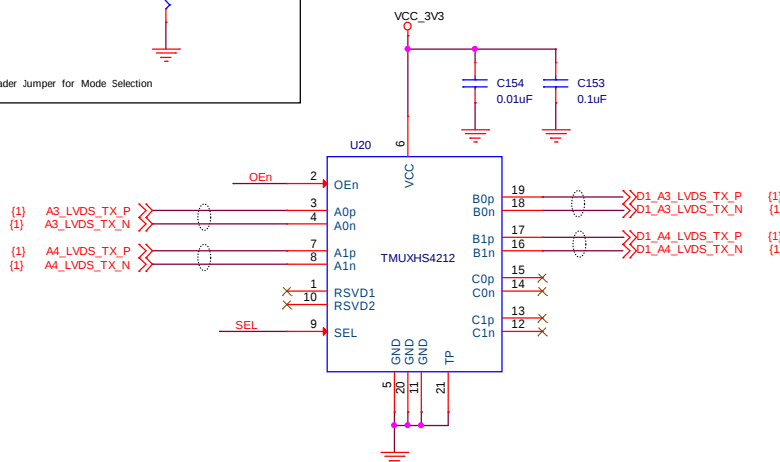
These 6 FPGA' s are independantly powered.
So During testing
only one FPGA is active. The Purpose of
TMUXHS4212 is to protect the FPGA IO's which is
unpowered during testing, from
the Common mode voltage of MAX9394 while
the MUX/DEMUX channel is suspended.
Since the TMUXHS4212 is a passive
differential switch we can expect to prevent the
Vcom=1.2V on the MUX/DEMUX side by
selecting the path from A to C. During
normal working , it shall be A to B

Title		
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Size	Document Number	Rev
A	<Doc>	<RevCode>
Date:	Sunday, October 18, 2020	Sheet 1 of 2

DE MUX A



SPARTAN 6 FPGA



Title			
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Size	Document	Number	Rev
A3	<Doc>		<RevCode>
Date:	Sunday, October 18, 2020		Sheet 2 of 2