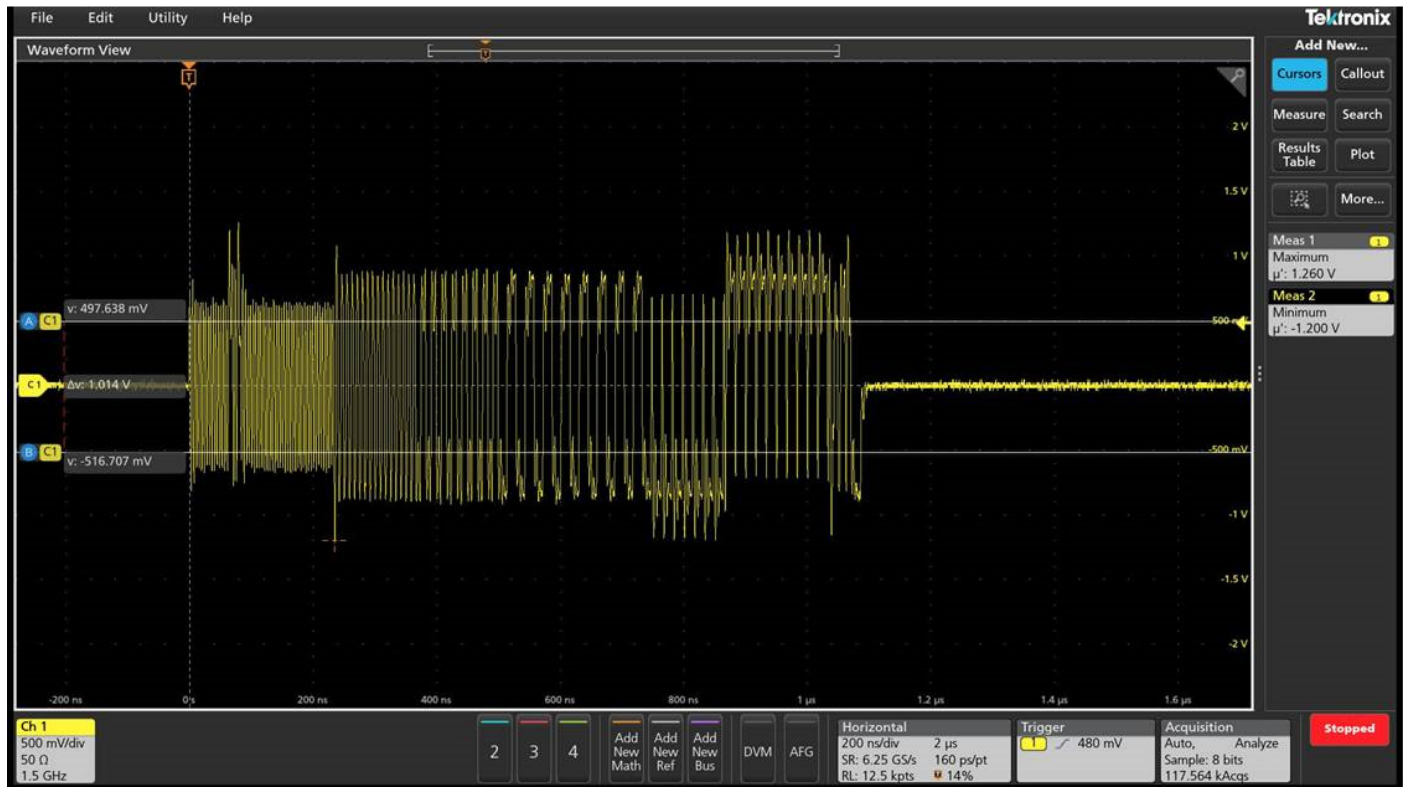
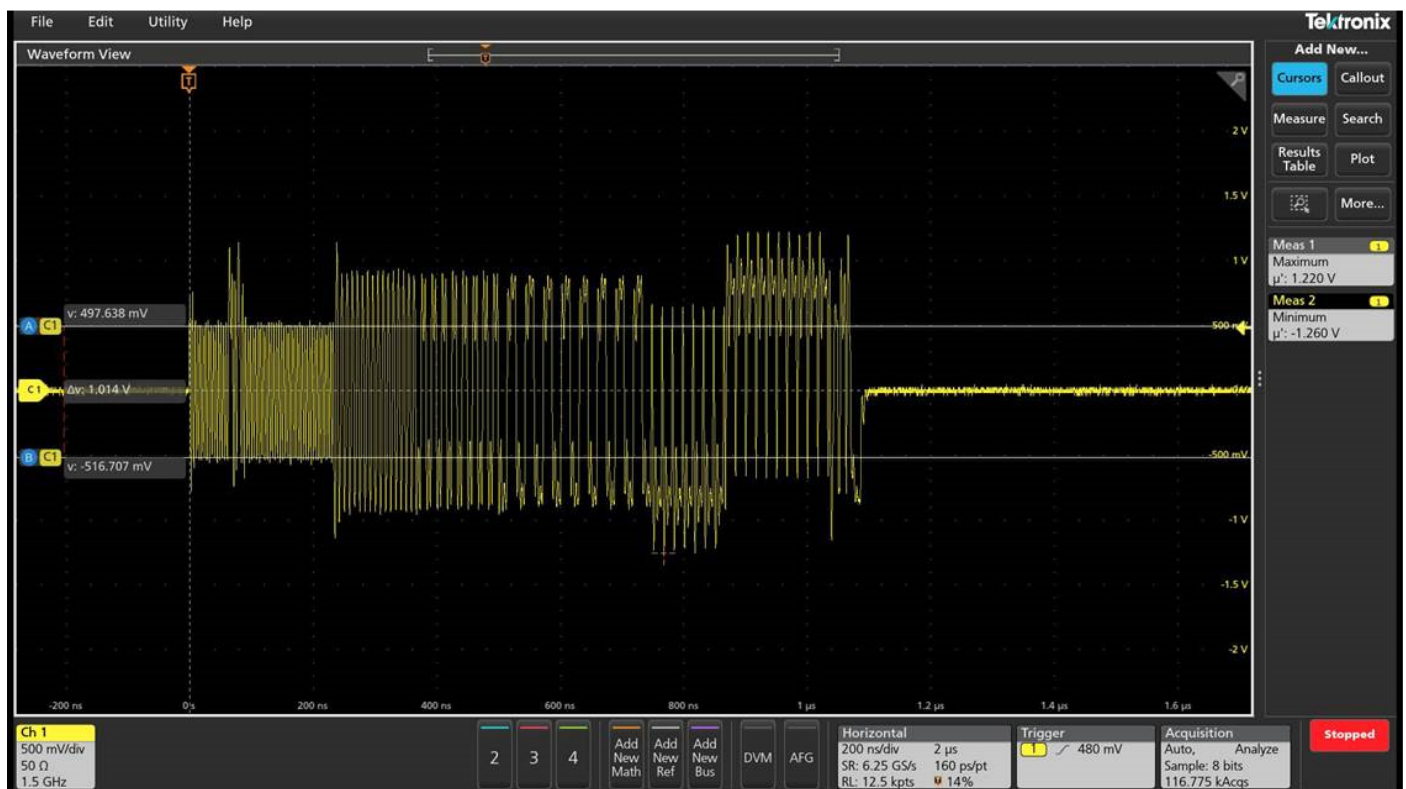


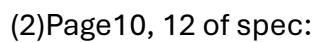
- 1.
- original schematic design: DC_BOOST/ENA_HS be high. (The strongest driving strength.)



Remove TI/TUSB212IRWBR



(1)Page1 of spec:



A typical application is shown in [Figure 6](#). In this setup, D2P and D2M face the USB connector while D1P and D1M face the USB host. If desired, the orientation may be reversed [that is, D2 faces transceiver and D1 faces connector].

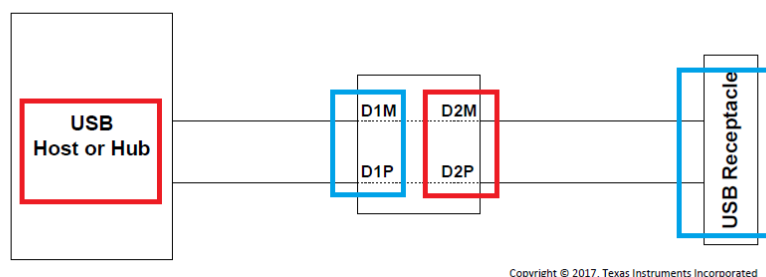
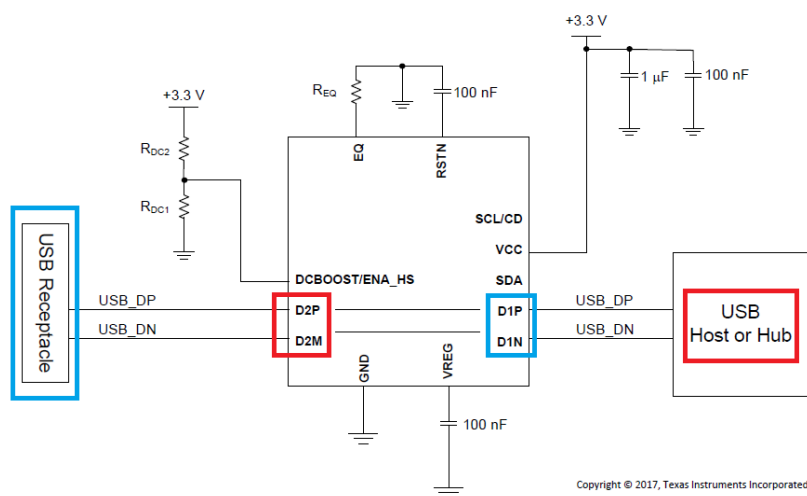


Figure 6. Typical Application



D2P must be shorted to D1P on PCB.
D2N must be shorted to D1N on PCB.

Figure 7. Reference Schematic

3.Our schematic design:

TEST CONDITIONS	EQ
AC Boost Level 0	160 ohm
AC Boost Level 1	2K ohm
AC Boost Level 2	3.9K ohm
AC Boost Level 3	6K ohm

USB 2.0 Redriver

