

SLIMSAS_CONNECTOR

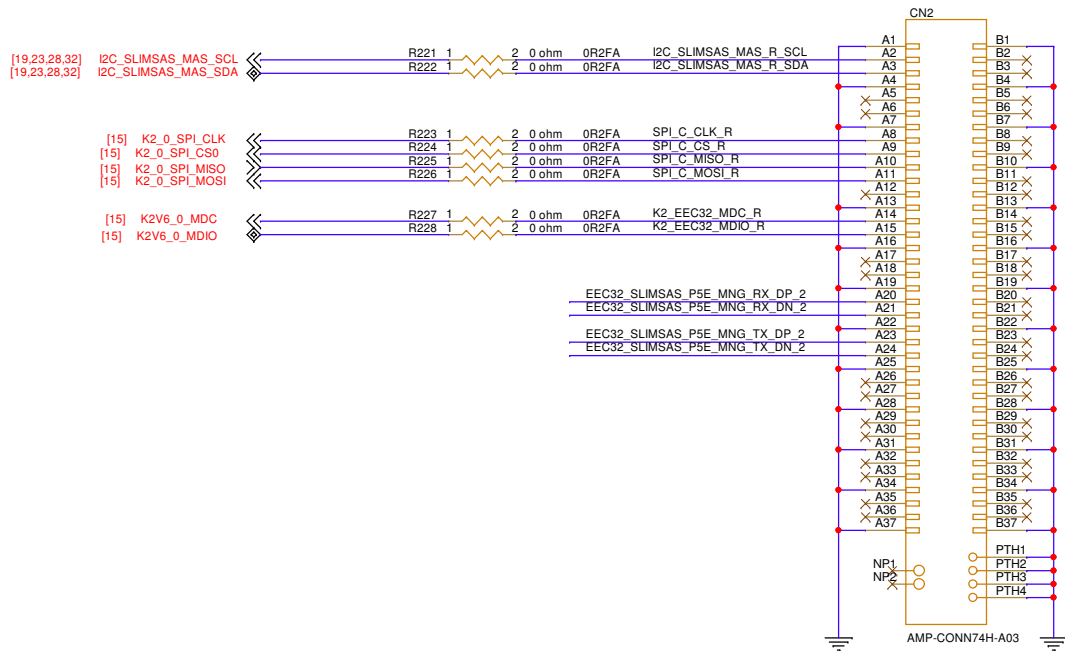
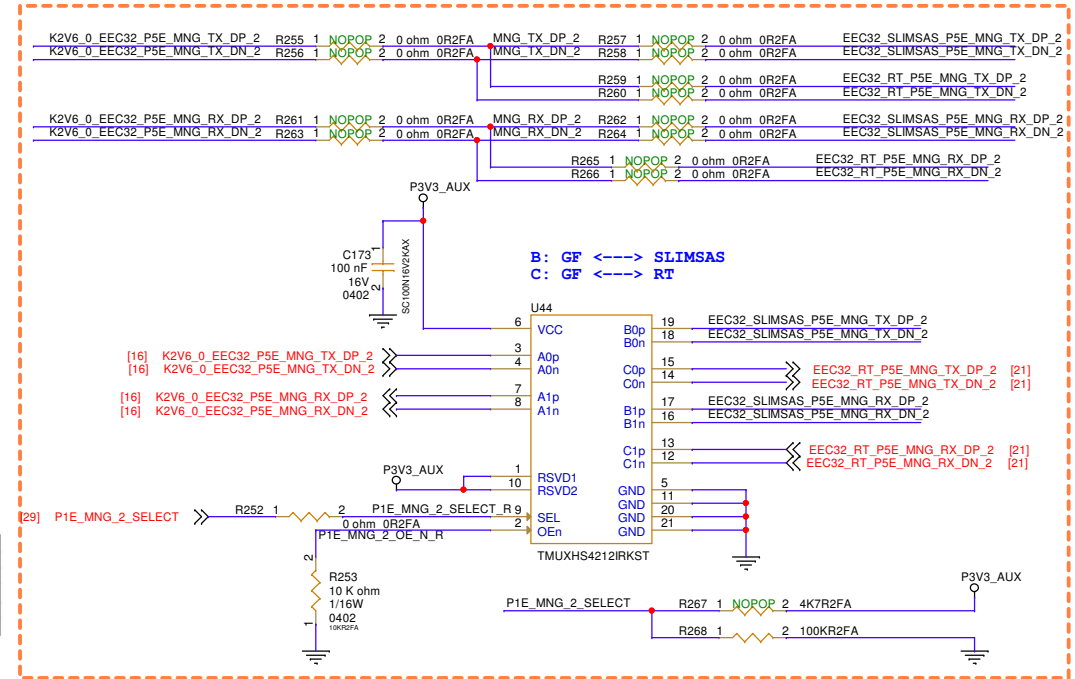


Table 8-1. Port Select Control Logic⁽¹⁾

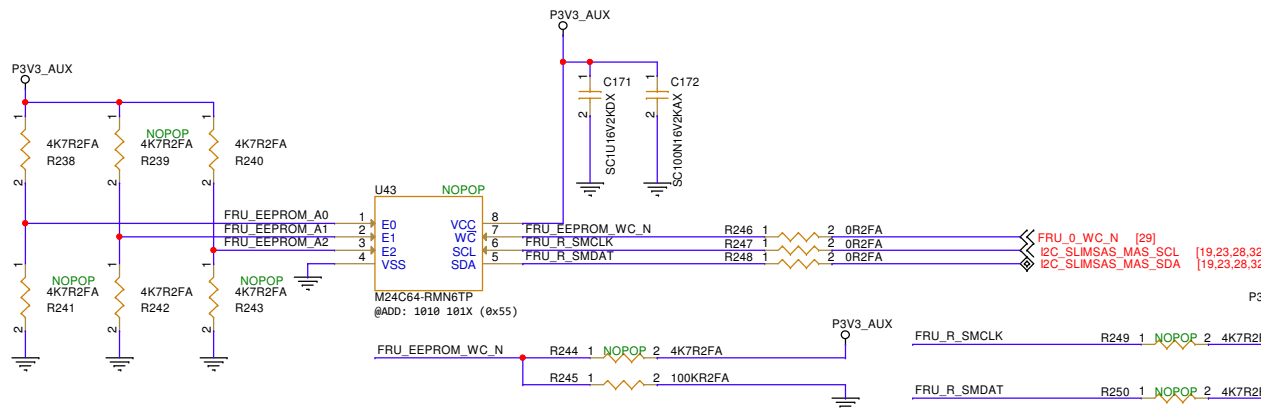
PORT A CHANNEL	PORT B OR PORT C CHANNEL CONNECTED TO PORT A CHANNEL	SEL = L	SEL = H
A0p	B0p	C0p	
A0n	B0n	C0n	
A1p	B1p	C1p	
A1n	B1n	C1n	

(1) The TMUXHS4212 can tolerate polarity inversions for all differential signals on Ports A, B, and C. Ensure that the same polarity is maintained on Port A versus Ports B or C in such flexible implementation.

Place near GF2(CO-LAY)



FRU



default status: FRU_WC (L)