Connection diagram of connector -TMDS 181

HDMI	IN Connec	tor	TMDS181		
Pin				Pin	
12 10	CLKn CLKp		IN_D2p IN_D2n	2	
9	DOn DOp		IN_D1 p IN_D1 n	5 6	
6 4	D1 n D1 p		IN_DOp IN_DOn	8	
3	D2n D2p		IN_CLKp IN_CLKn	11 12	

Evaluation is underway at TI - TMDS181RGZEVM.

As a result of the following procedure, the Lane Swap bit (09h bit 7) does not become "1". Procedure

1:SWAP/POL(1pin): "H" fixed.

2: Device power on

3: I 2 C control: Set to Retimer mode $(0Ah:1:0\rightarrow"11")$

Are there any mistakes in the procedure?
When swapping lanes and polarities, please tell me the order of setting.
Is TMDS 181 correspondence of replacing only one side of lane and polarity?
(Is it impossible to exchange both?)