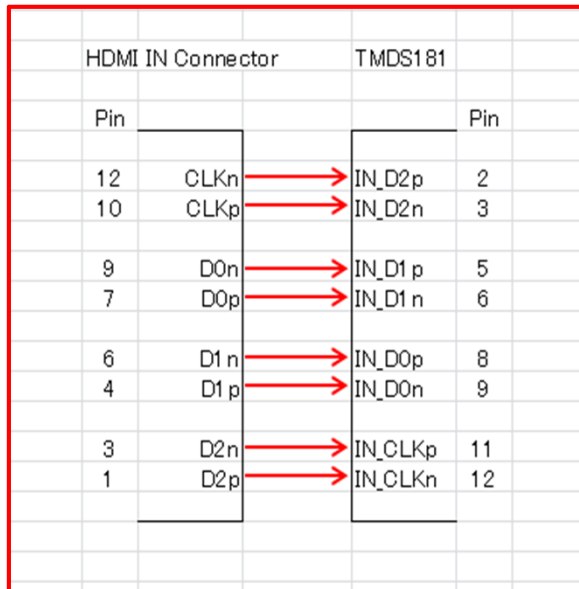


Connection diagram of connector -TMDS 181



Evaluation is underway at TI - TMDS181RGZEVN.

As a result of the following procedure, the Lane Swap bit (09h bit 7) does not become "1".

Procedure

1: SWAP/POL(1pin): "H" fixed.

2: Device power on

3: I2C control: Set to Retimer mode (0Ah: 1:0 → "11")

Are there any mistakes in the procedure?

When swapping lanes and polarities, please tell me the order of setting.

Is TMDS 181 correspondence of replacing only one side of lane and polarity?

(Is it impossible to exchange both?)