System　Block

FreeScale：
MPC866PCVR100A

3.3V
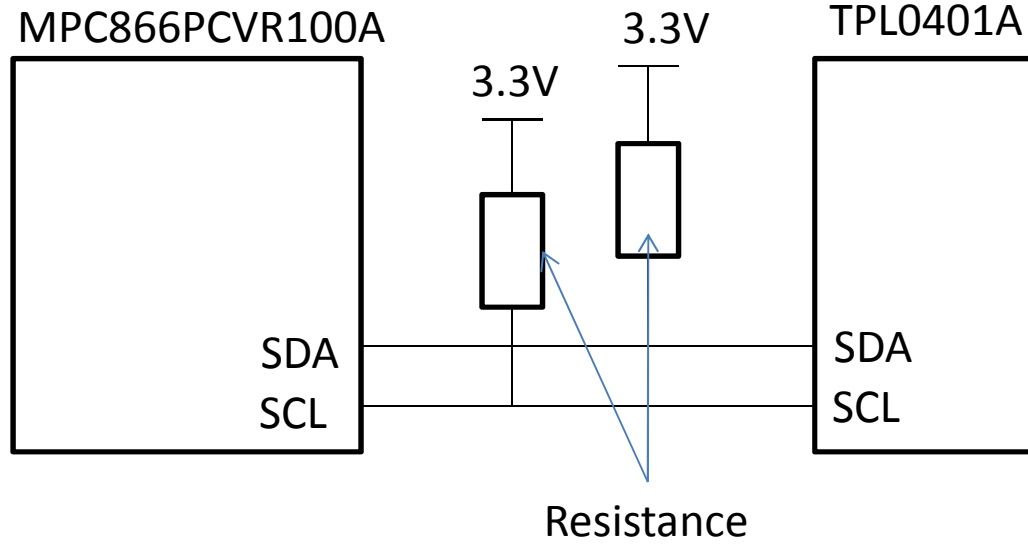
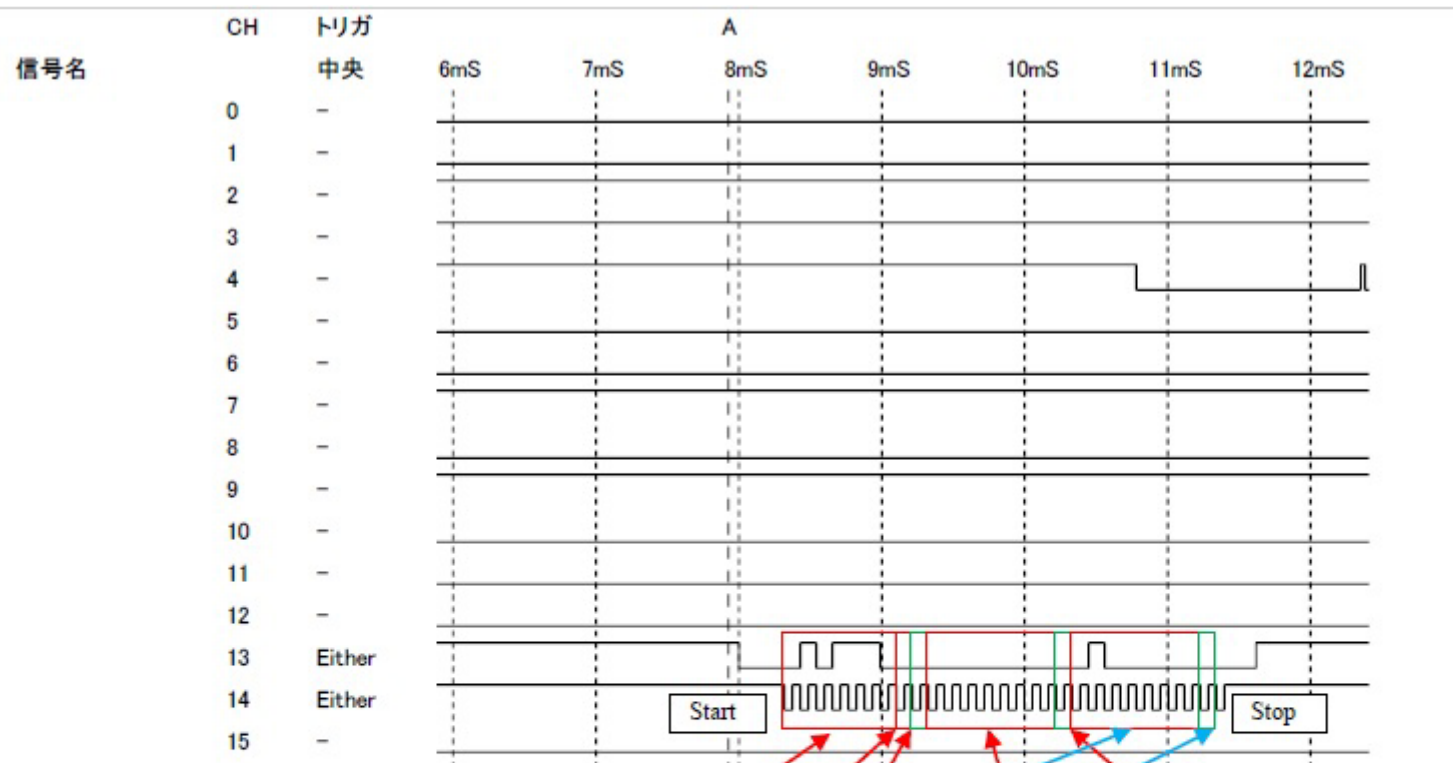3.3V

TPL0401A

SDA
SCL

SDA
SCL
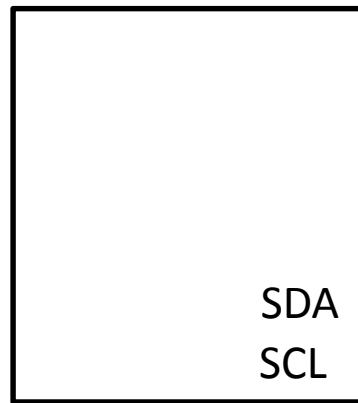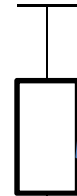
Resistance

Write data OrderTiming

Write data
書き込み Data = 01000000

Reading data order timing

信号名	CH	トリガ	AD							
		中央	0	1.25mS	2.5mS	3.75mS	5mS	6.25mS	7.5mS	
	0	Rise								
	1	–								
	2	–								
	3	–								
	4	–								
	5	–								
	6	–								
	7	–								
	8	–								
	9	–								
	10	–								
	11	–								
	12	–								
data	13	Either								
clk	14	Either					Start		Stop	
	15	–								

I^{2}C Read From A Register

Start	Address (01_1110)	0	Ack	Command (00000000)	Ack

Reading data

DataByte = 01111111
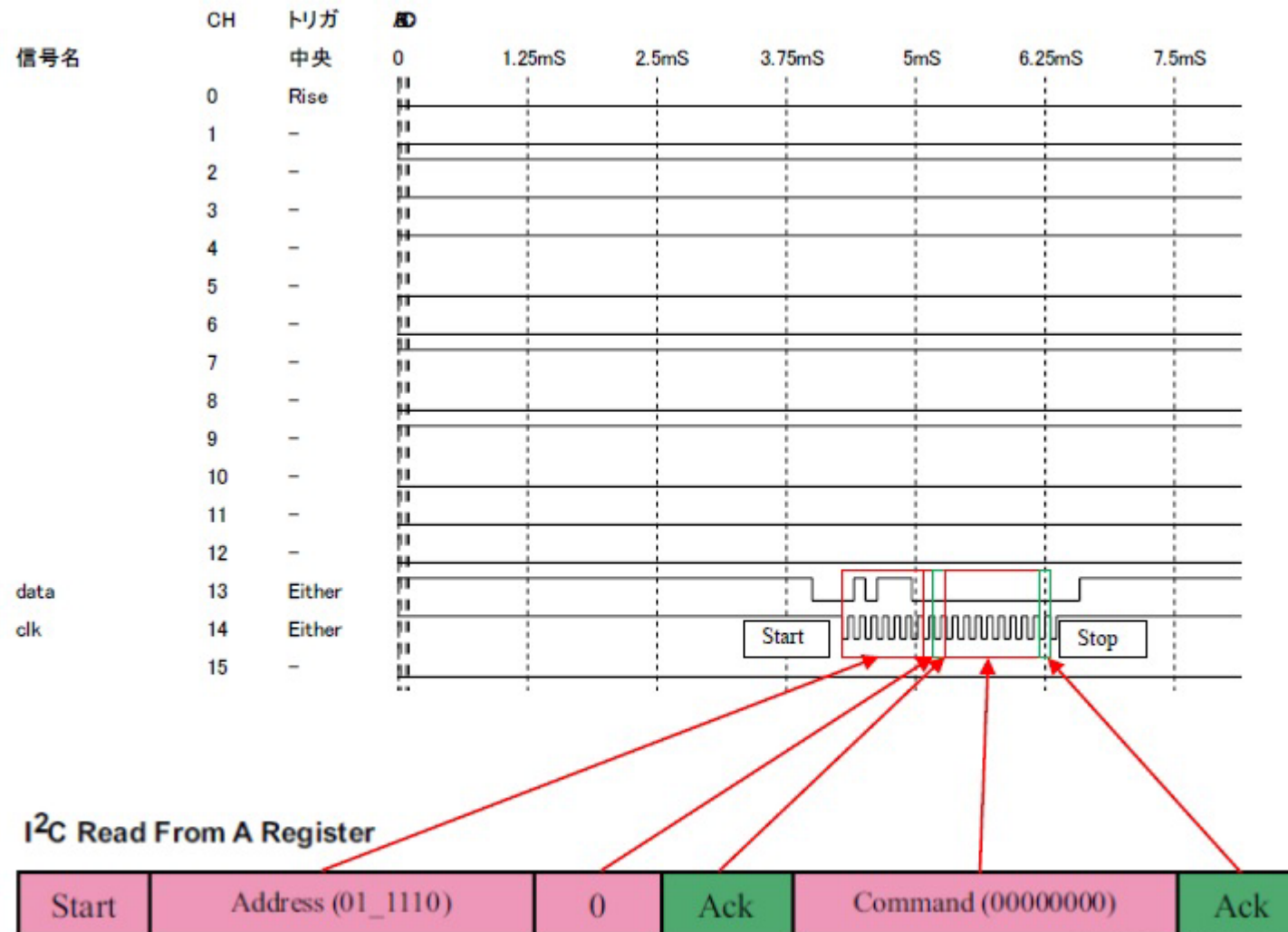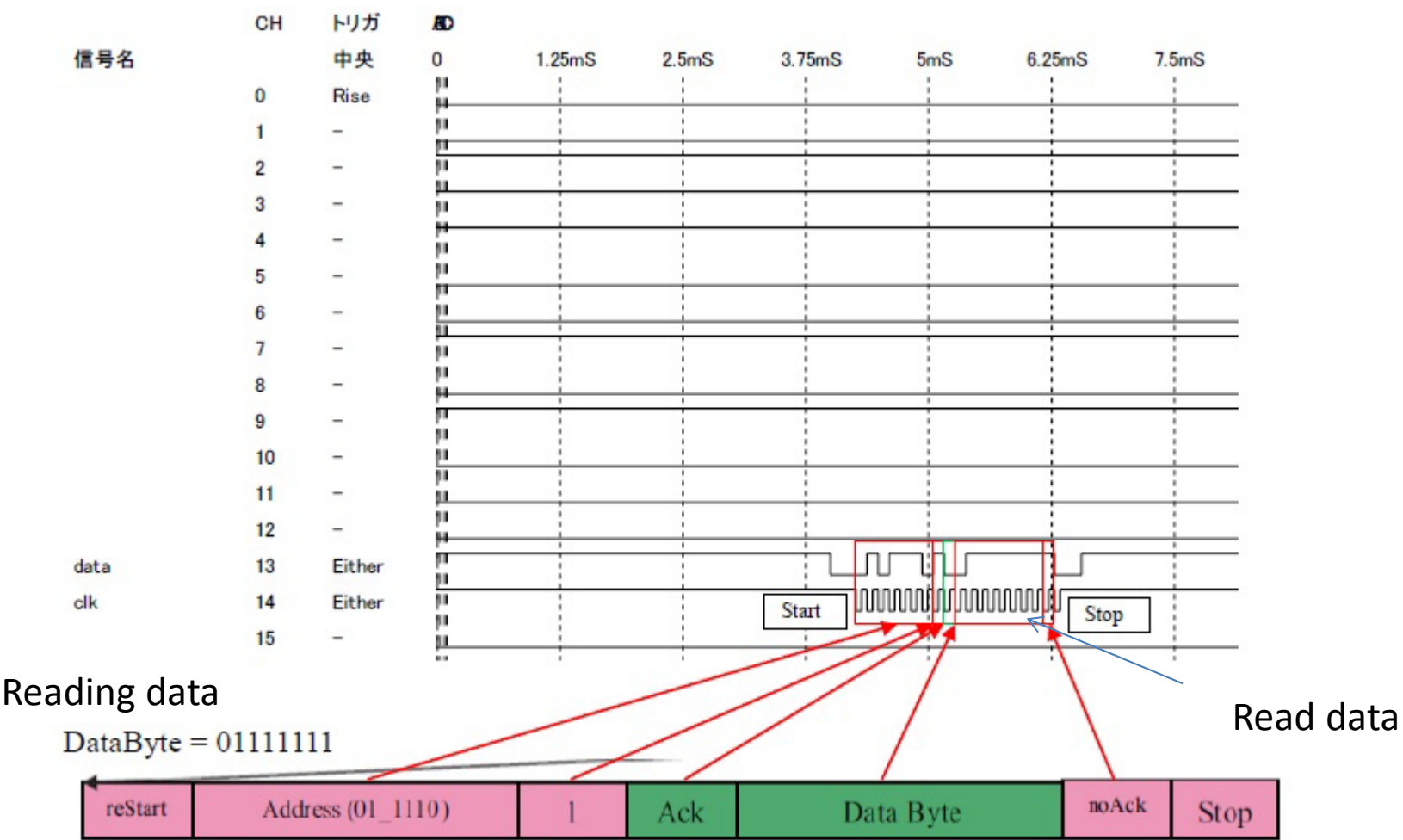
When the data written in is read, it'll be different data.

SDA,SCL timing

測定間隔:5nS   測定モード:シングル   AtoB:1.145μS   A   HEX
測定時間:1.31007mS   ズーム:×1/32   CtoD:500nS   15-0   029C

信号名	CH	トリガ 左	Æ/B 525μS	550μS	575μS	600μS	625μS
	0	Rise					
	1	–					
	2	–					
	3	–					
	4	–					
	5	–					
	6	–					
	7	–					
	8	–					
	9	–					
	10	–					
	11	–					
	12	–					
data	13	Either					
clk	14	Either					
	15	–					

SDA

SCL

Data Line
Stable
Data Valid

Change
of Data
Allowed