



ADCIN1 Table 7. Boot Mode Pin Strapping

| SPL_MISO | ADCIN1 DIV = R2/(R1+R2) ⁽¹⁾ | Dead Battery Mode | Device Configuration | |
|----------|---|-------------------|------------------------|--------------------|
| | DIV_MIN | DIV_MAX | | |
| 1 | 0.50 | 0.18 | BP_NoResponse | Safe Configuration |
| 1 | 0.20 | 0.28 | BP_WaitFor3V3_Internal | Safe Configuration |
| 1 | 0.30 | 0.38 | BP_ECWait_Internal | Infinite Wait |
| 1 | 0.40 | 0.48 | BP_WaitFor3V3_External | Safe Configuration |

(1) External resistor tolerance of 1% is required. Resistor values must be chosen to yield a DIV value centered nominally between listed MIN and MAX values.

ADCIN2 Table 6. I²C Address Selection

| DIV = R2/(R1+R2) ⁽¹⁾ | | I ² C UNIQUE ADDRESS [3:1] | |
|---------------------------------|---------|---------------------------------------|--------------------|
| DIV_min | DIV_max | I2C_ADDR_DECODE_C1 | I2C_ADDR_DECODE_C2 |
| Short ADCIN2 to GND | | 000b | 100b |
| 0.20 | 0.38 | 001b | 101b |
| 0.40 | 0.58 | 010b | 110b |
| Short ADCIN2 to LDO_3V3 | | 011b | 111b |

(1) External resistor tolerance of 1% is required. Resistor values must be chosen to yield a DIV value centered nominally between listed MIN and MAX values.

