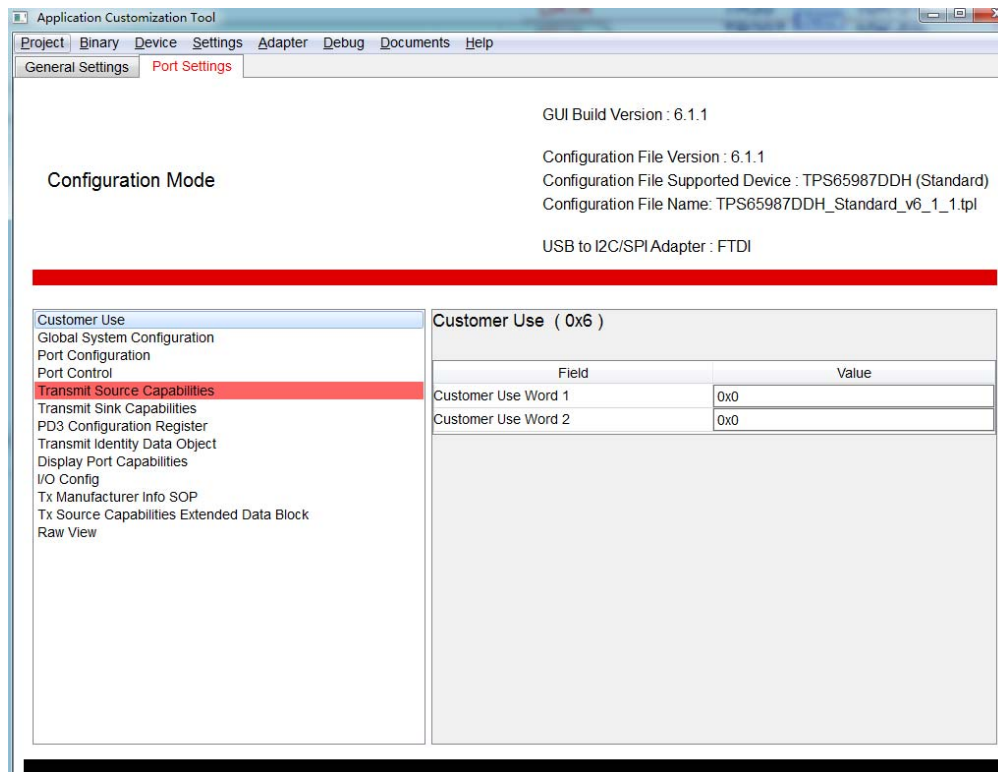
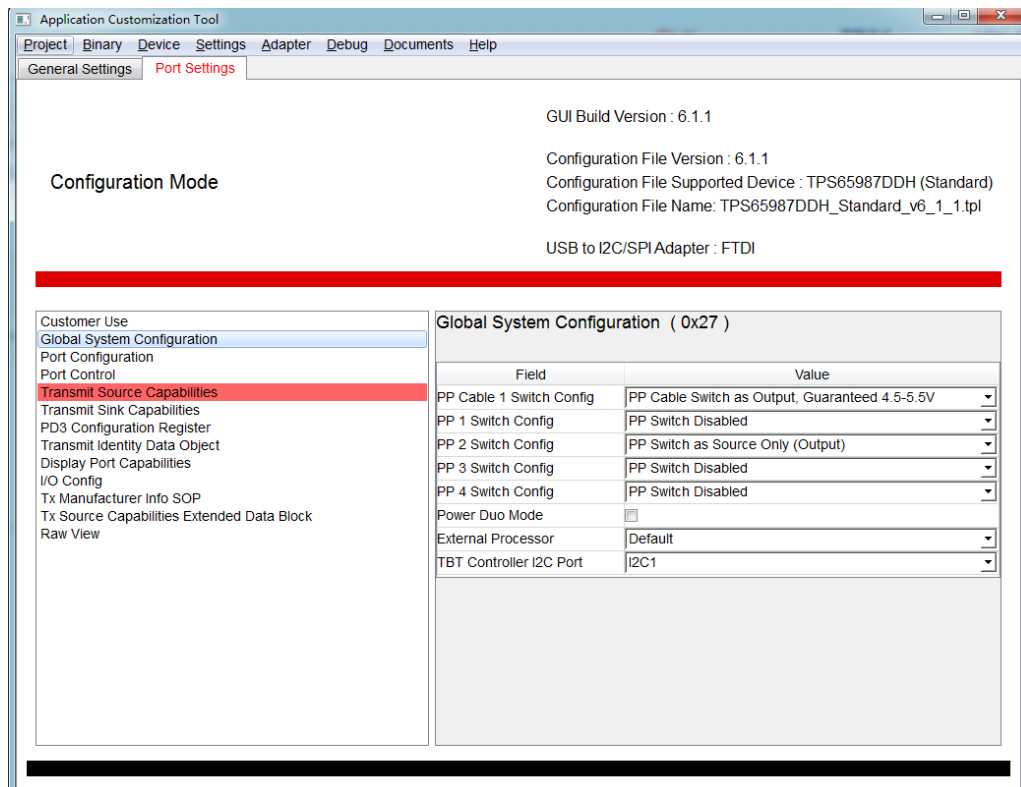


TPS6598X config is OK?

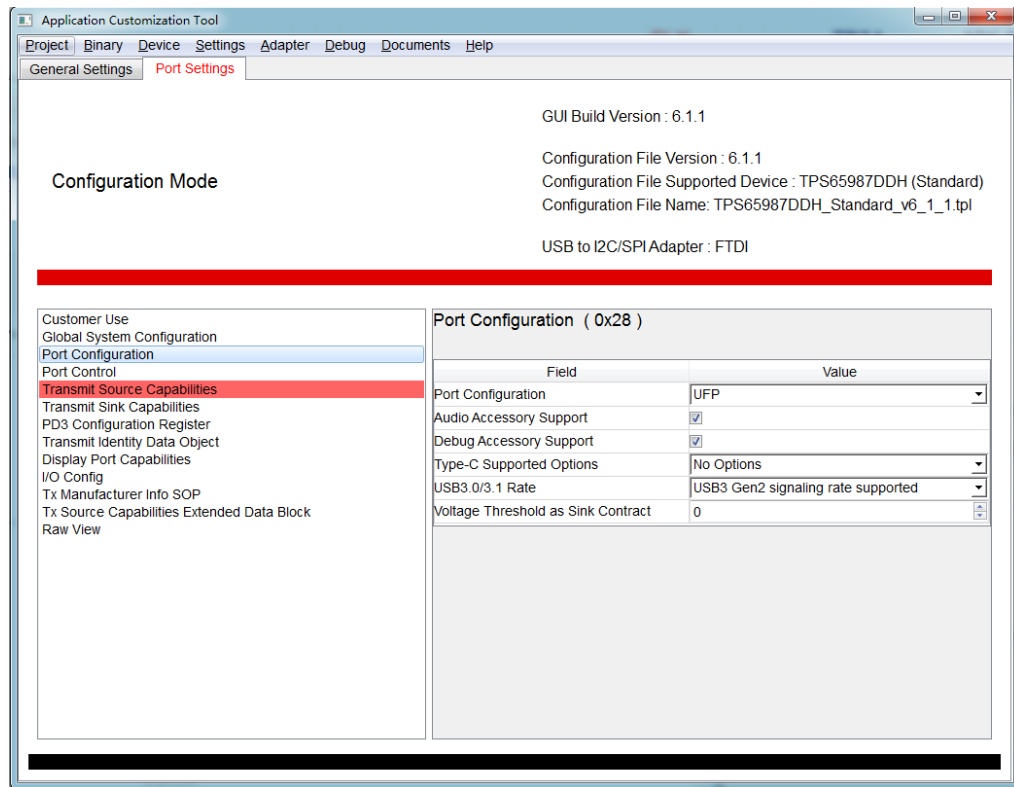
Customer use:



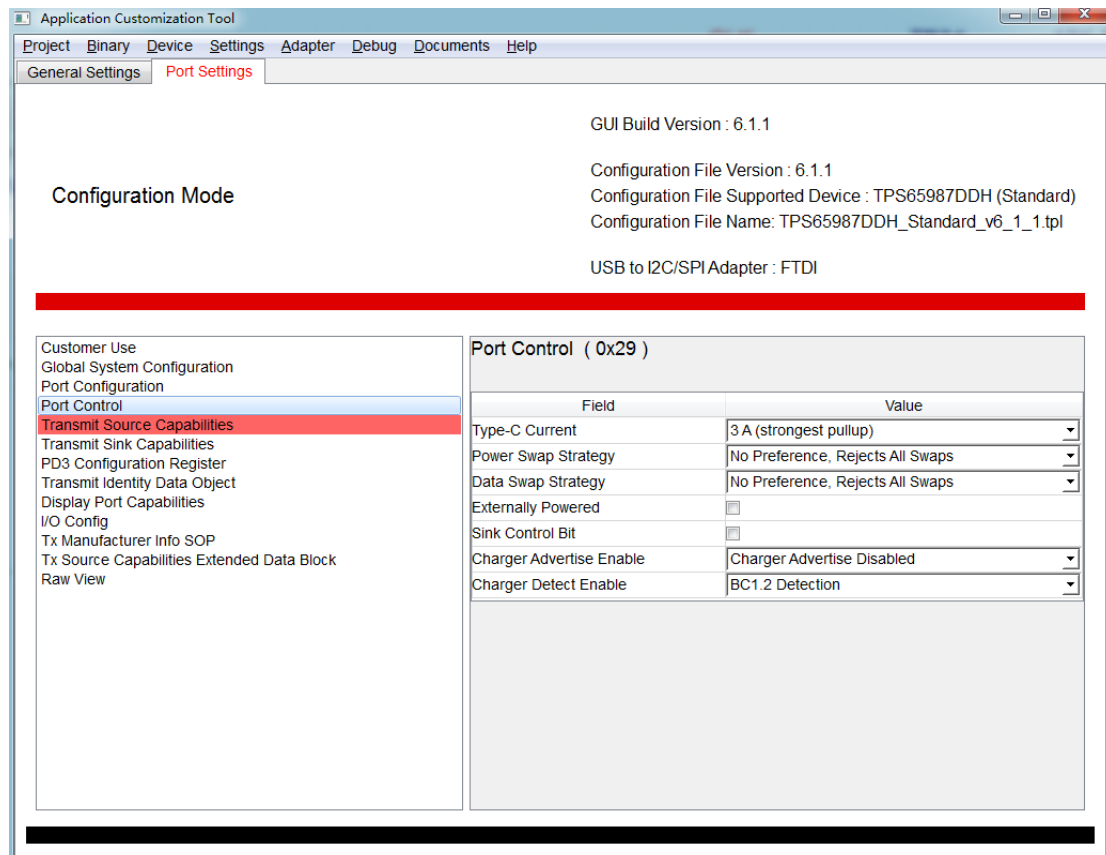
Global system configuration:



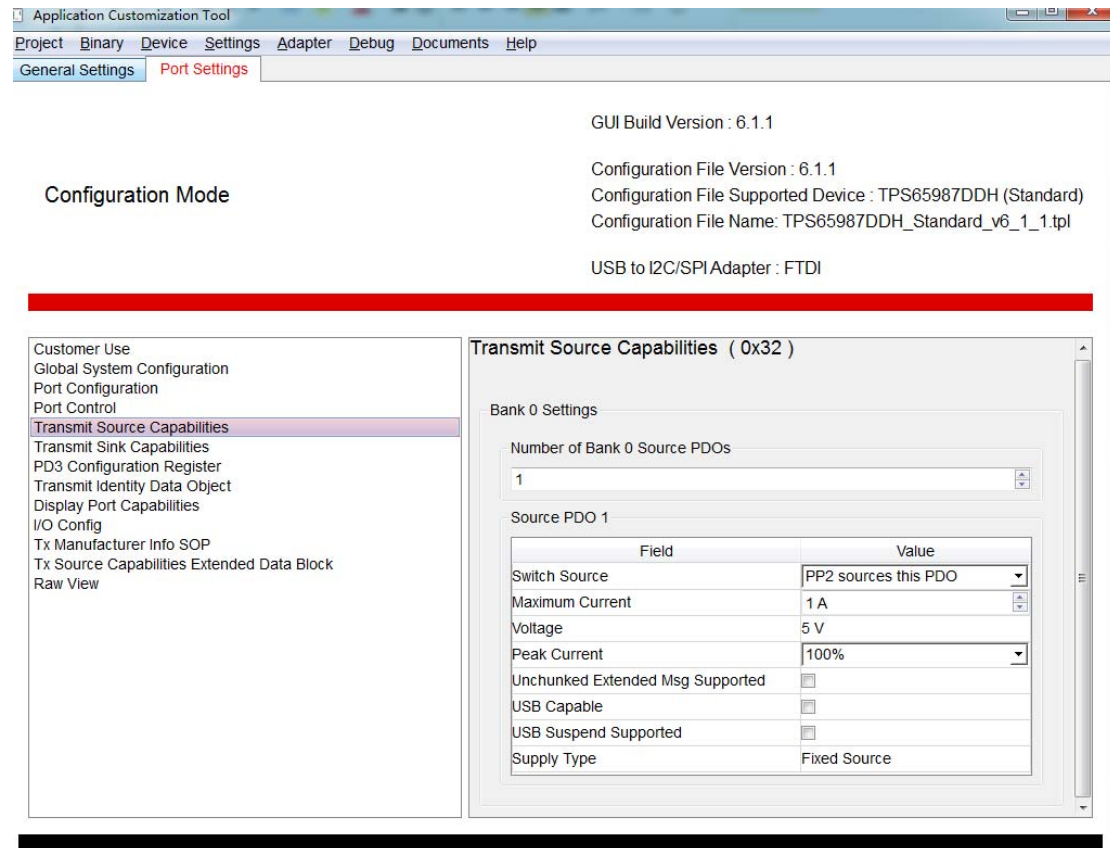
Port configuration:



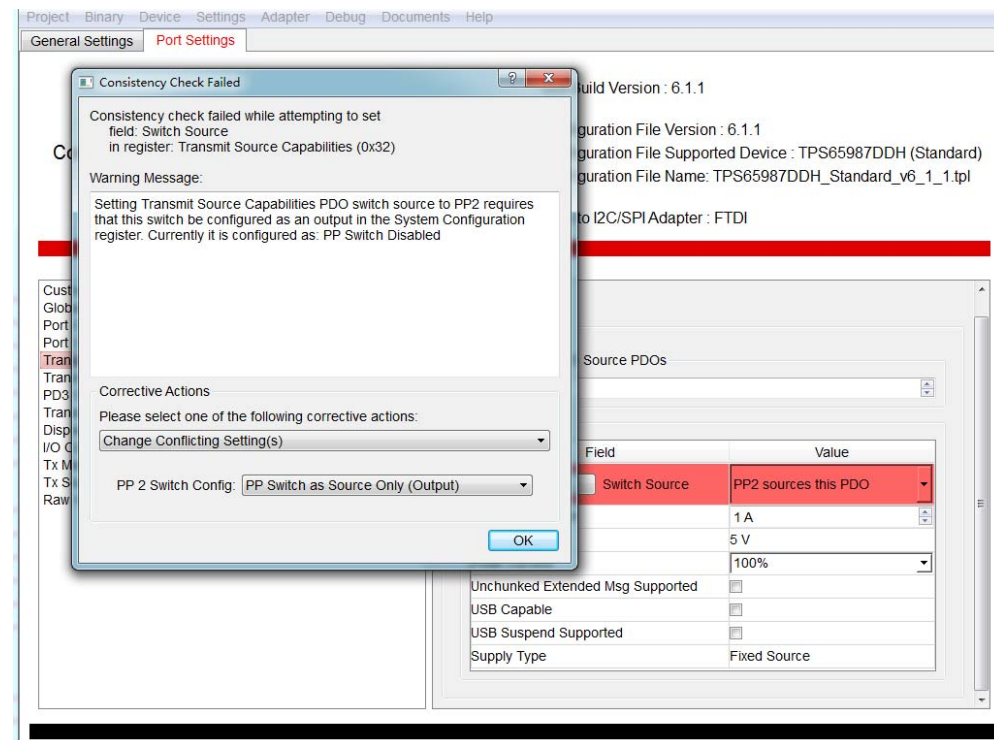
Port control:



Transmit source capabilities:

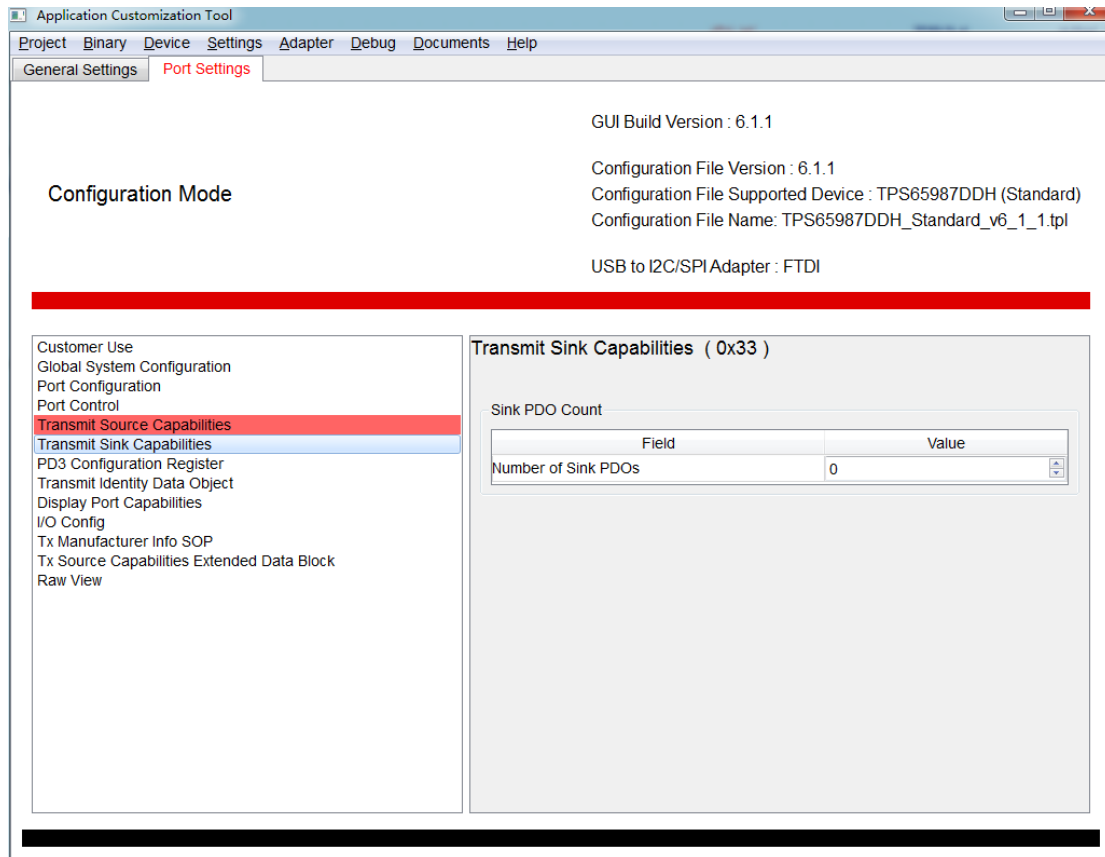


Where to set the pp switch enable?

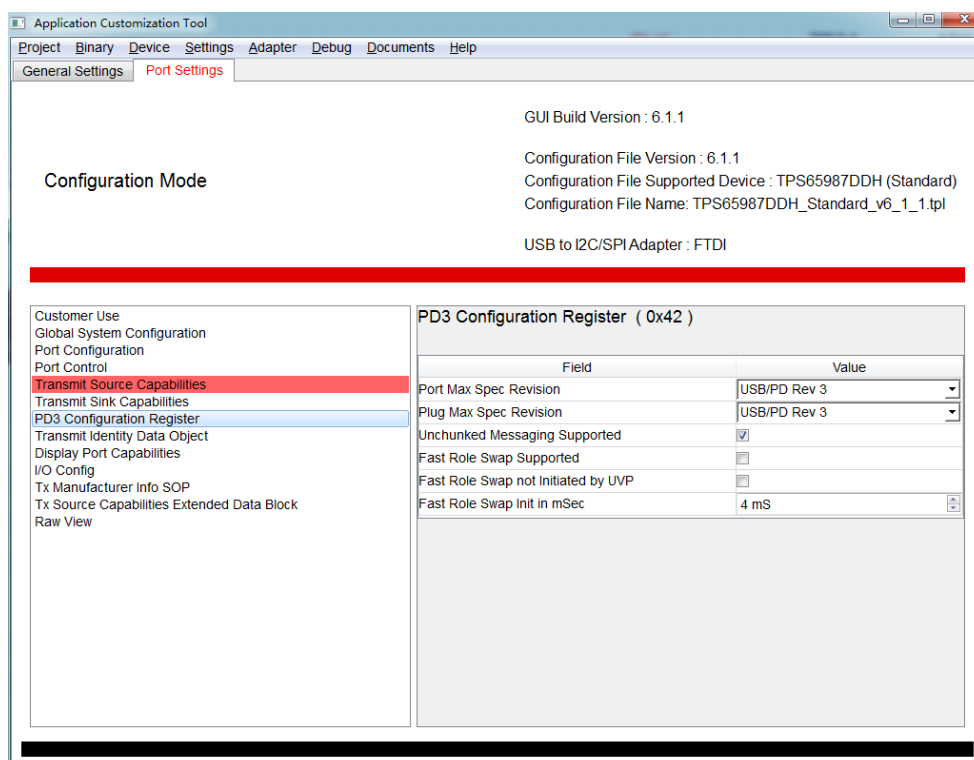


Transmit sink capabilities:

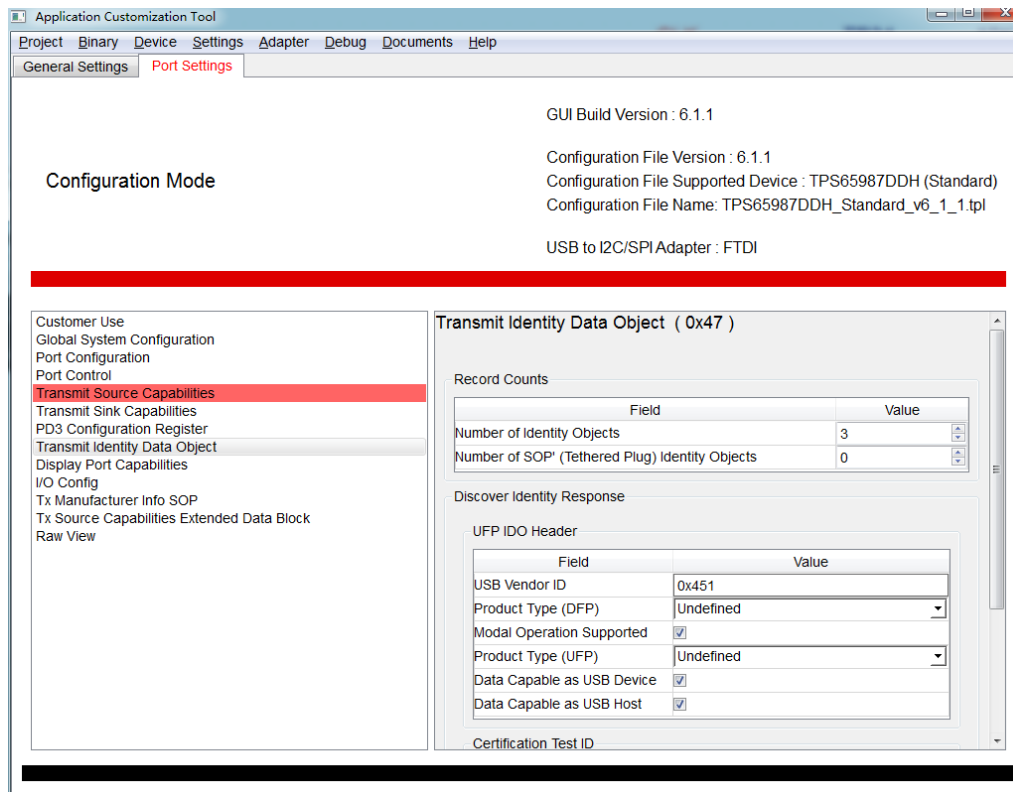
Our product didn't need Vbus power to PPHV.



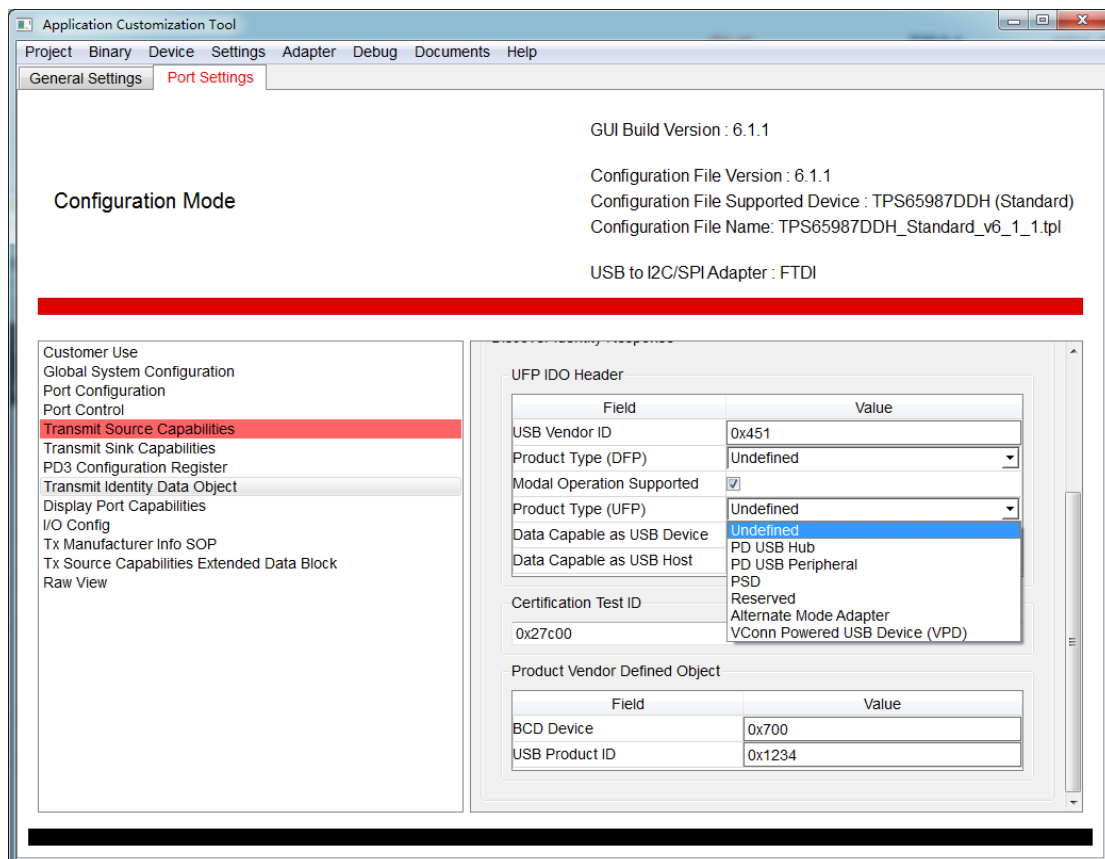
PD3 Configuration Register:



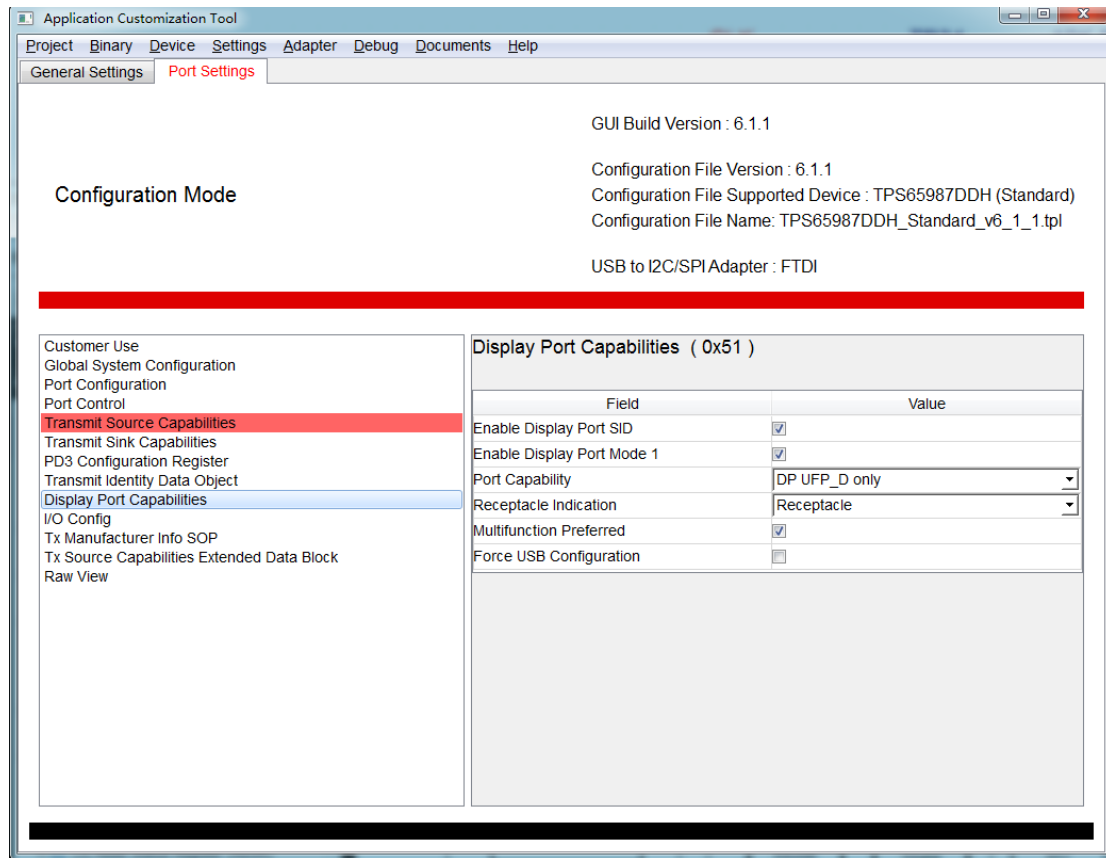
Transmit Identity data object:



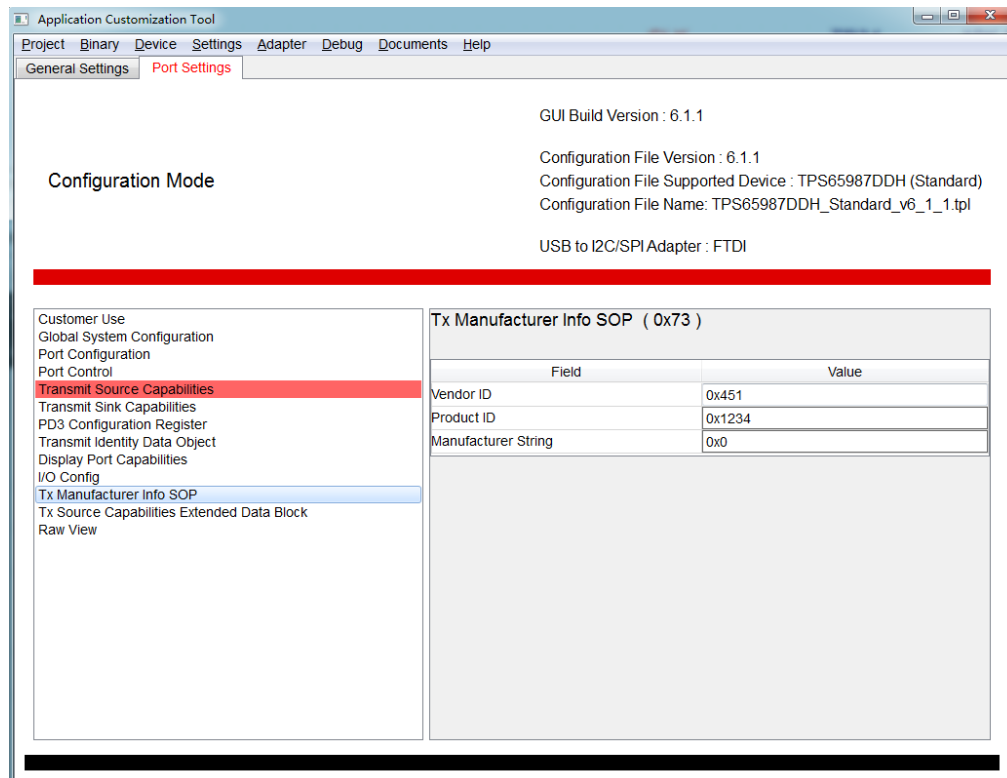
Which one should I choose?



Display port capabilities:



TX manufacturer info sop:



TX Source capabilities extender data block:

Application Customization Tool

Project Binary Device Settings Adapter Debug Documents Help

General Settings Port Settings

GUI Build Version : 6.1.1

Configuration File Version : 6.1.1

Configuration File Supported Device : TPS65987DDH (Standard)

Configuration File Name: TPS65987DDH_Standard_v6_1_1.tpl

USB to I2C/SPI Adapter : FTDI

Configuration Mode

Customer Use
Global System Configuration
Port Configuration
Port Control
Transmit Source Capabilities
Transmit Sink Capabilities
PD3 Configuration Register
Transmit Identity Data Object
Display Port Capabilities
I/O Config
Tx Manufacturer Info SOP
Tx Source Capabilities Extended Data Block
Raw View

Tx Source Capabilities Extended Data Block (0x77)

Identification

Field	Value
Vendor ID (VID)	0x451
Product ID (PID)	0x1234
Manufacturer ID (XID)	0x0

Version

Field	Value
Firmware Version	0x1
Hardware Version	0x1

Voltage Regulation

Field	Value
Load Step	150 mA/uSec Load Step
loc	25% IoC

Application Customization Tool

Project Binary Device Settings Adapter Debug Documents Help

General Settings Port Settings

GUI Build Version : 6.1.1

Configuration File Version : 6.1.1

Configuration File Supported Device : TPS65987DDH (Standard)

Configuration File Name: TPS65987DDH_Standard_v6_1_1.tpl

USB to I2C/SPI Adapter : FTDI

Configuration Mode

Customer Use
Global System Configuration
Port Configuration
Port Control
Transmit Source Capabilities
Transmit Sink Capabilities
PD3 Configuration Register
Transmit Identity Data Object
Display Port Capabilities
I/O Config
Tx Manufacturer Info SOP
Tx Source Capabilities Extended Data Block
Raw View

Compliance

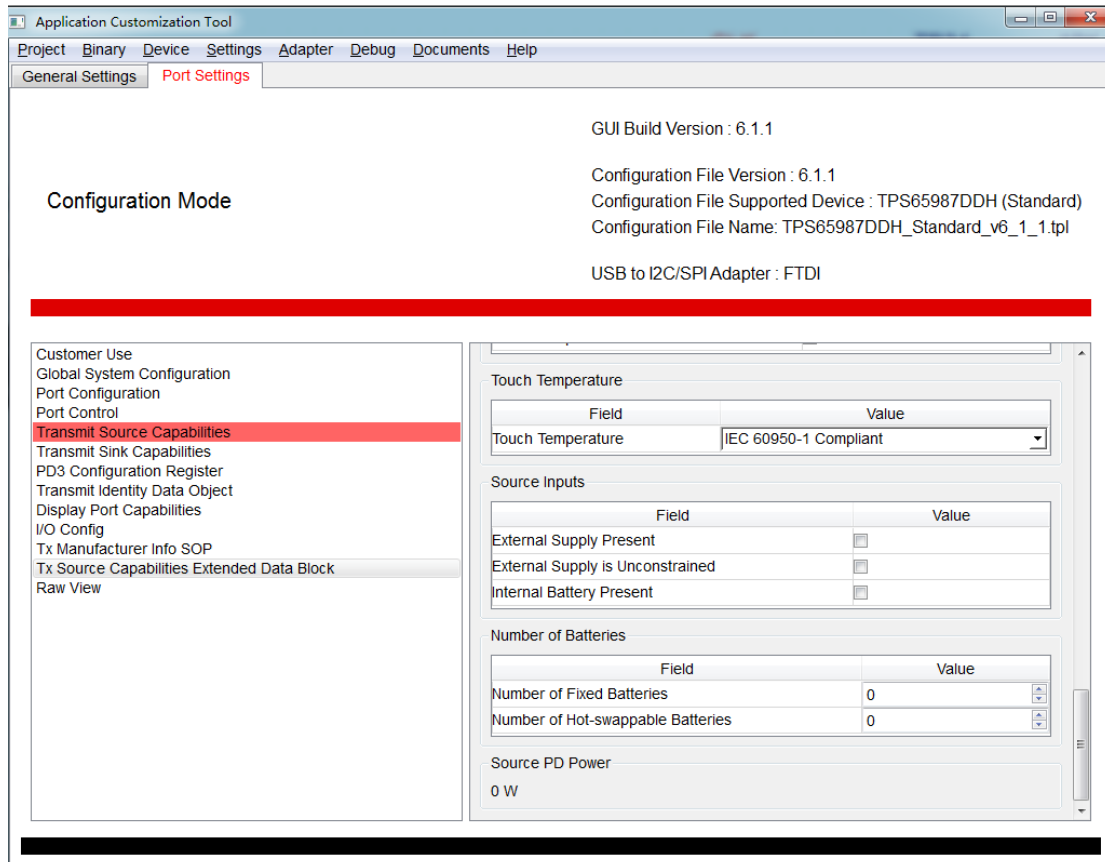
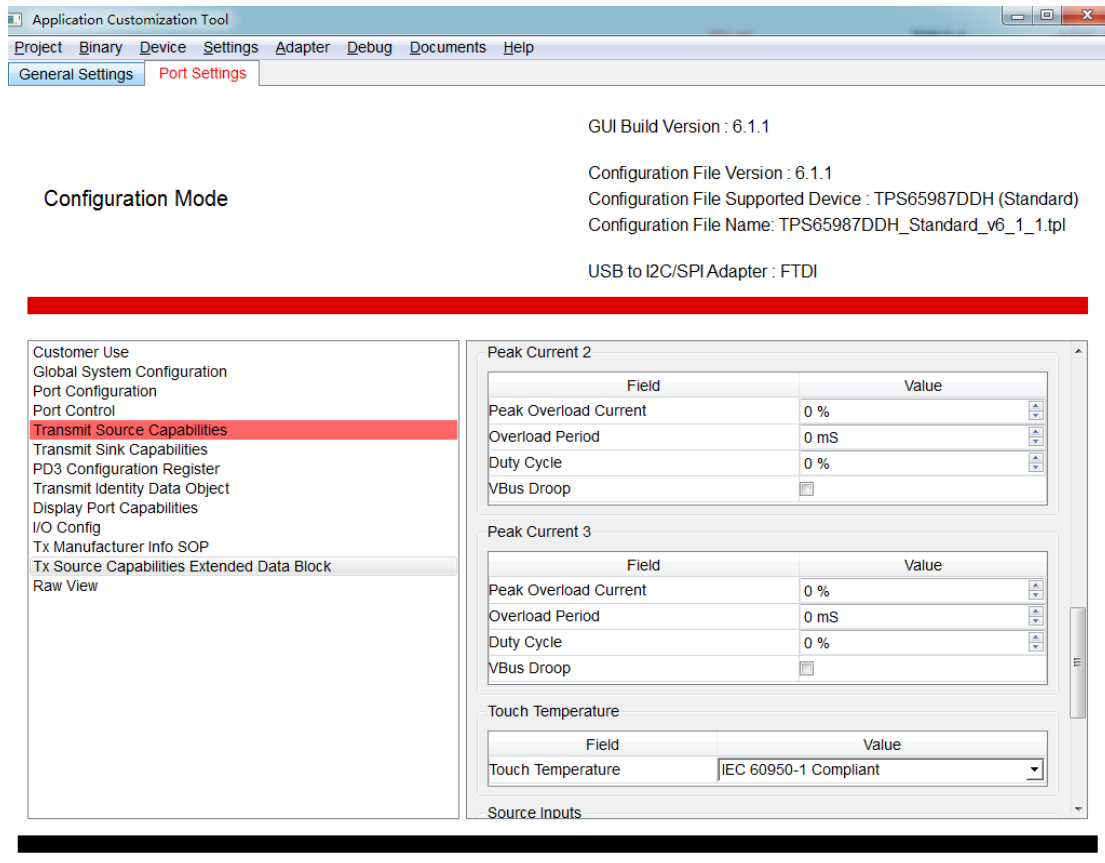
Field	Value
LPS Compliant	☐
PS1 Compliant	☐
PS2 Compliant	☐

Touch Current

Field	Value
Low Touch Current EPS	☐
Ground Pin Supported	☐
Ground Pin Intended for Protective Earth	☐

Peak Current 1

Field	Value
Peak Overload Current	0 %
Overload Period	0 mS
Duty Cycle	0 %
VBus Droop	☐



Raw view:

Application Customization Tool

Project

Binary

Device

Settings

Adapter

Debug

Documents

Help

General Settings

Port Settings

Configuration Mode

GUI Build Version : 6.1.1

Configuration File Version : 6.1.1

Configuration File Supported Device : TPS65987DDH (Standard)

Configuration File Name: TPS65987DDH_Standard_v6_1_1.tpl

USB to I2C/SPI Adapter : FTDI

Customer Use

Global System Configuration

Port Configuration

Port Control

Transmit Source Capabilities

Transmit Sink Capabilities

PD3 Configuration Register

Transmit Identity Data Object

Display Port Capabilities

I/O Config

Tx Manufacturer Info SOP

Tx Source Capabilities Extended Data Block

Raw View

Raw View

Mask	Register	Offset	Length	Value
0xf	0x6	0	8	0x0
0xf	0x27	0	14	0x7800000014140080000000080001
0xf	0x28	0	5	0x1dfb658c8
0xf	0x29	0	4	0x40110402
0xf	0x32	0	64	000000000001906400000001000000f9
0xf	0x33	0	57	000000000000009901905a3601905a00
0xf	0x42	0	4	0x41a
0xf	0x47	0	49	0000001234070020027c00c400045103
0xf	0x51	0	7	0x110c1c4503
0xf	0x55	0	1	0x4
0xf	0x5c	0	64	00000000000000000000000000000015
0xf	0x73	0	26	0x12340451
0xf	0x77	0	24	00000000000001010000000012340451