

TUSB1002A RGE | S-parameter model

High Speed Signal Conditioners
Jan 26, 2021

Setup Notes:

- TUSB1002A settings
 - Basic Redriver mode: MODE pin = “0”
 - 1200mV Linearity setting and DC Gain of 0dB.
 - CFG1 pin = “F”
 - CFG2 pin = “F”
- 24-pin RGE package
- Measuring TX diff return loss TX1, TX2
- Measuring RX diff return loss on RX1, RX2
- Bench board traces have been de-embedded at the VNA (Ag N5225A)
- Using RF power of -10dBm
- VNA start freq: 10MHz, stop freq: 20GHz, IF BW:1kHz
- Port order: (1,3,2,4)

TUSB1002A S-parameter test setup

