

Schematic Review Form

Customer/Project

TI Confidential - NDA Restrictions

Pin #	Name	Info	Violations	Description
15	VDD3V3	We recommend a 1uF cap and a 100nF cap.		3.3V Supply Voltage
3, 11	VDD1V8	We recommend a 1uF cap and a 100nF cap per VDD1V8 pin.		1.8V analog supply voltage
8, 18, A, B, C, D	GND	Okay.		Ground
5	RESETB	PU to 1.8V with a 100nF PD cap, okay.		Active low reset. Upon de-assertion of RESETB both repeaters will be enabled and be in eUSB2 default mode awaiting configuration from eDSPr or eUSBp.
1	CROSS	PD to GND, okay.		Indicates mux orientation. Used to specify orientation of internal Crossbar switch CROSS = Low: eUSB0 <-> USB A and eUSB1 <-> USB B CROSS = High: eSUB0 <-> USB B and euSB1 <-> USB A Sampled at de-assertion of RESETB
14	RSVD1	Okay.		Reserved pins connect 1kOhm pull up to 1.8V
13	RSVD2	Okay.		Reserved pins connect 1kOhm pull up to 1.8V
12	RSVD3	Okay.		Reserved pin leave it unconnected
2	EQ0	PD to GND, okay.		Compensation Level 0: EQ1=low EQ0=low Compensation Level 1: EQ1=low EQ0=high Compensation Level 2: EQ1=high EQ0=low Compensation Level 3: EQ1=low EQ0=high * Pins are sampled at RESETB de-assertion.
4	EQ1	PD to GND, okay.		Compensation Level 0: EQ1=low EQ0=low Compensation Level 1: EQ1=low EQ0=high Compensation Level 2: EQ1=high EQ0=low Compensation Level 3: EQ1=low EQ0=high * Pins are sampled at RESETB de-assertion.
16	eDP0	Okay.		eUSB2 port 0 D+ pin
17	eDN0	Okay.		eUSB port 0 D- pin
19	eDN1	Okay.		eUSB port 1 D- pin
20	eDP1	Okay.		eUSB port 1 D+ pin
10	DPA	What are the SH components for?		USB port A D+ pin
9	DNA	What are the SH components for?		USB port A D- pin
7	DNB	What are the SH components for?		USB port B D- pin

6	DPB	What are the SH components for?		USB port B D+ pin
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Comments