

TI Confidential - NDA Restrictions

Schematic Review Form

Customer/Project

Pin #	Name	Info	Violations	Description
15	VDD3V3	We recommend a 100nF and a 1uF cap for VDD3V3.		3.3V Supply Voltage
3, 11	VDD1V8	We recommend a 100nF cap per VDD1V8 pin and a 1uF cap. So there should be two 100nF		1.8V analog supply voltage
8, 18, A, B, C, D	GND	Okay.		Ground
5	RESETB	1.8V PU and 100nF PD cap, okay.		Active low reset. Upon de-assertion of RESETB both repeaters will be enabled and be in eUSB2 default mode awaiting configuration from eDSPr or eUSBp.
1	CROSS	Pulled to GND, okay.		Indicates mux orientation. Used to specify orientation of internal Crossbar switch CROSS = Low: eUSB0 <-> USBA and eUSB1 <-> USBB CROSS = High: eSUB0 <-> USBB and euSB1 <-> USBA Sampled at de-assertion of RESETB
14	RSVD1	1Kohm PU, okay.		Reserved pins connect 1kOhm pull up to 1.8V
13	RSVD2	1K0hm PU, okay.		Reserved pins connect 1kOhm pull up to 1.8V
12	RSVD3	floating, okay.		Reserved pin leave it unconnected

2	EQ0	PD to GND, okay. Please ensure this matched the compensation needed in the system.		Compensation Level 0: EQ1=low EQ0=low Compensation Level 1: EQ1=low EQ0=high Compensation Level 2: EQ1=high EQ0=low Compensation Level 3: EQ1=low EQ0=high * Pins are sampled at RESETB de-assertion.
4	EQ1	PD to GND, okay. Please ensure this matched the compensation needed in the system.		Compensation Level 0: EQ1=low EQ0=low Compensation Level 1: EQ1=low EQ0=high Compensation Level 2: EQ1=high EQ0=low Compensation Level 3: EQ1=low EQ0=high * Pins are sampled at RESETB de-assertion.
16	eDP0	Okay, 90.9KOhm pads should not be needed for a typical application. these can		eUSB2 port 0 D+ pin
17	eDN0	Okay, 90.9KOhm pads should not be needed for a typical application. these can		eUSB port 0 D- pin
19	eDN1	Okay, 90.9KOhm pads should not be needed for a typical application. these can		eUSB port 1 D- pin
20	eDP1	Okay, 90.9KOhm pads should not be needed for a typical application. these can		eUSB port 1 D+ pin
10	DPA	What are these components needed for on the DP/DN lanes? 		USB port A D+ pin
9	DNA	What are these components needed for on the DP/DN lanes? 		USB port A D- pin

7	DNB	What are these components needed for on the DP/DN lanes? 		USB port B D- pin
6	DPB	What are these components needed for on the DP/DN lanes? 		USB port B D+ pin

Comments