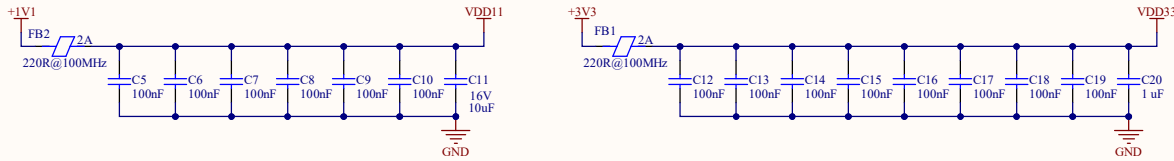
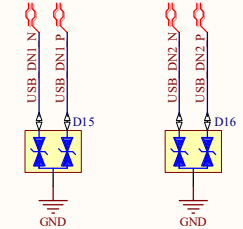


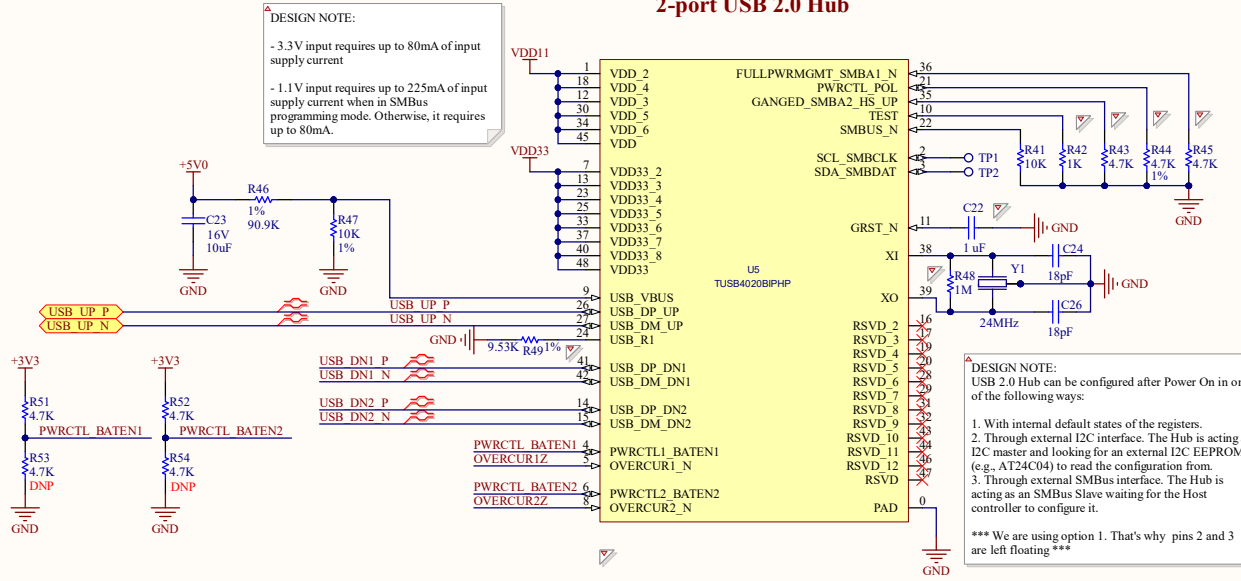
Bypass capacitors



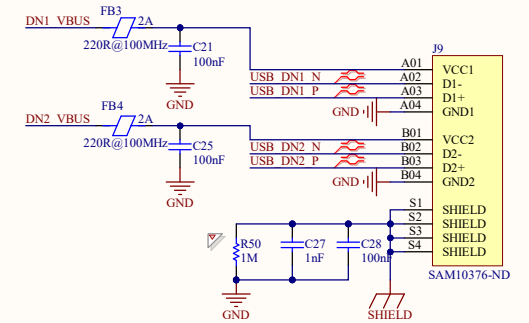
Transient Voltage Supression (TVS)



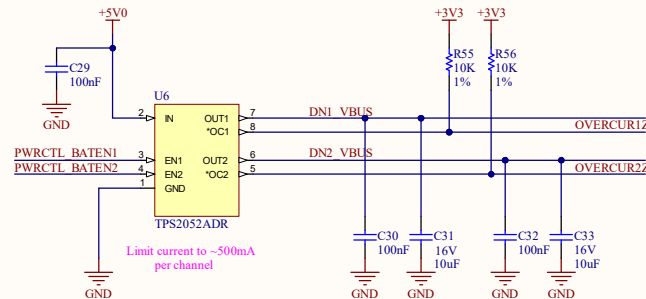
2-port USB 2.0 Hub



USB Type-A



Current-Limited Power Switch



LAYOUT NOTES:
Layout guidelines can be found in Section 11 of uSB 2.0 Hub datasheet
<https://www.ti.com/lit/ds/symlink/tusb4020b.pdf>
https://www.ti.com/lit/ds/symlink/tusb4020b.pdf#q1=1624457446845&ref_url=https%253A%252F%252Fwww.digkey.de%252F
Another useful Application Note:
<http://ww1.microchip.com/downloads/en/Appnotes/AN26.2-Application-Note-DS00001876C.pdf>

DESIGN NOTE:

PWRCTL_BATEx	Battery Charging Supported
LOW (PD)	Battery Charging NOT supported
HIGH (PU)	Battery Charging Supported

Those I/O pins (4, and 6), when acting as outputs, also control downstream power Switches. Contain internal PD resistor.

DESIGN NOTE:
Through GANGED input (pin 35), USB 2.0 Hub is configured to support individual power control. When overcurrent event is detected with the power switch, FAULTX output will signalize it to the Hub on OVERCURxZ input. As a consequence, the Hub will turn off power switch on appropriate channel through the PWRCTL_BATEx output.

Swiftlane, Inc. 65 Langton st. San Francisco, CA, 94132	Author: AUTHOR GOES HERE	Title: PROJECT TITLE
	Date: 11/9/2021	Schematic PN:
	PCB Number:	Revision: Revision
	Assembly PN:	Sheet 5 of 15