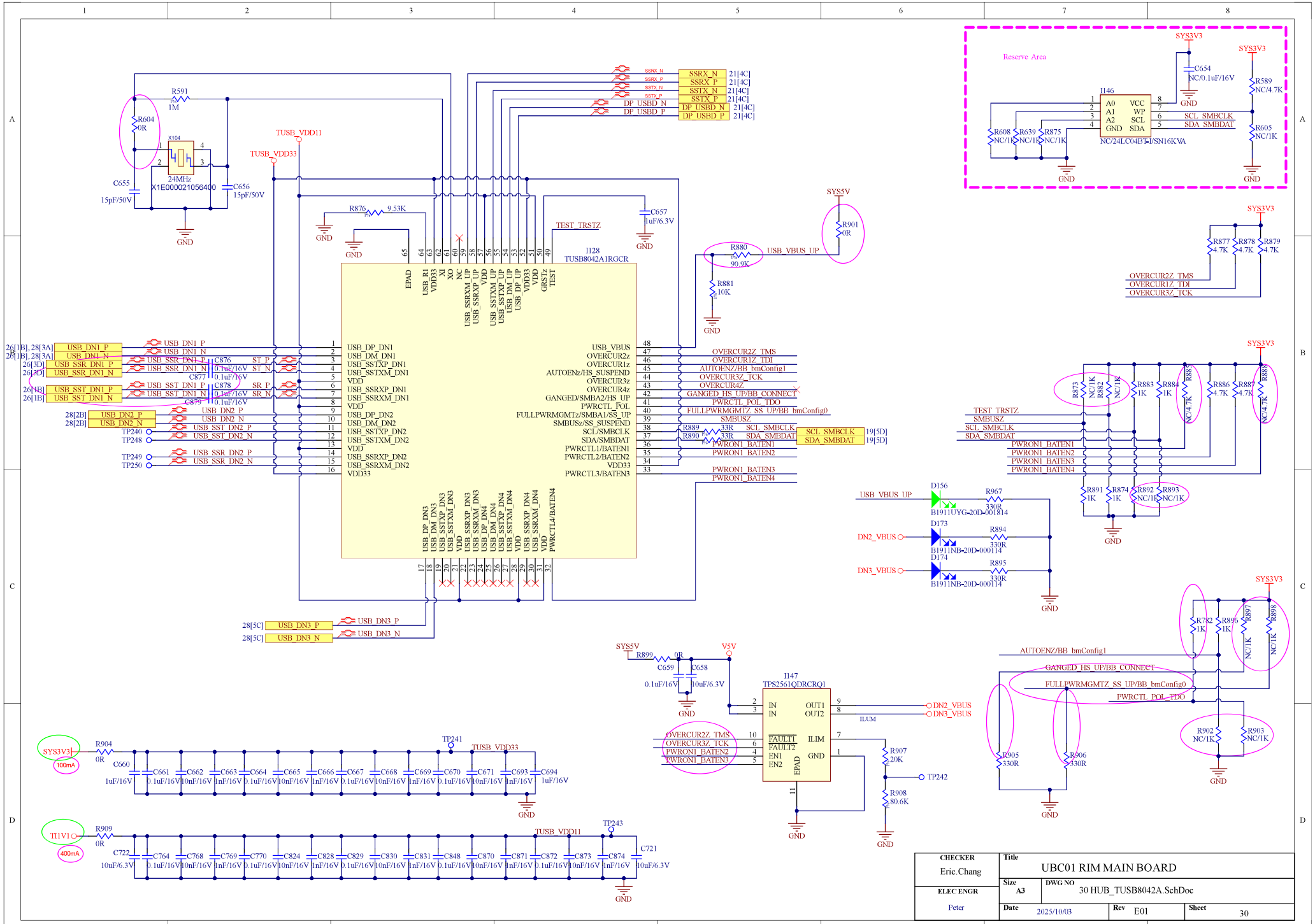
I128
TUSB8042A1RGCR
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I147
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Reserve Area
X104
24MHz
X1E000021056400
TUSB_VDD11
TUSB_VDD33
TEST_TRSTZ
USB_VBUS_UP
SYS5V
SYS3V3
V5V
TI1V1
D156
B1911UYG-20D-001814
D173
B1911NB-20D-000114
D174
B1911NB-20D-000114
DN2_VBUS
DN3_VBUS
GND
CHECKER
Eric.Chang
ELEC ENGR
Peter
Title
UBC01 RIM MAIN BOARD
Size
A3
DWG NO
30 HUB_TUSB8042A.SchDoc
Date
2025/10/03
Rev
E01
Sheet
30