USB Host REAR

TUSB4041IPAPR

USB Host 4

USB Host 3

USB Host Front

30 cm USB cable to receptacle

✗EL_33 CHIRP Timing Response Reference: USB 2.0 Specification, Section 7.1.7.5.
FAIL   Test Description: EL_33: Downstream ports start sending and alternating sequence of CHIRP K's and CHIRP J's within 100us after device CHIRP K stops.
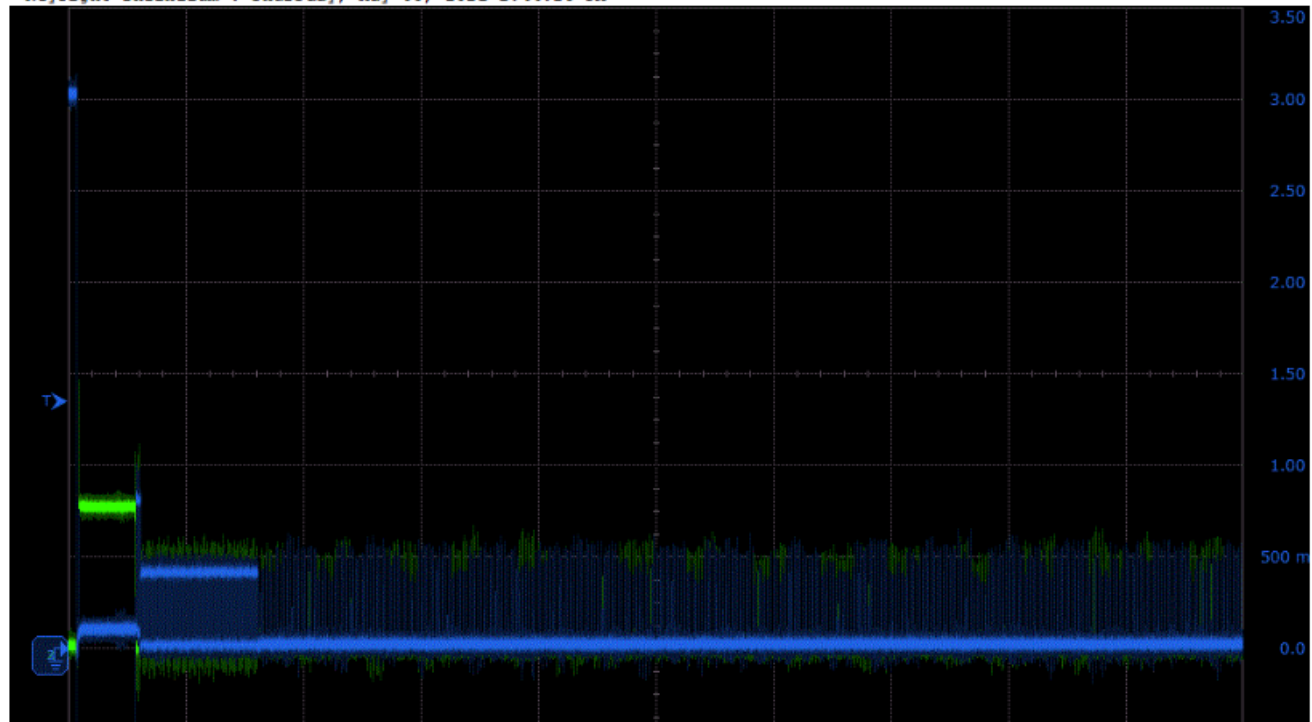Pass Limits:[1 ns to 100.000 µs]CHIRP Response Time137.174 µs
Result Details:
Result Details
EL_33:Port Under TestPort1
Trial 1
Trial 1: CHIRP Response Time

USB Host Front CHIRP fail

✘EL_33 CHIRP Timing Response Reference: USB 2.0 Specification, Section 7.1.7.5.
 FAIL   Test Description: EL_33: Downstream ports start sending and alternating sequence of CHIRP K's and CHIRP J's within 100us after device CHIRP K stops.
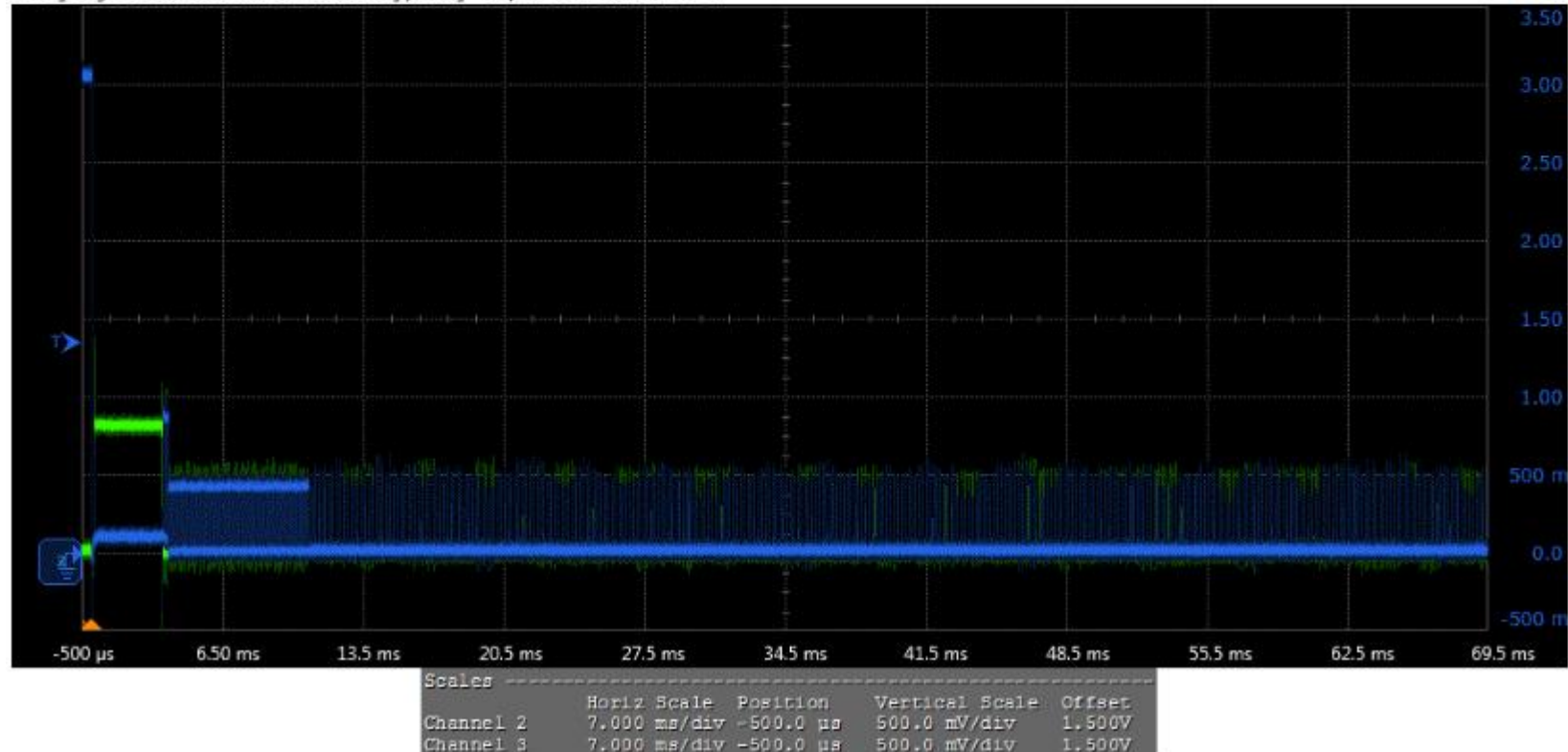Pass Limits:[1 ns to 100.000 µs]CHIRP Response Time138.705 µs
Result Details:

Result Details

EL_33:Port Under TestPort1
Trial 1
Trial 1: CHIRP Response Time

Keysight Infiniium : Thursday, May 05, 2022 1:30:16 PM

USB Host 3 CHIRP fail

✗EL_21 Sync Field Length Test Reference: USB 2.0 Specification, Section 8.2.

FAIL   Test Description: EL_21: The SYNC field for all transmitted packets (not repeated packets) must begin with a 32 bit SYNC field.

Pass Limits:[MinSync s to 67.700 ns]Host Sync Field Length96.051 ns
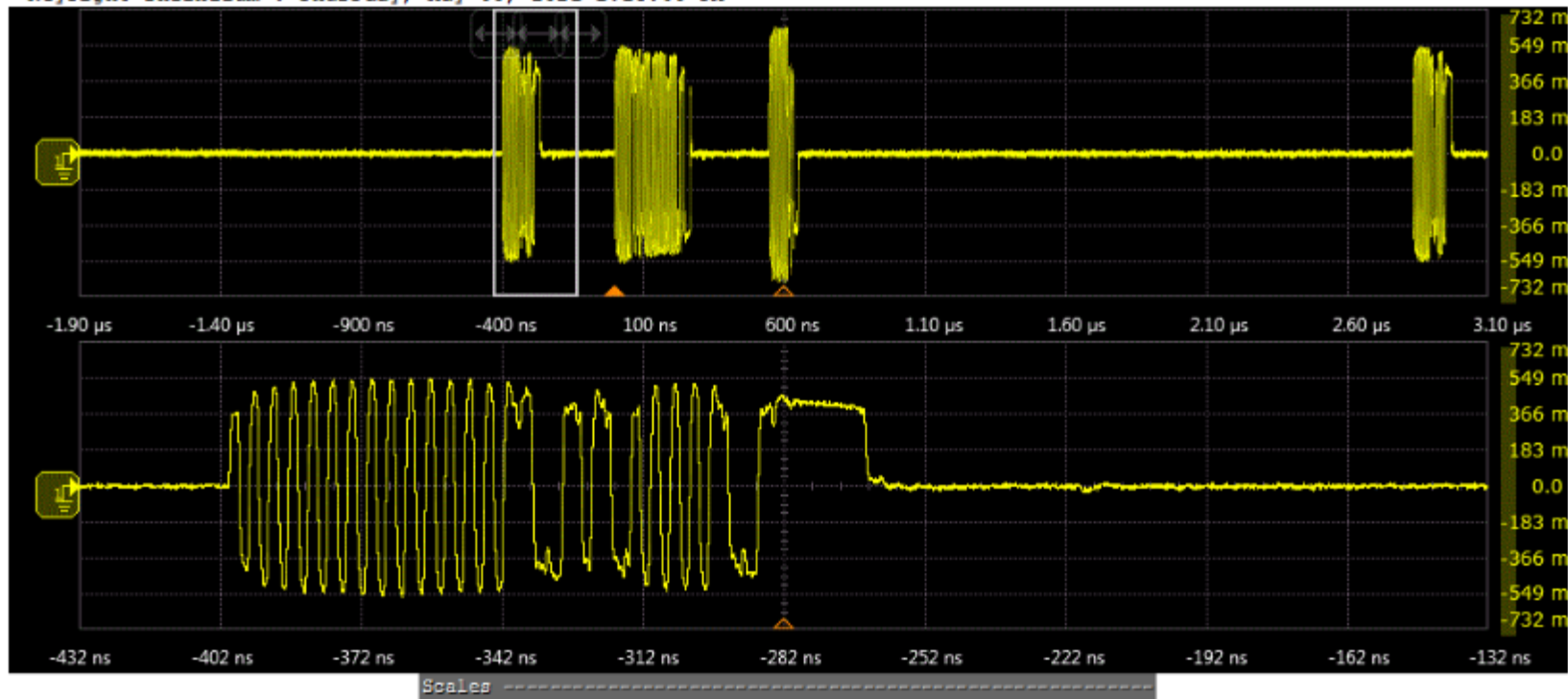
Result Details:

Result Details

Packet 1-4.021209e-007**Packet 2**-5.730220e-011**Packet 3**5.497242e-007**SOF Packet Start**-400.164 ns**SOF Packet End**-331.249 ns**SOF Sync Field**(See image)**EL_21:SOF Sync Field**68.915 ns**SOF Sync Field Bits**33.13 bits**EL_21:Port Under Test**Port1**Data Packet Start**-176 ps**Data Packet End**95.875 ns**Data Sync Field**(See image)**EL_21:Data Sync Field**96.051 ns**Data Sync Field Bits**46.11 bits**PassLimit Min (MinSync)**57.288 ns

Trial 1

Trial 1: SOF Sync Field

USB Host Rear Sync Fail