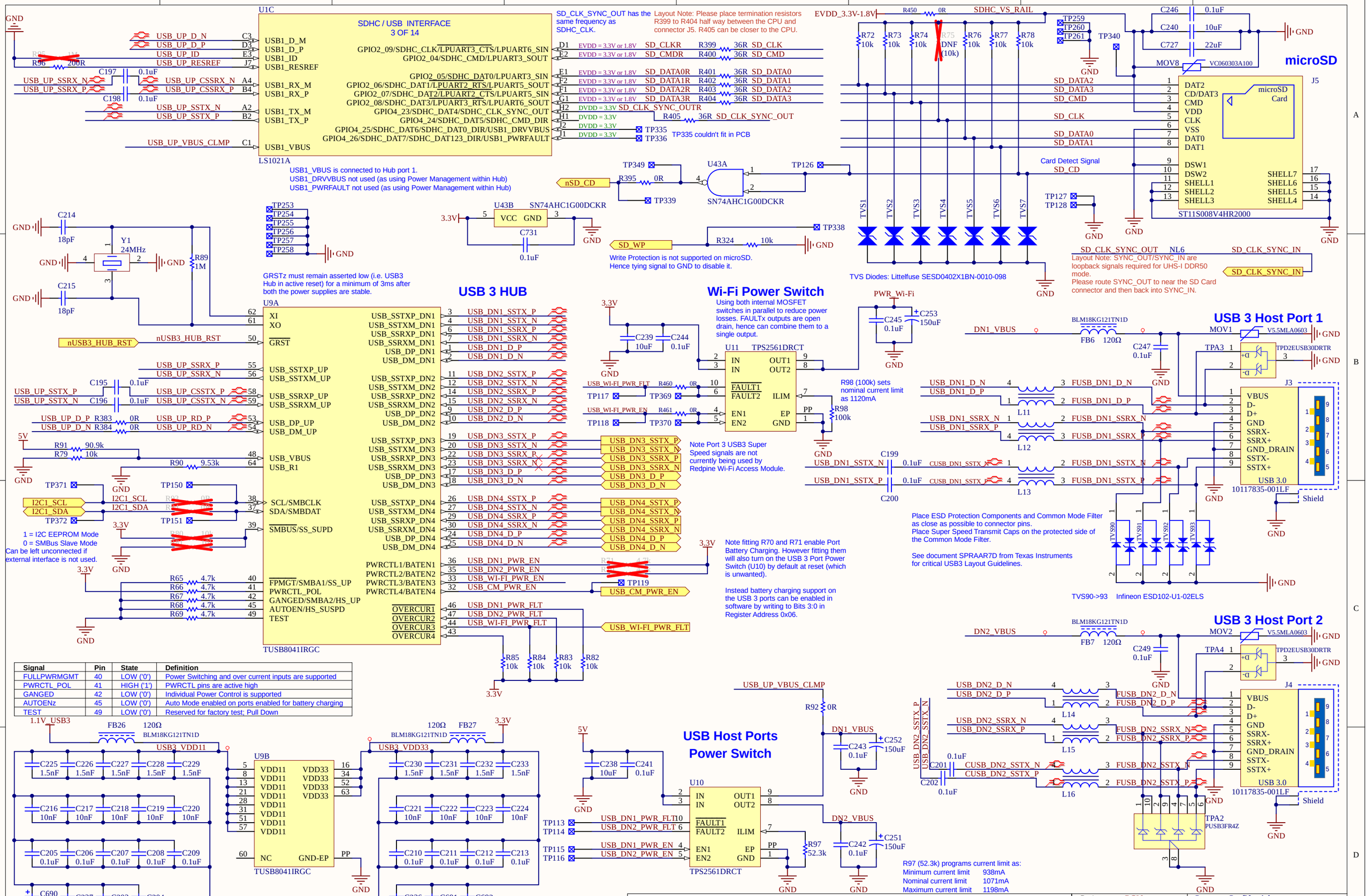

Signal	Pin	State	Definition
FULLPWRMGMT	40	LOW ('0')	Power Switching and over current inputs are supported
PWRCTL_POL	41	HIGH ('1')	PWRCTL pins are active high
GANGED	42	LOW ('0')	Individual Power Control is supported
AUTOEN2	45	LOW ('0')	Auto Mode enabled on ports enabled for battery charging
TEST	49	LOW ('0')	Reserved for factory test; Pull Down

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Project:	RC20
Module:	REL20
Assembly:	Main Board
Part Number:	RC20-3001
Modified Date:	2/24/2019
Size: A3	Print: 2/24/2019
File: 08 - USB 3 Hub & SDHC.SchDoc	

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ABN 21 099 412 807

Title: ***USB3 Hub & microSD***

Variant: ***Standard*** Rev: ***1.0***

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