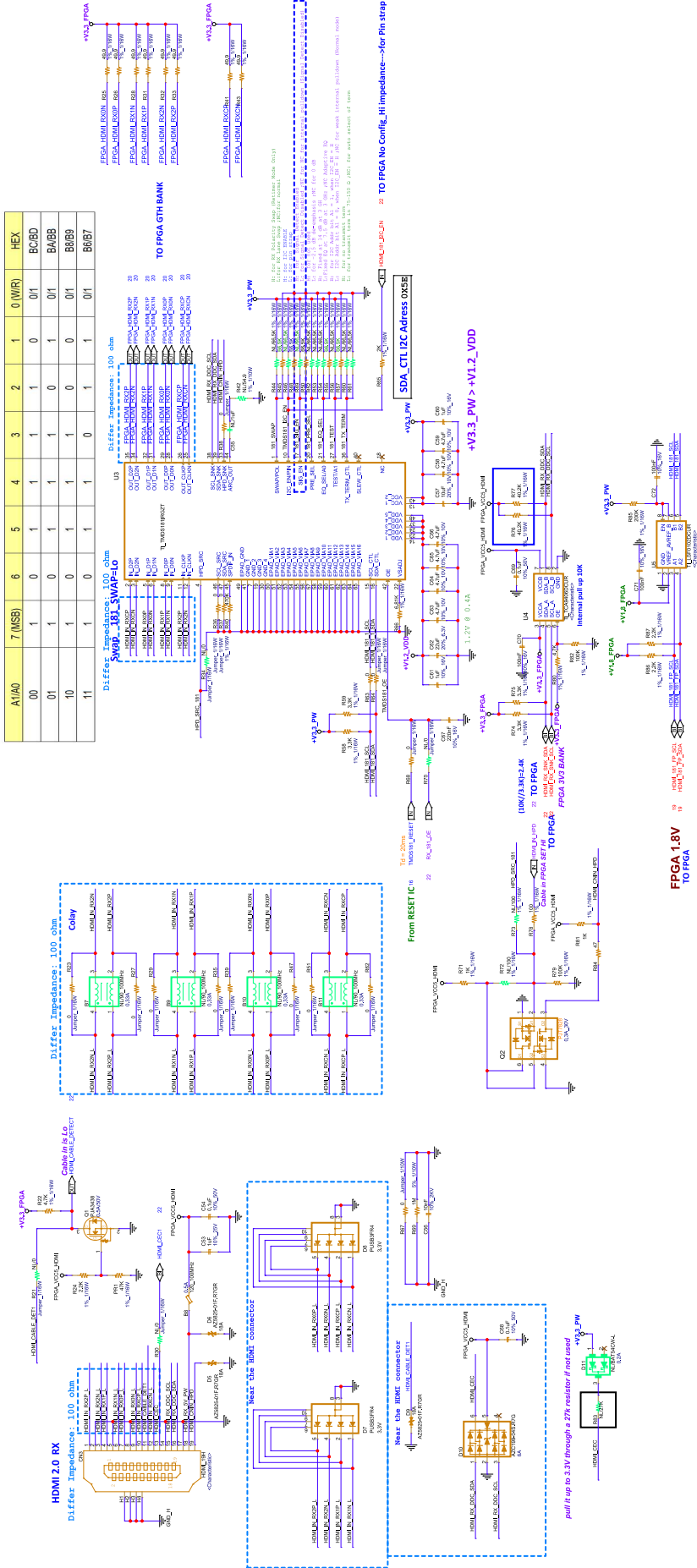


HDMI 2.0 INPUT_RX1

Figure 31. TMS181 IC Device Address Description

A1/A0	7 (MSB)	6	5	4	3	2	1	0 (WR)	HEX
00	1	0	1	1	1	1	0	01	BCBD
01	1	0	1	1	1	0	1	01	BABB
10	1	0	1	1	1	0	0	01	B8B9
11	1	0	1	1	1	0	1	01	B8B7



HDMI 2.0 INPUT_RX2

Figure 31. TMS1811 IC Device Address Description

A[1:0]	7 (MSB)	6	5	4	3	2	1	0 (VRR)	HEX
00	1	0	1	1	1	1	0	01	BCBD
01	1	0	1	1	1	0	1	01	BABB
10	1	0	1	1	1	0	0	01	B8B9
11	1	0	1	1	1	0	1	01	B8B7

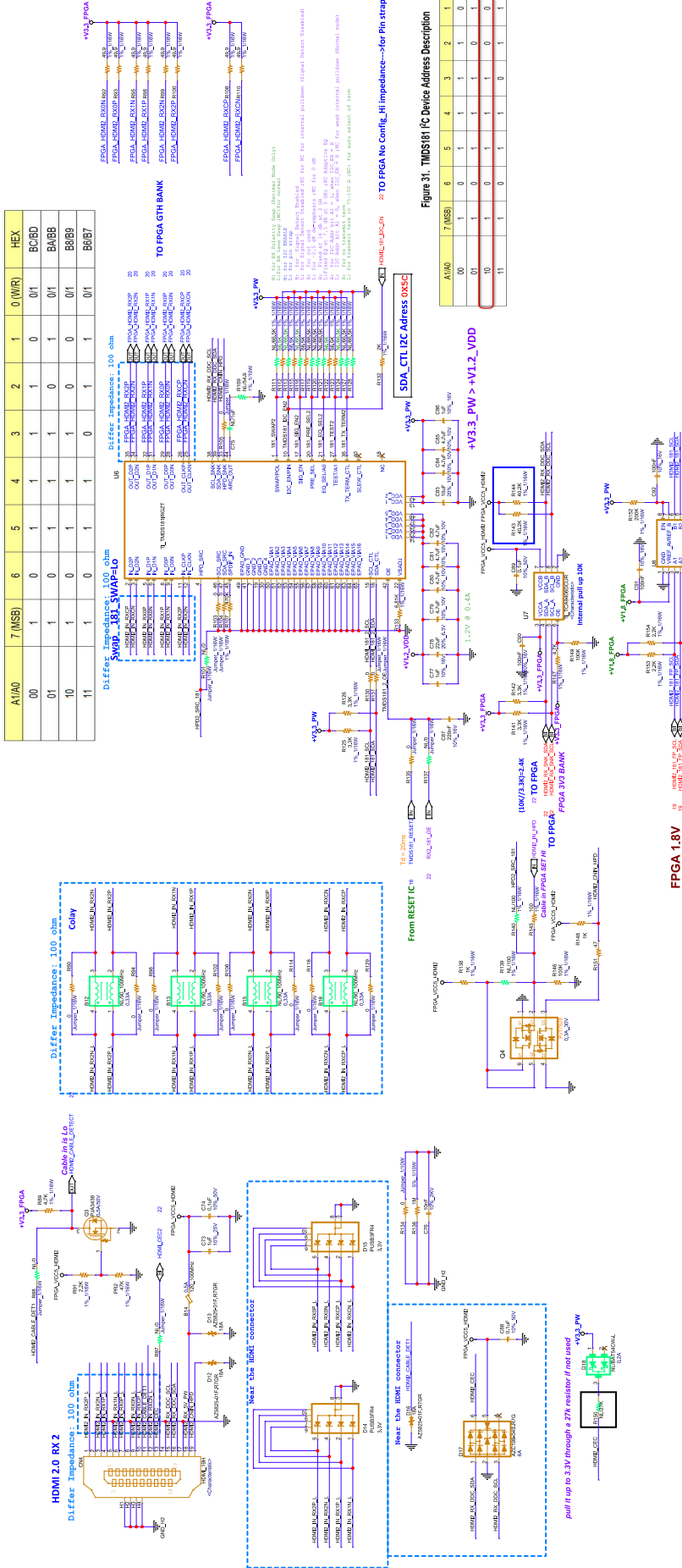


Figure 31. TMS1811 IC Device Address Description

A[1:0]	7 (MSB)	6	5	4	3	2	1	0 (VRR)	HEX
00	1	0	1	1	1	1	0	01	BCBD
01	1	0	1	1	1	0	1	01	BABB
10	1	0	1	1	1	0	0	01	B8B9
11	1	0	1	1	1	0	1	01	B8B7

FPGA 1.8V TO FPGA

pull it up to 3.3V through a 27k resistor if not used

