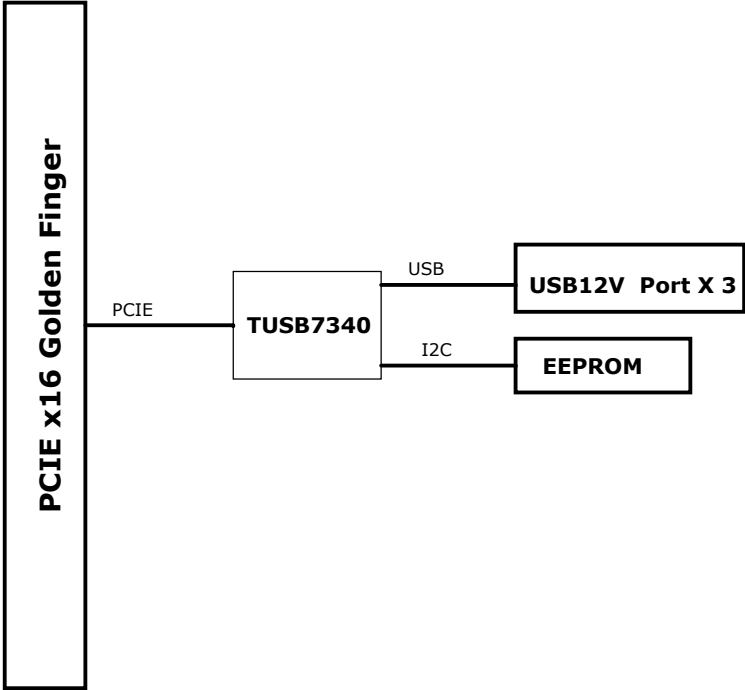
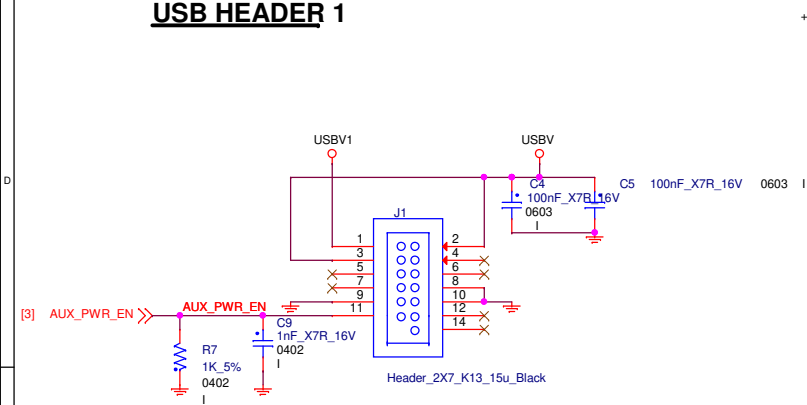


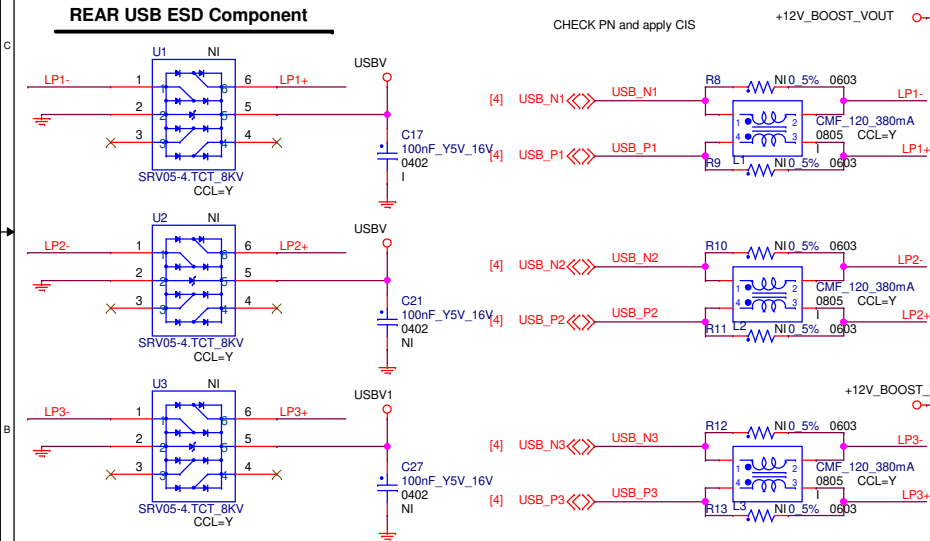


## USB HEADER 1

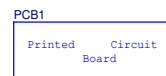


have apply CIS footprint & -H PN ;Check Pin Define

## REAR USB ESD Component



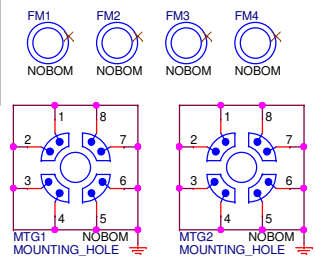
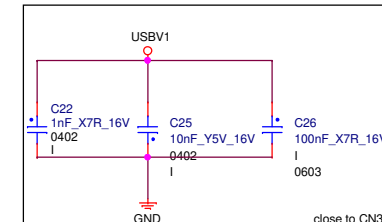
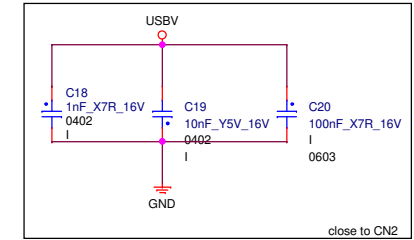
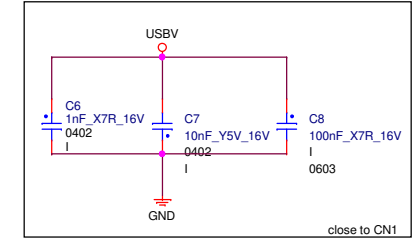
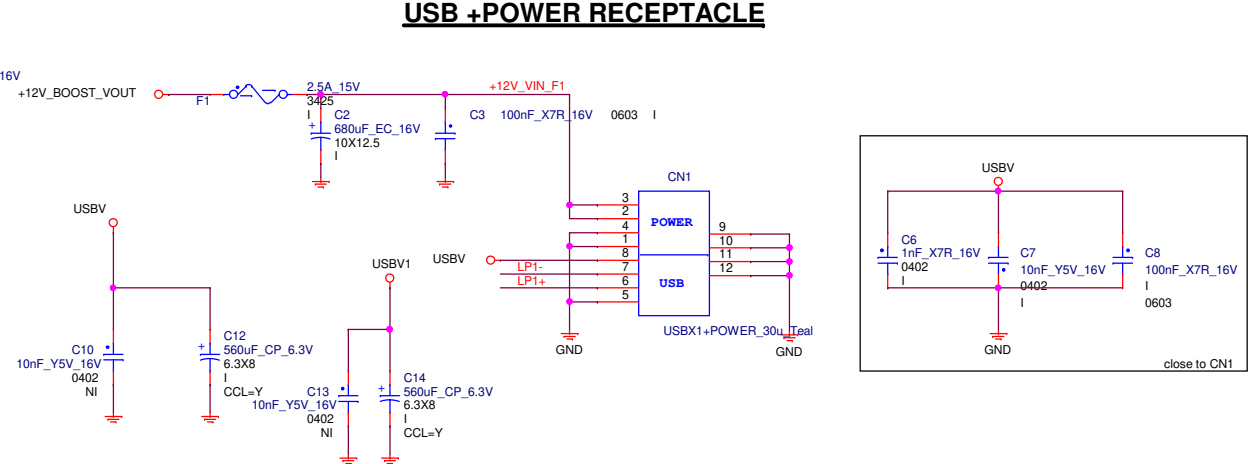
## 4 PINS POWER CONNECTOR



PCB1  
Printed Circuit Board  
PCB\_USB\_12V\_REV:A  
CCL=Y  
4-Layer PCB, Color With Green Soldermask, White Silkscreen, 3.452X2.689 inch, OSP, Rev.A, ROHS, HF  
010196V03-575-H

PCB dimension need confirm

## USB +POWER RECEPTACLE

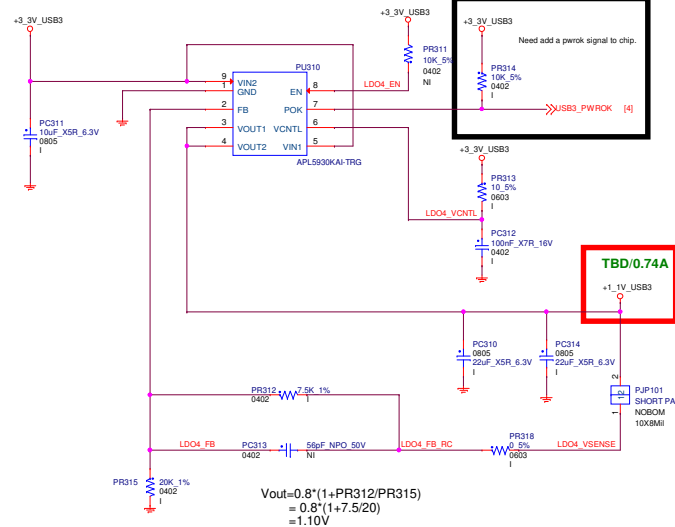


|          |                          |                            |   |                      |   |
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|          |                          | <b>HEWLETT<br/>PACKARD</b> |   | <b>HP RESTRICTED</b> |   |
| DRAWN BY |                          | Foxconn                    |   |                      |   |
| Title    |                          | USB12V CONN                |   |                      |   |
| Size     | Document Number          |                            |   | Rev                  |   |
| Custom   | 754495-000               |                            |   | A                    |   |
| Date:    | Thursday, March 20, 2014 | Sheet                      | 2 | of                   | 7 |



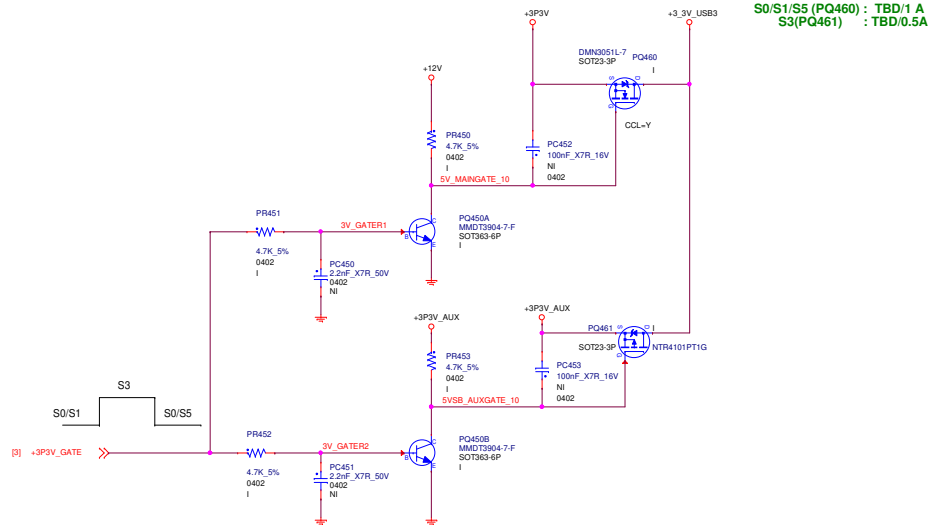


# +1\_1V\_USB3



Note: net-name:LDO4

# +3\_3V\_USB3





## EVT1

2013/10/14

P03 Change U63 from Microchip\_24LC01BTI/SN to ST\_M24C01-WMN6TP for Microchip\_24LC01BTI/SN is not HF.

2 Change C22/C25/C26 from power net USBV to USBV1 .

2013/10/15

P01 Change C2/C15/C23 from RUBYCON\_16MBZ470MHFCCR8X11.5 to RUBYCON\_16ZLH470MHFCCR8X11.5 for EOL.

2013/10/16

P03 Change U63 from ST\_M24C01-WMN6TP to ST\_M24C02-WMN6TP for low cost.

## DVT

2013/11/11

P01 Change PCB1 from T-APOLLO-USB-H to 010196V01-575-H.

P03 Change R26/R27 from 10Kohm to 220ohm and Change R29/R30 from 15Kohm to 330ohm for potential overcurrent issue.

2013/11/25

P02 Add R18,R4,R5,R6,R2,R3,R1,Q1,Q2,C40,C41 for TI\_TUSB7340 power consumption issue from G3 to S5.

2013/11/26

P02 Add R31,Q3 for TI\_TUSB7340 power consumption issue from G3 to S5.

2013/11/27

P03 Add CR13,CR14 for potential leakage from USBV/USBV1 to +3\_3V\_USB3 in S5.

2013/11/27 DC Power

P04:Change PU310 from APL5620QBI-TRG to APL5930KAI-TRG

Combin PQ450 } PQ451 form MMBT3904LT1G to Dual NPN BJT MMDT3904-7-F PQ450AB

2013/11/28

P03 Change U61 from TI\_TUSB7340RKM to TI\_TUSB7340RKMR for manufacture.

2013/12/02

P03 Change R5637 from I to NI and Add R32,R33,Q7 for petential leakage from wake# to +3\_3V\_USB3 in s5 .

P03 Change R5622 from I to NI for low cost .

2013/12/04 DC Power

P05 Add 12V\_BOOST CIRCUIT for 12V stabilization.

2013/12/04

P02 Add bleed off circurt for +12V\_BOOST\_VOUT.

2013/12/05

P01 PCB size enlarge from 2.552\*2.689 to 3.452\*2.689

2013/12/10 DC Power

P05 :Change PCE906 } PCE907 } PCE908 from I to NI

Change PCE956 } PCE957 } PCE958 from NI to I to get good performance and layout.

2013/12/11 DC Power

P05 :Add PD960,PD961 for IC's duty request .

2013/12/11

P01 :Change J1,F1,F2,F3 from CCL N to CCL Y.

2013/12/14

P01 :Add P01 block diagram.

2013/12/11 DC Power

P06 :Change PD960 from B540C-13-F to S8KC-13;

Change PD960 from NI to I,Change PD961 from I to NI;

Change PR952 from 210K\_1% to 143K\_1%,Change PR953 from 24K\_1% to 16.2K\_1%.

Change PR963 from 249K\_1% to 499K\_1% to fix frequency to 100KHz.

## PVT

2014/01/14

P01 Change PCB1 from 010196V01-575-H to 010196V02-575-H.

P01 Change SCH Version from X2 to A.

2014/02/06 DC Power

P06 :Add PR927 } PR928 } PR929 0\_5% 1206 colay with PD960 and PD961.

Change PD960 from I to NI for thermal optimize.

Change PR952 from 143K\_1% to 182K\_1% ,Change PR953 from 16.2K\_1% to 20.5K\_1% for vout regulate

2014/02/06

P01 Change SCH Version from A to X3.

2014/02/10 DC Power

P06 Delete Colay component PR927 } PR928 } PR929 } PD960 PD961 } PD950 for layout placement

2014/02/12

P02 Change c2/c15/c23 from 470uf to 680uf for power droop.

And del c11 to give more space for c2/c15/c23 for layout reason.

P02 Change R38 from 1.05k\_1% to 0.75K\_1% for better bleed off.

2014/02/18

P04 Change R24 from 9.09k\_1% to 8.06K\_1% for USB eye diagram issue.

## MVB

2014/03/13

P01 Change PCB1 from 010196V02-575-H to 010196V03-575-H.

P01 Change SCH Version from X3 to A.

|                                                                                       |                               |                            |  |                                                                                                                                                                                                              |                 |
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| Title <b>Change List</b>                                                              |                               |                            |  |                                                                                                                                                                                                              |                 |
| Size<br>Custom                                                                        | Document Number<br>754495-000 |                            |  |                                                                                                                                                                                                              | Rev<br><b>A</b> |
| Date:                                                                                 | Thursday, March 20, 2014      |                            |  | Sheet                                                                                                                                                                                                        | 7 of 7          |