

Titel

SCHEMATIC, GCS-DOCK , V1.0

File			
SYSGRATION LTD.			
Size	Document Number		Rev
	Battery Dock - Title		8.0
Date	Monday, June 15, 2026	Sheet	1 of 8
APPROVED BY		CHECKED BY	DESIGNED BY
Jason Cheng		Bruce Lee	Tony Li

Revision History

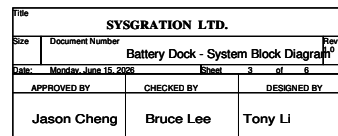
Revision History

Schematic changes history

MCN	REV	Update Date	Changed By	Change description
	V1.0	6/4/2026	Bruce Lee	.SP1 Initial

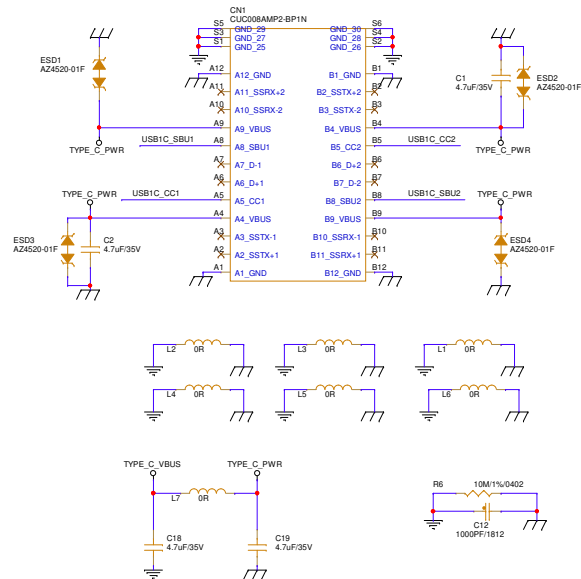
File			
SYSGRATION LTD.			
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Jason Cheng		Bruce Lee	Tony Li

Details System Block Diagram [TBC]



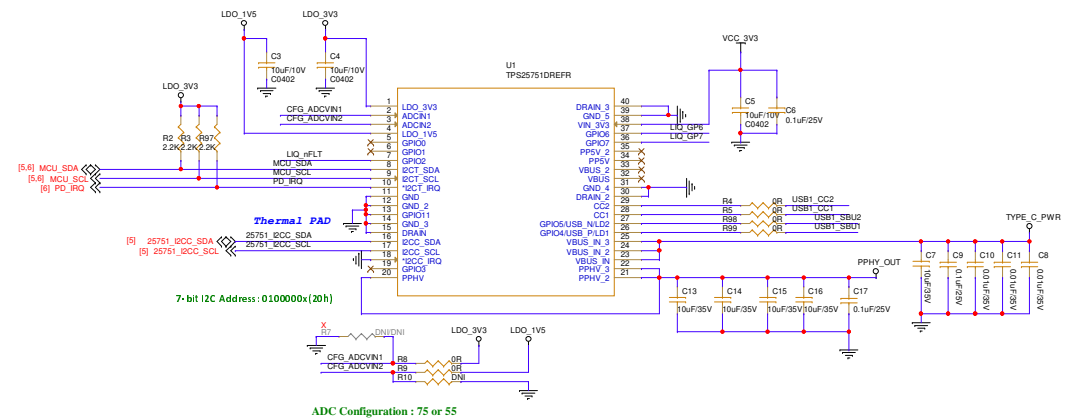
PD Controller

MAIN TYPE-C Port

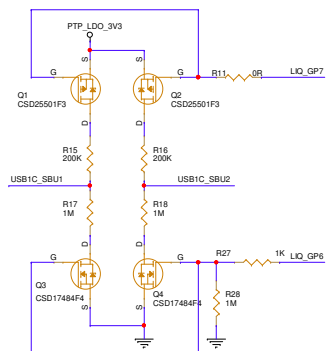


CC Logic and USB3.0 Switch

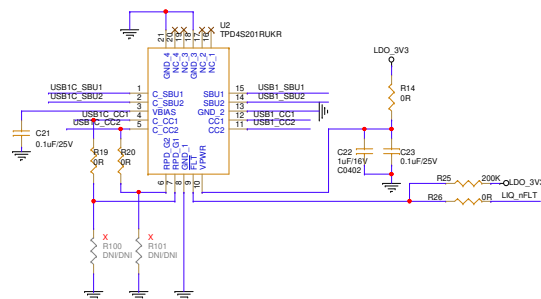
Configuration Truth Tables								
ADCINx Switches					Device Configuration			
1:	2:	3:	4:	Decoded Value	ADCIN1	ADCIN2	I/C Add.	Dead Ban
0	0	0	0	0	7	5	#1	Always Enable
0	0	1	1	1	5	5	#2	Always Enable



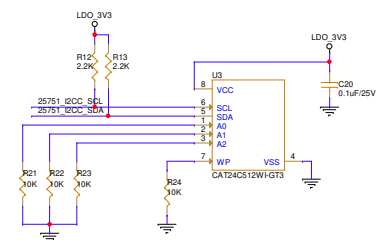
LIQ Detected Curcuit



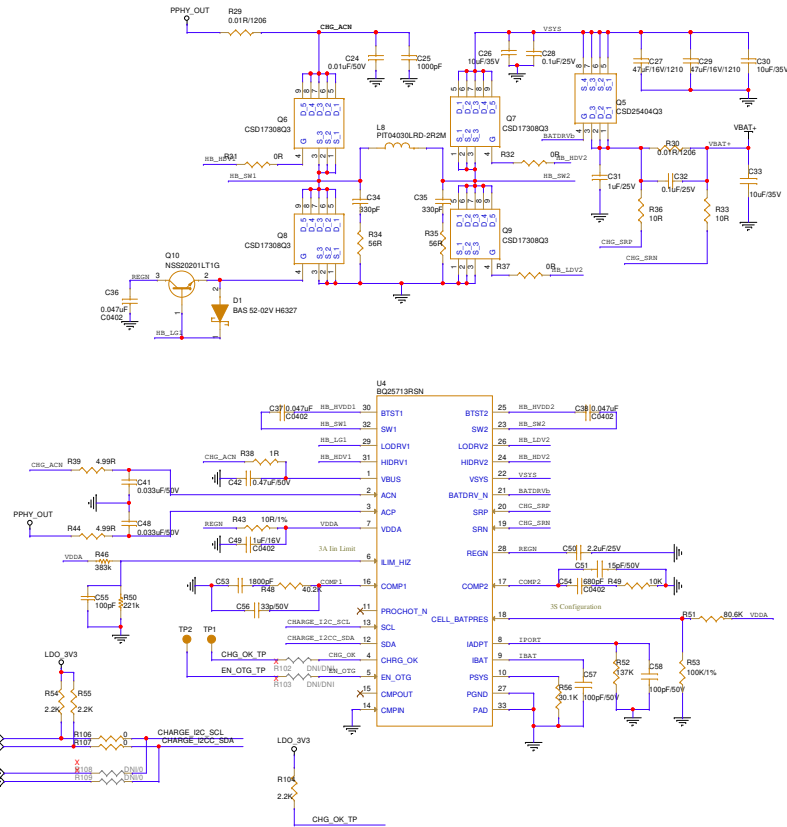
OVP/ESD



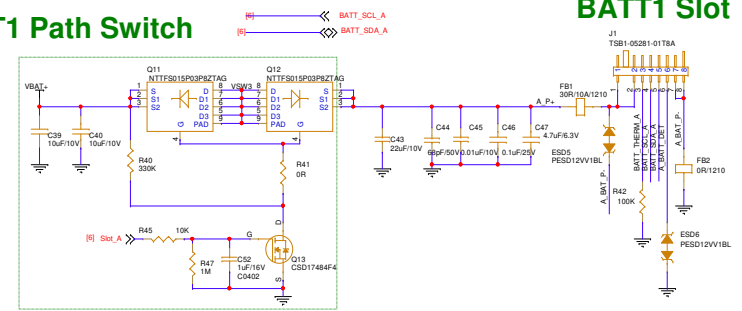
EEPROM



Charger

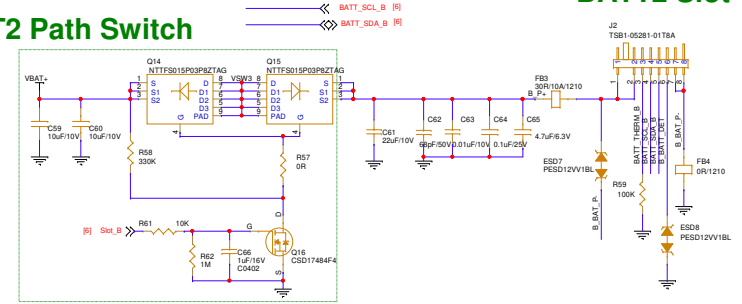


VBAT1 Path Switch



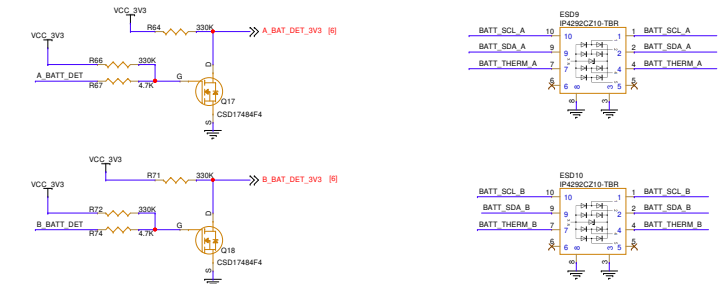
BATT1 Slot

VBAT2 Path Switch

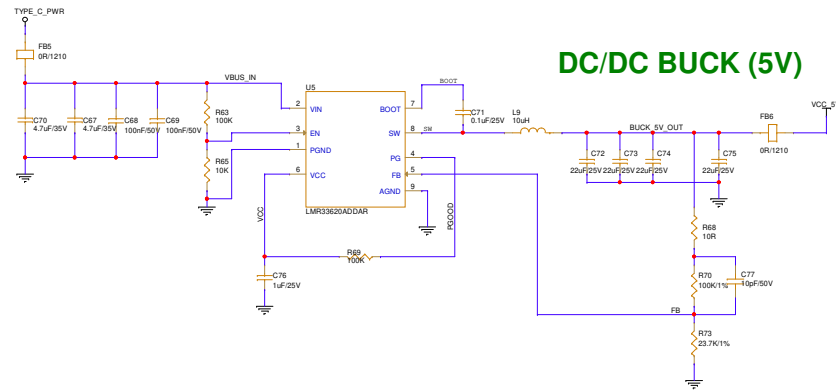


BATT2 Slot

VBAT1/VBAT2 Detect circuit



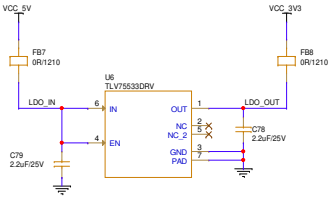
DC/DC BUCK (5V)



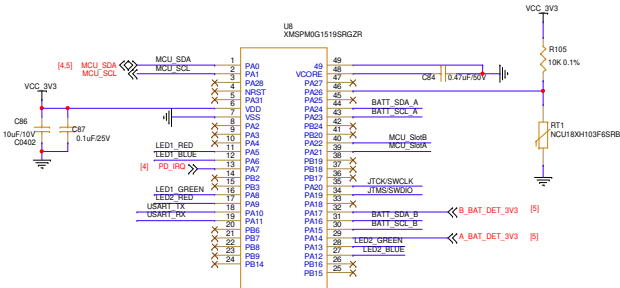
SYSGRATION LTD.				
Rev	Document Number	Battery Dock - Charger		Rev
1.0	1.0	1.0	1.0	1.0
Date: Monday, Aug 15, 2022				
APPROVED BY		CHECKED BY		DESIGNED BY
Jason Cheng		Bruce Lee		Tony Li

MCU

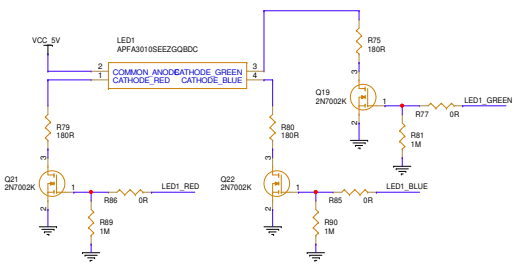
LDO



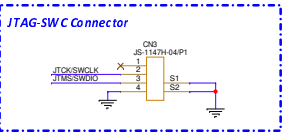
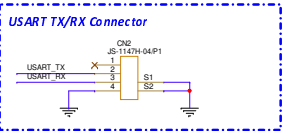
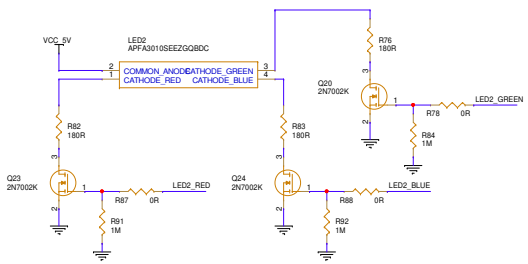
MCU



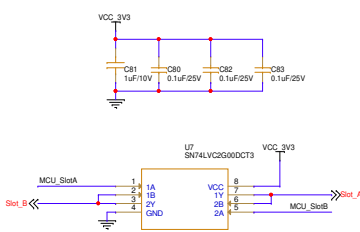
BATT1 Charging Status LED



BATT2 Charging Status LED



NAND Logic (HW Interlock)



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