four 0.1-µF capacitors and
two 1-µF capacitors directly beneath the
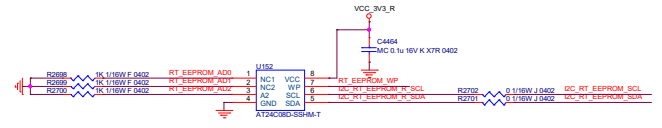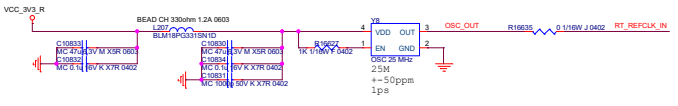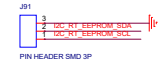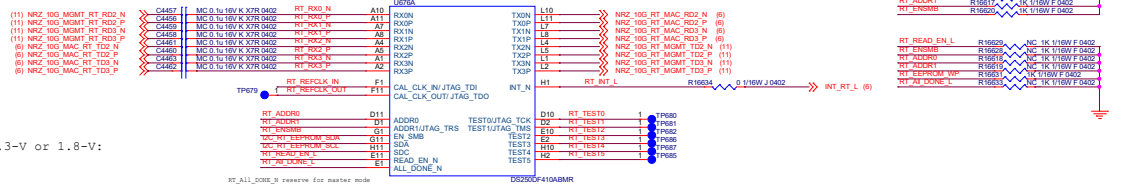device or as close to the VDD pins as
possible.

EN_SMB
Select the electrical voltage of SMBus interface. 2.5-V/3.3-V or 1.8-V:
1: 1 k to VDD - 1.8-V SMBus interface
F: Float - 1.8-V SMBus interface
0: 1 k to GND - 2.5-V/3.3-V SMBus interface

READ_EN_N This pin must be pulled high or left floating for normal operation in SMBus Slave Mode.
READ_EN_N This pin can be held low for normal device operation in SMBus Master Mode.
If SMBus Slave Mode will be used for all device configurations, an EEPROM is not needed.

0: 1 k to GND
• R: 10 k to GND (20 kΩ also acceptable)
• F: Float
• 1: 1 k to VDD

Table 7. SMBus Address Map

| 8-BIT WRITE ADDRESS [HEX] | REQUIRED ADDRESS PIN STRAP VALUE | |
	ADDR1	ADDR0
0x30	0	0
0x32	0	R
0x34	0	F
0x36	0	1
0x38	R	0
0x3A	R	R
0x3C	R	F
0x3E	R	1
0x40	F	0
0x42	F	R
0x44	F	F
0x46	F	1
0x48	1	0
0x4A	1	R
0x4C	1	F
0x4E	1	1