



| Receptacle On Source Device |                 |      |   | DisplayPort Standard Cable |                 |              |      |                 |        |         |             | Receptacle on the Sink Device |         |  |  |
|-----------------------------|-----------------|------|---|----------------------------|-----------------|--------------|------|-----------------|--------|---------|-------------|-------------------------------|---------|--|--|
| At SOURCE                   |                 | Pin# |   | Source Side Plug           |                 | Cable Wiring |      | Sink Side Plug  |        | At SINK |             | Sink End Dongle               |         |  |  |
| Signal Type                 |                 |      |   | Signal Type                | Pin#            |              | Pin# | Signal Type     |        | Pin#    | Signal Type |                               |         |  |  |
| Out                         | ML_Lane 0 (p)   | 1    | → | Out                        | ML_Lane 0 (p)   | →            | 1    | ML_Lane 3 (n)   | In     | →       | 1           | ML_Lane 3 (n)                 | In      |  |  |
| GND                         | GND             | 2    | → | GND                        | GND             | →            | 2    | GND             | GND    | →       | 2           | GND                           | GND     |  |  |
| Out                         | ML_Lane 0 (n)   | 3    | → | Out                        | ML_Lane 0 (n)   | →            | 3    | ML_Lane 3 (p)   | In     | →       | 3           | ML_Lane 3 (p)                 | In      |  |  |
| Out                         | ML_Lane 1 (p)   | 4    | → | Out                        | ML_Lane 1 (p)   | →            | 4    | ML_Lane 2 (n)   | In     | →       | 4           | ML_Lane 2 (n)                 | In      |  |  |
| GND                         | GND             | 5    | → | GND                        | GND             | →            | 5    | GND             | GND    | →       | 5           | GND                           | GND     |  |  |
| Out                         | ML_Lane 1 (n)   | 6    | → | Out                        | ML_Lane 1 (n)   | →            | 6    | ML_Lane 2 (p)   | In     | →       | 6           | ML_Lane 2 (p)                 | In      |  |  |
| Out                         | ML_Lane 2 (p)   | 7    | → | Out                        | ML_Lane 2 (p)   | →            | 7    | ML_Lane 1 (n)   | In     | →       | 7           | ML_Lane 1 (n)                 | In      |  |  |
| GND                         | GND             | 8    | → | GND                        | GND             | →            | 8    | GND             | GND    | →       | 8           | GND                           | GND     |  |  |
| Out                         | ML_Lane 2 (n)   | 9    | → | Out                        | ML_Lane 2 (n)   | →            | 9    | ML_Lane 1 (p)   | In     | →       | 9           | ML_Lane 1 (p)                 | In      |  |  |
| Out                         | ML_Lane 3 (p)   | 10   | → | Out                        | ML_Lane 3 (p)   | →            | 10   | ML_Lane 0 (n)   | In     | →       | 10          | ML_Lane 0 (n)                 | In      |  |  |
| GND                         | GND             | 11   | → | GND                        | GND             | →            | 11   | GND             | GND    | →       | 11          | GND                           | GND     |  |  |
| Out                         | ML_Lane 3 (n)   | 12   | → | Out                        | ML_Lane 3 (n)   | →            | 12   | ML_Lane 0 (p)   | In     | →       | 12          | ML_Lane 0 (p)                 | In      |  |  |
| CONFIG                      | CONFIG1         | 13   | ↔ | CONFIG                     | CONFIG1         | ↔            | 13   | CONFIG1         | CONFIG | ↔       | 13          | CONFIG1                       | CONFIG  |  |  |
| CONFIG                      | CONFIG2         | 14   | ↔ | CONFIG                     | CONFIG2         | ↔            | 14   | CONFIG1         | CONFIG | ↔       | 14          | CONFIG2                       | CONFIG  |  |  |
| IO                          | AUX_CH (p)      | 15   | ↔ | IO                         | AUX_CH (p)      | ↔            | 15   | AUX_CH (p)      | IO     | ↔       | 15          | AUX_CH (p)                    | IO      |  |  |
| GND                         | GND             | 16   | ↔ | GND                        | GND             | ↔            | 16   | GND             | GND    | ↔       | 16          | GND                           | GND     |  |  |
| IO                          | AUX_CH (n)      | 17   | ↔ | IO                         | AUX_CH (n)      | ↔            | 17   | AUX_CH (n)      | IO     | ↔       | 17          | AUX_CH (n)                    | IO      |  |  |
| In                          | Hot Plug Detect | 18   | ← | In                         | Hot Plug Detect | ←            | 18   | Hot Plug Detect | Out    | ←       | 18          | Hot Plug Detect               | Out     |  |  |
| PWR RTN                     | Return DP_PWR   | 19   | ← |                            | Return DP_PWR   | ←            | 19   | Return DP_PWR   |        | ←       | 19          | Return DP_PWR                 | PWR RTN |  |  |
| PWR Out                     | DP_PWR          | 20   | → |                            | DP_PWR          | →            | 20   | DP_PWR          |        | →       | 20          | DP_PWR                        | PWR Out |  |  |