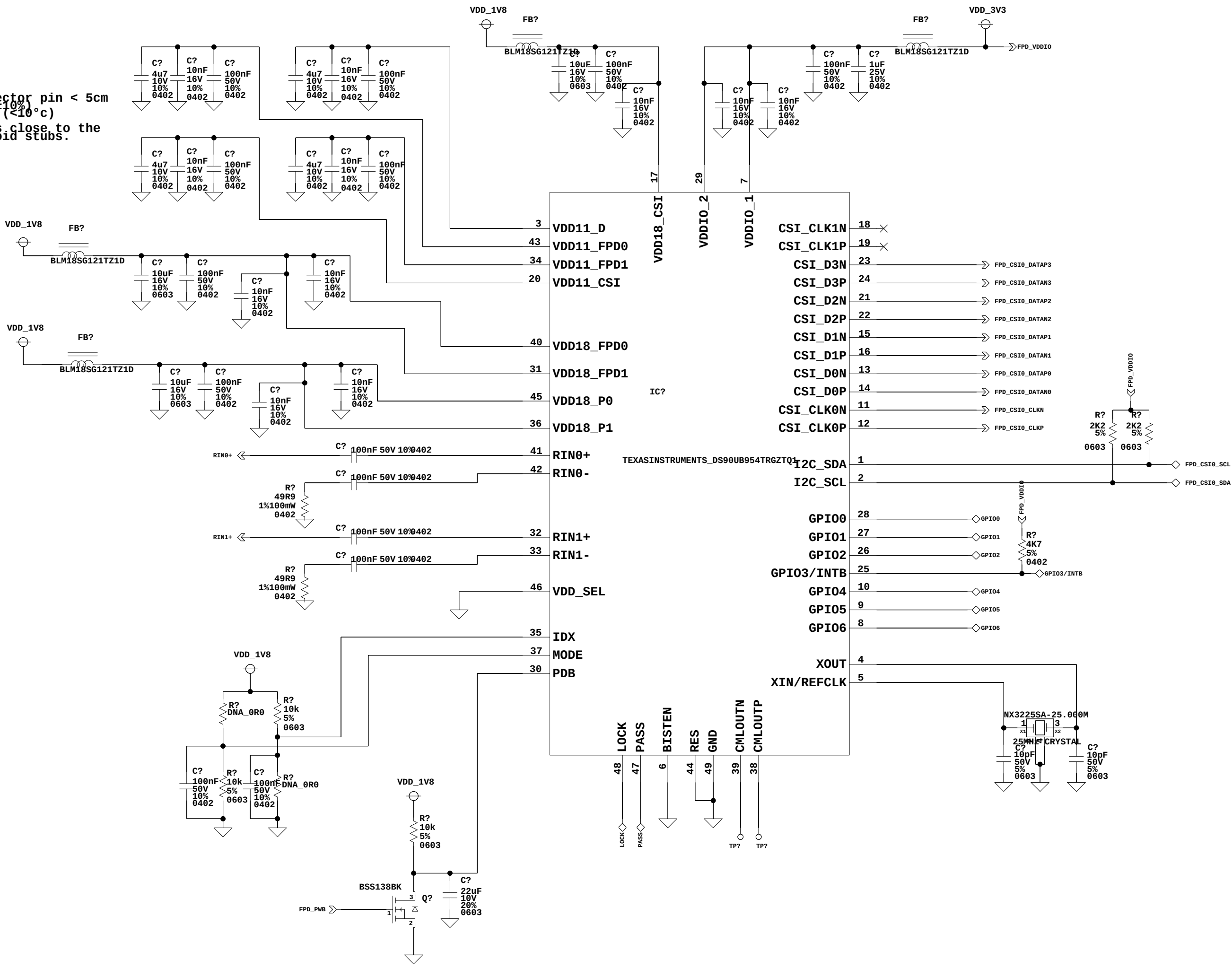
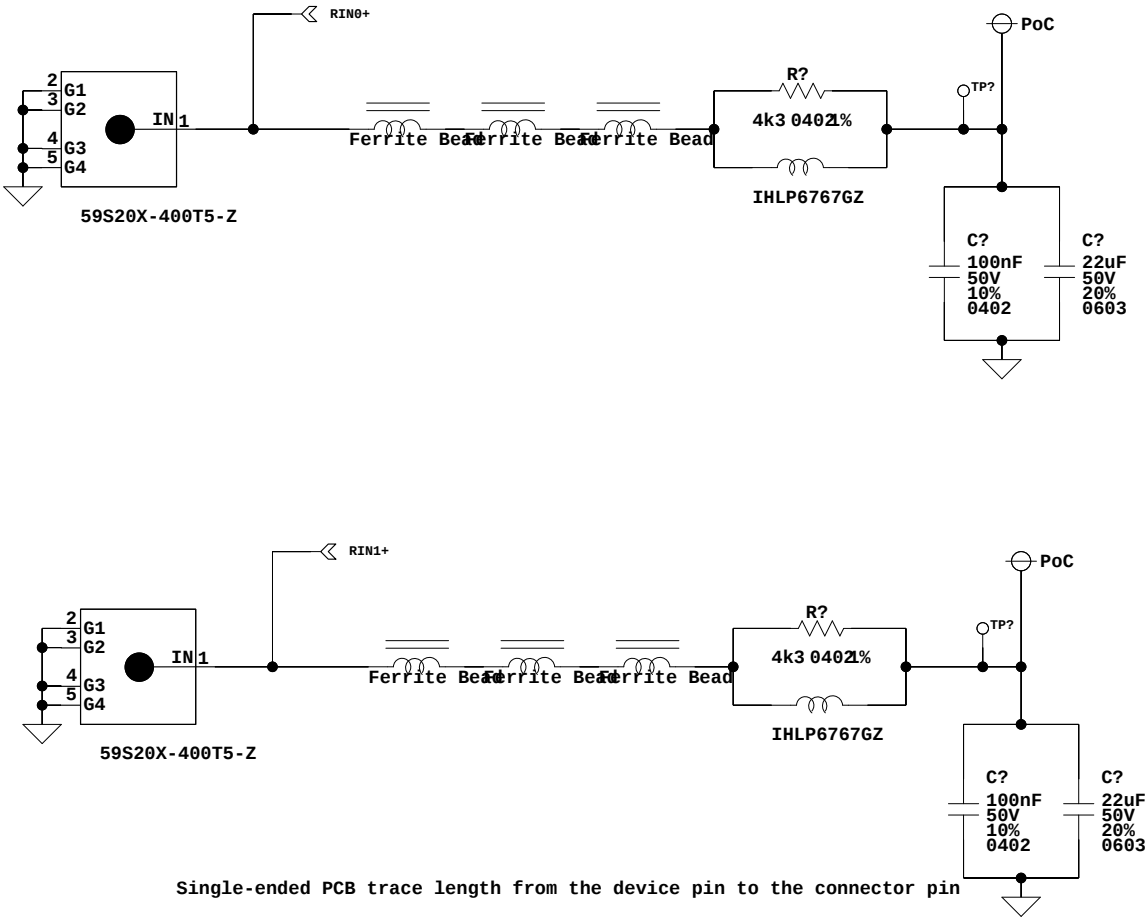


Layout Guidelines :
1. Place 100nF capacitors near to pin RIN0+, RIN0-, RIN1+, RIN1-
2. Single-ended PCB trace length from the device pin (RIN0+, RIN1+) to the connector pin < 5cm
3. Route RIN+ trace as a 50- single-ended trace with tight impedance control ($\pm 10\%$)
4. Ensure RIN+ trace can carry PoC current without significant temperature rise ($< 10^{\circ}\text{C}$)
5. Route RIN- trace loosely coupled to the RIN+ trace ($S > 3W$)
6. Place the smallest component, typically a ferrite bead or a chip inductor, as close to the connector as possible. Route the high-speed trace through one of its pads to avoid stubs.

*W is a trace width. S is a gap between adjacent traces.



Power over Co-ax circuit



Header Connector for Multifunction Pins

