

Setup Information			
DUT ID	E1_S1_Lane0	DPOJET Version	10.3.0.5
Date/Time	2025-11-11 15:06:24	Scope Model	MSO72504DX
Device Type	CEM	Scope Serial Number	B260604
TekExpress Version	PCI Express:10.6.0.188	SPC, FactoryCalibration	PASS;PASS
TekExpress Framework Version	5.1.0.9	Scope F/W Version	10.12.1 Build 26
TekExpress Execution Mode	Live	SSC Status	On
Test Mode	Compliance	Channel Info	Non-ATI
Spec Version	Gen4 - 4.0	Probe1 Model	P76CA-292C;P7625
SigTest Version	3.2.0.3(Gen1,2,3) 4.0.52 (Gen4)-Signal Tests 4.0.52 (Gen4)-Preset Tests	Probe1 Serial Number	B001359;B001429
		Probe2 Model	N/A
		Probe2 Serial Number	N/A
		Probe3 Model	P76CA-292C;P7625
		Probe3 Serial Number	B001360;B001439
		Probe4 Model	N/A
Sigtest Template	Gen1: TX_SYS_CON.dat Gen2 3dB: DUAL_PORT_SYS_CON_225_PAT_CHK.dat Gen2 6dB: DUAL_PORT_SYS_CON_225_PAT_CHK.dat Gen3: PCIE_3_8GB_CEM_DUAL_PORT.dat Gen4: PCIE_4_16GB_CEM_DUAL_PORT_SSC_ENABLED.dat	Probe4 Serial Number	N/A
		Voltage Swing	Full
		Acquisition Count	3
		Embed Filter File	Gen4 refpkg_endpoint_3db_thru.ft
		Slot Number	01
		Overall Test Result	Fail
Overall Execution Time	0:14:22		
DUT COMMENT:	DUT001		

Test Name Summary Table				
Test Name	Lane Number	Equalization	Status	Value
Min Eye Width Gen4	Lane0	P07 Gen4	N.A	Result Unavailable
Extrapolated Eye Height Gen4	Lane0	P07 Gen4	N.A	Result Unavailable

Unit Interval Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mean Unit Interval	Lane0	2.5Gbps	-	400.005 ps	Pass	L:0.125ps H:2.115ps	399.88ps	402.12ps
Min Unit Interval	Lane0	2.5Gbps	-	399.981 ps	Pass	L:0.101ps	399.88ps	N.A
Max Unit Interval	Lane0	2.5Gbps	-	400.029 ps	Pass	H:2.091ps	N.A	402.12ps
COMMENTS	Spec Reference : PCI Express Base Specification Revision 4.0 v1.0							

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Transition Eye Diagram Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Transition Eye Height	Lane0	2.5Gbps	-	717.204 mV	Informative	N.A	N.A	N.A
Min Transition Voltage	Lane0	2.5Gbps	-	-462.393 mV	Pass	L:137.607mV	-600mV	N.A
Max Transition Voltage	Lane0	2.5Gbps	-	462.676 mV	Pass	H:137.324mV	N.A	600mV
Min Transition Top Margin	Lane0	2.5Gbps	-	218.339 mV	Pass	L:218.339mV	0mV	N.A
Min Transition Bottom Margin	Lane0	2.5Gbps	-	-224.865 mV	Pass	H:224.865mV	N.A	0mV
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Non Transition Eye Diagram Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Non Transition Eye Height	Lane0	2.5Gbps	-	613.794 mV	Informative	N.A	N.A	N.A
Min Non Transition Voltage	Lane0	2.5Gbps	-	-429.318 mV	Pass	L:170.682mV	-600mV	N.A
Max Non Transition Voltage	Lane0	2.5Gbps	-	424.946 mV	Pass	H:175.054mV	N.A	600mV
Min Non Transition Top Margin	Lane0	2.5Gbps	-	179.332 mV	Pass	L:179.332mV	0mV	N.A
Min Non Transition Bottom Margin	Lane0	2.5Gbps	-	-181.462 mV	Pass	H:181.462mV	N.A	0mV

COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							
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Min Eye Width Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Eye Width	Lane0	2.5Gbps	-	382.490 ps	Informative	N.A	N.A	N.A

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Min Time Between Crossovers Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Time Between Crossovers	Lane0	2.5Gbps	-	390.581 ps	Informative	N.A	N.A	N.A

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RMS Jitter (Per Edge) Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
RMS Jitter	Lane0	2.5Gbps	-	2.034 ps	Informative	N.A	N.A	N.A

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Median Peak Jitter Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Median-to-Peak Jitter	Lane0	2.5Gbps	-	12.455 ps	Pass	H:64.545ps	N.A	77.0ps
Mean Median-to-Peak Jitter	Lane0	2.5Gbps	-	6.340 ps	Pass	H:70.660ps	N.A	77.0ps
Min Median-to-Peak Jitter	Lane0	2.5Gbps	-	4.056 ps	Pass	H:72.944ps	N.A	77.0ps

COMMENTS Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0

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Peak to Peak Jitter Gen1								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Peak-to-Peak Jitter	Lane0	2.5Gbps	-	17.510 ps	Informative	N.A	N.A	N.A
Min Peak-to-Peak Jitter	Lane0	2.5Gbps	-	7.573 ps	Informative	N.A	N.A	N.A
Mean Peak-to-Peak Jitter	Lane0	2.5Gbps	-	10.913 ps	Informative	N.A	N.A	N.A

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Unit Interval Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mean Unit Interval	Lane0	5Gbps	3.5dB	200.003 ps	Pass	L:0.063ps H:1.057ps	199.94ps	201.06ps
Mean Unit Interval	Lane0	5Gbps	6dB	200.003 ps	Pass	L:0.063ps H:1.057ps	199.94ps	201.06ps

COMMENTS Spec Reference : PCI Express Base Specification Revision 4.0 v1.0

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Composit Eye Height Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Composit Eye Height	Lane0	5Gbps	3.5dB	516.348 mV	Informative	N.A	N.A	N.A
Composit Eye Height	Lane0	5Gbps	6dB	393.316 mV	Informative	N.A	N.A	N.A

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Transition Eye Diagram Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Transition Eye Height	Lane0	5Gbps	3.5dB	569.236 mV	Informative	N.A	N.A	N.A
Min Transition Voltage	Lane0	5Gbps	3.5dB	-440.680 mV	Pass	L:159.320mV	-600mV	N.A
Max Transition Voltage	Lane0	5Gbps	3.5dB	434.920 mV	Pass	H:165.080mV	N.A	600mV
Min Transition Top Margin	Lane0	5Gbps	3.5dB	169.149 mV	Pass	L:169.149mV	0mV	N.A
Min Transition Bottom Margin	Lane0	5Gbps	3.5dB	-175.087 mV	Pass	H:175.087mV	N.A	0mV

Min Transition Eye Height	Lane0	5Gbps	6dB	580.238 mV	Informative	N.A	N.A	N.A
Min Transition Voltage	Lane0	5Gbps	6dB	-440.640 mV	Pass	L:159.360mV	-600mV	N.A
Max Transition Voltage	Lane0	5Gbps	6dB	430.560 mV	Pass	H:169.440mV	N.A	600mV
Min Transition Top Margin	Lane0	5Gbps	6dB	172.100 mV	Pass	L:172.100mV	0mV	N.A
Min Transition Bottom Margin	Lane0	5Gbps	6dB	-183.139 mV	Pass	H:183.139mV	N.A	0mV
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Non Transition Eye Diagram Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Non Transition Eye Height	Lane0	5Gbps	3.5dB	516.348 mV	Informative	N.A	N.A	N.A
Min Non Transition Voltage	Lane0	5Gbps	3.5dB	-409.880 mV	Pass	L:190.120mV	-600mV	N.A
Max Non Transition Voltage	Lane0	5Gbps	3.5dB	404.120 mV	Pass	H:195.880mV	N.A	600mV
Min Non Transition Top Margin	Lane0	5Gbps	3.5dB	143.023 mV	Pass	L:143.023mV	0mV	N.A
Min Non Transition Bottom Margin	Lane0	5Gbps	3.5dB	-148.326 mV	Pass	H:148.326mV	N.A	0mV
Min Non Transition Eye Height	Lane0	5Gbps	6dB	393.316 mV	Informative	N.A	N.A	N.A
Min Non Transition Voltage	Lane0	5Gbps	6dB	-383.440 mV	Pass	L:216.560mV	-600mV	N.A
Max Non Transition Voltage	Lane0	5Gbps	6dB	373.360 mV	Pass	H:226.640mV	N.A	600mV
Min Non Transition Top Margin	Lane0	5Gbps	6dB	81.572 mV	Pass	L:81.572mV	0mV	N.A
Min Non Transition Bottom Margin	Lane0	5Gbps	6dB	-86.744 mV	Pass	H:86.744mV	N.A	0mV
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Min Eye Width Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Eye Width	Lane0	5Gbps	3.5dB	166.894 ps	Informative	N.A	N.A	N.A
Min Eye Width	Lane0	5Gbps	6dB	160.516 ps	Informative	N.A	N.A	N.A
COMMENTS								

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Min Time Between Crossovers Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Time Between Crossovers	Lane0	5Gbps	3.5dB	188.368 ps	Informative	N.A	N.A	N.A
Min Time Between Crossovers	Lane0	5Gbps	6dB	190.312 ps	Informative	N.A	N.A	N.A
COMMENTS								

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TJ @ E-12 Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
TJ@E-12	Lane0	5Gbps	3.5dB	33.106 ps	Pass	H:71.894ps	N.A	105.0ps
TJ@E-12	Lane0	5Gbps	6dB	39.484 ps	Pass	H:65.516ps	N.A	105.0ps
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Dj_dd Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Dj_dd	Lane0	5Gbps	3.5dB	10.864 ps	Pass	H:46.136ps	N.A	57.0ps
Dj_dd	Lane0	5Gbps	6dB	17.538 ps	Pass	H:39.462ps	N.A	57.0ps
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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RJ(RMS) Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
RJ	Lane0	5Gbps	3.5dB	1.582 ps	Informative	N.A	N.A	N.A
RJ	Lane0	5Gbps	6dB	1.561 ps	Informative	N.A	N.A	N.A
COMMENTS		Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0						

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Peak to Peak Jitter Gen2								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Peak-to-Peak Jitter	Lane0	5Gbps	3.5dB	28.157 ps	Informative	N.A	N.A	N.A
Max Peak-to-Peak Jitter	Lane0	5Gbps	6dB	34.230 ps	Informative	N.A	N.A	N.A
COMMENTS								

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Unit Interval Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mean Unit Interval	Lane0	8Gbps	P0 Gen3	125.002 ps	Pass	L:0.039ps H:0.661ps	124.9625ps	125.6625ps
COMMENTS		Spec Reference : PCI Express Base Specification Revision 4.0 v1.0						

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Composit Eye Height Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Composit Eye Height	Lane0	8Gbps	P0 Gen3	209.072 mV	Pass	L:175.072mV	34mV	N.A
COMMENTS		Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0						

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Transition Eye Diagram Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Transition Eye Height	Lane0	8Gbps	P0 Gen3	219.029 mV	Informative	N.A	N.A	N.A
Min Transition Voltage	Lane0	8Gbps	P0 Gen3	-239.738 mV	Pass	L:360.262mV	-600mV	N.A
Max Transition Voltage	Lane0	8Gbps	P0 Gen3	247.760 mV	Pass	H:352.240mV	N.A	600mV
Min Transition Top Margin	Lane0	8Gbps	P0 Gen3	86.761 mV	Pass	L:86.761mV	0mV	N.A
Min Transition Bottom Margin	Lane0	8Gbps	P0 Gen3	-86.268 mV	Pass	H:86.268mV	N.A	0mV
COMMENTS		Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0						

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Non Transition Eye Diagram Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Non Transition Eye Height	Lane0	8Gbps	P0 Gen3	231.092 mV	Informative	N.A	N.A	N.A
Min Non Transition Voltage	Lane0	8Gbps	P0 Gen3	-247.253 mV	Pass	L:352.747mV	-600mV	N.A
Max Non Transition Voltage	Lane0	8Gbps	P0 Gen3	246.581 mV	Pass	H:353.419mV	N.A	600mV
Min Non Transition Top Margin	Lane0	8Gbps	P0 Gen3	93.787 mV	Pass	L:93.787mV	0mV	N.A
Min Non Transition Bottom Margin	Lane0	8Gbps	P0 Gen3	-91.305 mV	Pass	H:91.305mV	N.A	0mV
COMMENTS		Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0						

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Min Eye Width Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Eye Width	Lane0	8Gbps	P0 Gen3	82.613 ps	Informative	N.A	N.A	N.A
COMMENTS								

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Min Time Between Crossovers Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Time Between Crossovers	Lane0	8Gbps	P0 Gen3	110.014 ps	Informative	N.A	N.A	N.A

COMMENTS	
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TJ @ E-12 Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
TJ@E-12	Lane0	8Gbps	P0 Gen3	42.387 ps	Pass	H:41.363ps	N.A	83.75ps
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Dj_dd Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Dj_dd	Lane0	8Gbps	P0 Gen3	32.480 ps	Informative	N.A	N.A	N.A
COMMENTS								

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RJ(RMS) Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
RJ	Lane0	8Gbps	P0 Gen3	0.705 ps	Informative	N.A	N.A	N.A
COMMENTS								

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Peak to Peak Jitter Gen3								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Peak-to-Peak Jitter	Lane0	8Gbps	P0 Gen3	42.210 ps	Informative	N.A	N.A	N.A
COMMENTS								

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Unit Interval Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Mean Unit Interval R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Mean Unit Interval R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Mean Unit Interval R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Mean Unit Interval Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS	Spec Reference : PCI Express Base Specification Revision 4.0 v1.0							

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Composite Eye Height Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Composit Eye Height R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Composit Eye Height R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Composit Eye Height R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Composit Eye Height Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Transition Eye Diagram Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Transition Eye Height R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Voltage R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Transition Voltage R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Top Margin R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Bottom Margin R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Eye Height R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Voltage R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Transition Voltage R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Top Margin R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A

Min Transition Bottom Margin R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Eye Height R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Voltage R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Transition Voltage R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Top Margin R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Bottom Margin R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Transition Eye Height Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Transition Voltage Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Max Transition Voltage Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Transition Top Margin Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Transition Bottom Margin Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A

COMMENTS Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0

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Non Transition Eye Diagram Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Non Transition Eye Height R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Voltage R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Non Transition Voltage R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Top Margin R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Bottom Margin R1	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Eye Height R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Voltage R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Non Transition Voltage R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Top Margin R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Bottom Margin R2	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Eye Height R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Voltage R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Max Non Transition Voltage R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Top Margin R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Bottom Margin R3	Lane0	16Gbps	P07 Gen4	N.A	Informative	N.A	N.A	N.A
Min Non Transition Eye	Lane0	16Gbps	P07 Gen4	Result	Informative	N.A	N.A	N.A

Height Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Unavailable	Informative	N.A	N.A	N.A
Min Non Transition Voltage Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Max Non Transition Voltage Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Non Transition Top Margin Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Non Transition Bottom Margin Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Min Eye Width Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Eye Width R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Eye Width R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Eye Width R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Eye Width Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	N.A	N.A	N.A	N.A
COMMENTS								

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Min Time Between Crossovers Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Min Time Between Crossovers R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Time Between Crossovers R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Time Between Crossovers R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Min Time Between Crossovers Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS								

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TJ @ E-12 Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
TJ@E-12 R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
TJ@E-12 R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
TJ@E-12 R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
TJ@E-12 Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS	Spec Reference : PCI Express Card Electromechanical Specification Revision 4.0 v1.0							

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Dj_dd Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Dj_dd R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Dj_dd R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Dj_dd R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Dj_dd Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS								

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RJ(RMS) Gen4								
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Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
RJ R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
RJ R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
RJ R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
RJ Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS								

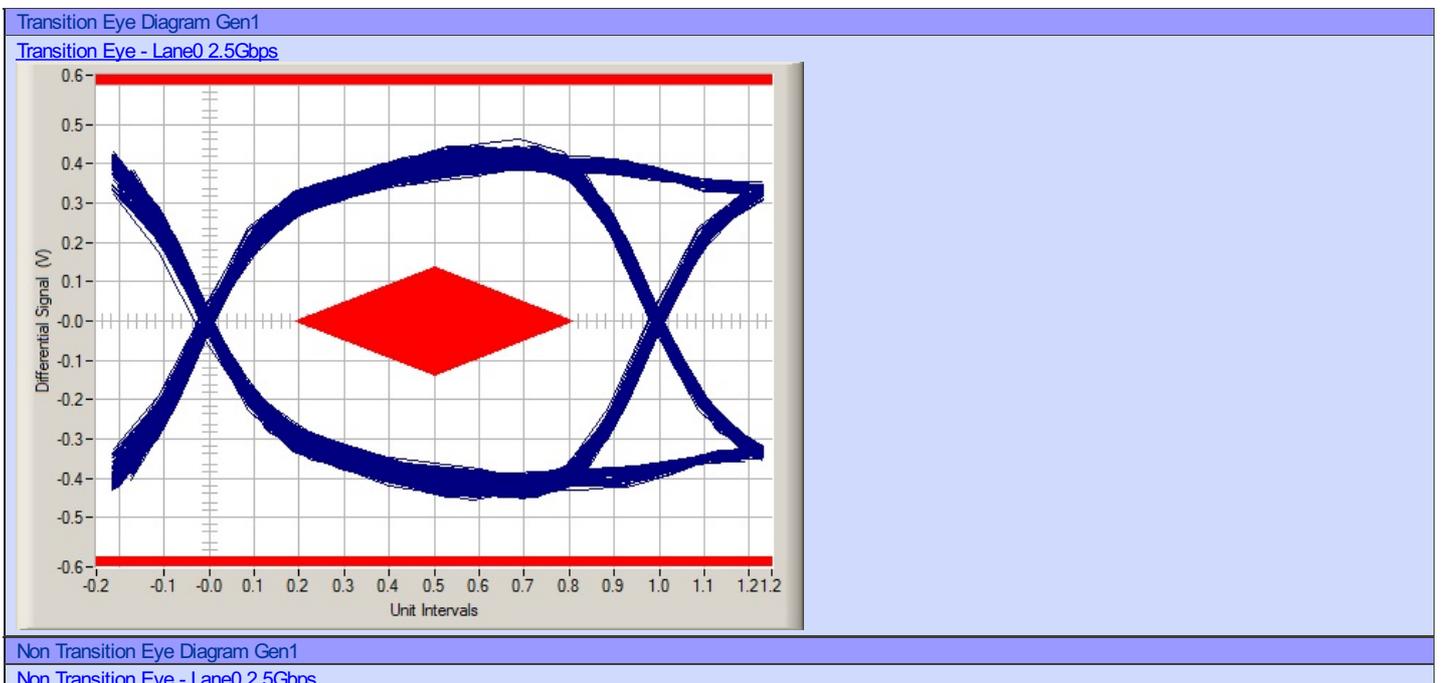
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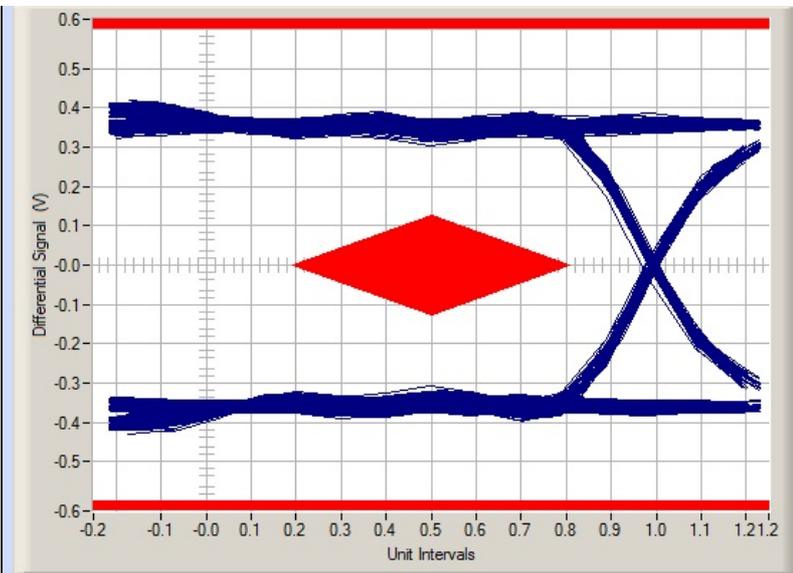
Peak to Peak Jitter Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Max Peak-to-Peak Jitter R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Max Peak-to-Peak Jitter R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Max Peak-to-Peak Jitter R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Max Peak-to-Peak Jitter Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
COMMENTS								

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Extrapolated Eye Height Gen4								
Measurement Details	Lane Name	Data Rate	Equalization	Measured Value	Test Result	Margin	Low Limit	High Limit
Extrapolated Eye Height R1	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Extrapolated Eye Height R2	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Extrapolated Eye Height R3	Lane0	16Gbps	P07 Gen4	Result Unavailable	Informative	N.A	N.A	N.A
Extrapolated Eye Height Mean of 3 Acquisition	Lane0	16Gbps	P07 Gen4	Result Unavailable	N.A	N.A	N.A	N.A
COMMENTS		Spec Reference : PCI Express Card Electromechanical Specification Revision 5.0 v0.7						

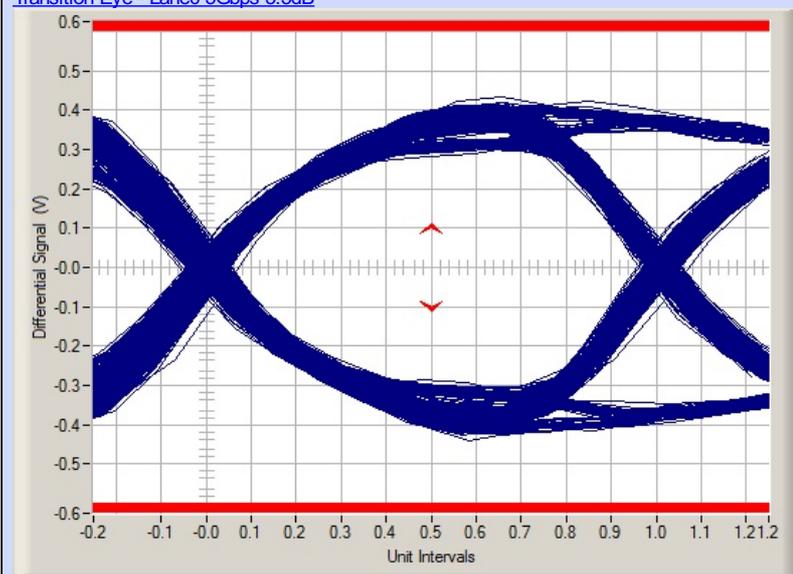
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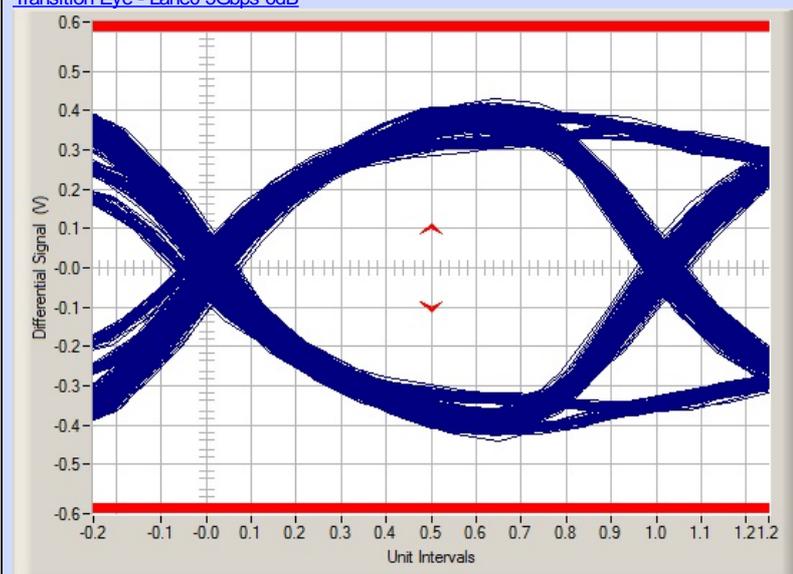


Transition Eye Diagram Gen2

Transition Eye - Lane0 5Gbps 3.5dB

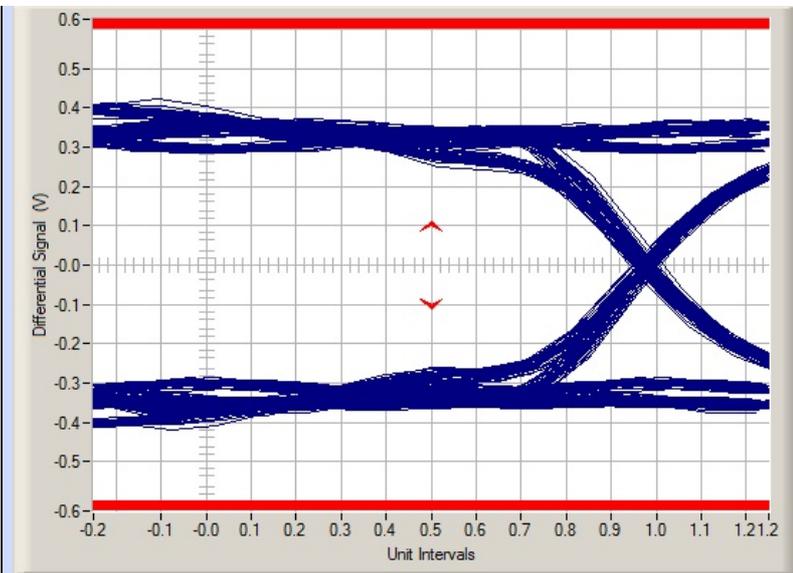


Transition Eye - Lane0 5Gbps 6dB

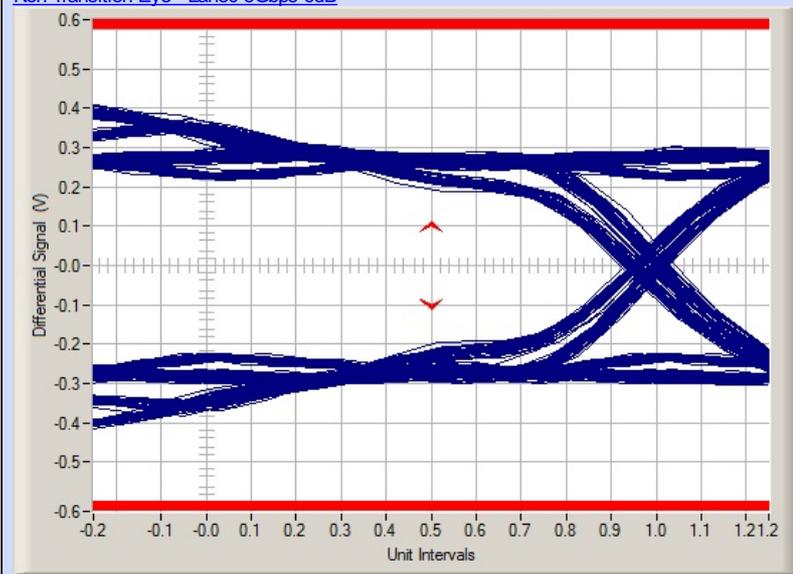


Non Transition Eye Diagram Gen2

Non Transition Eye - Lane0 5Gbps 3.5dB

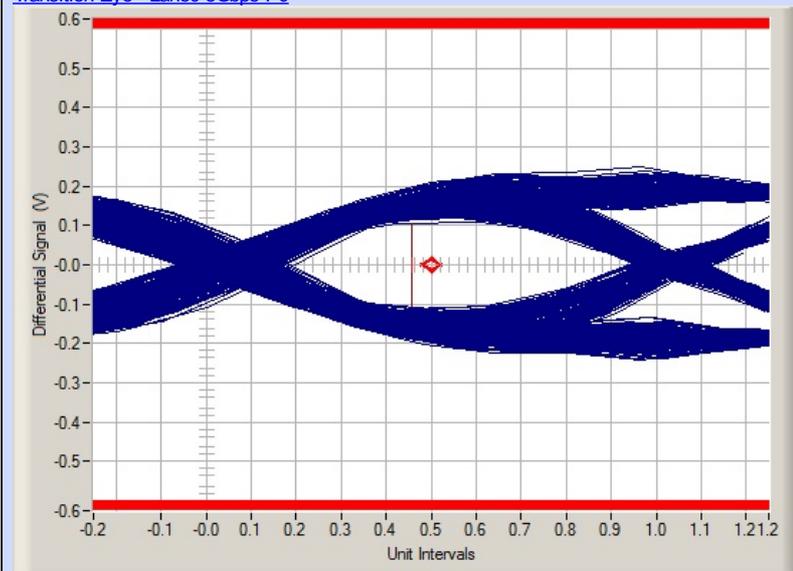


Non Transition Eye - Lane0 5Gbps 6dB



Transition Eye Diagram Gen3

Transition Eye - Lane0 8Gbps P0



Non Transition Eye Diagram Gen3

Non Transition Eye - Lane0 8Gbps P0

