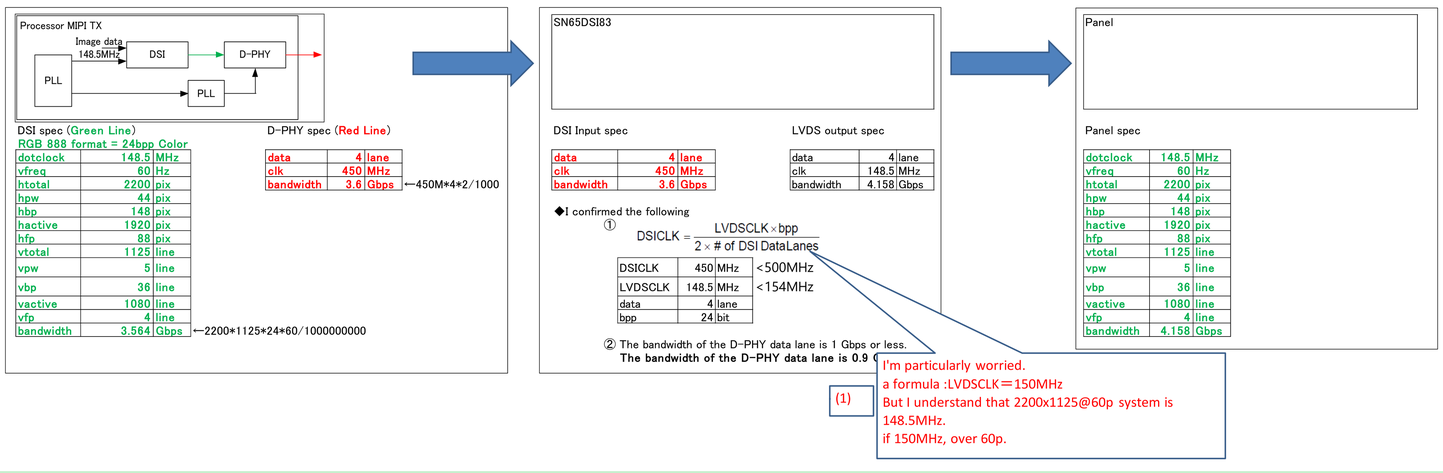
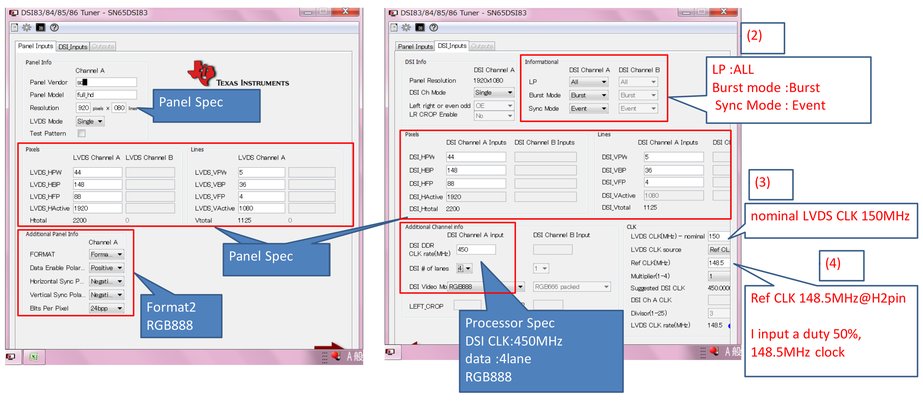
1、SYSTEM



2、Question



(2)
LP :ALL
Burst mode :Burst
Sync Mode : Event

(3)
nominal LVDS CLK 150MHz

(4)
Ref CLK 148.5MHz@H2pin

I input a duty 50%,
148.5MHz clock