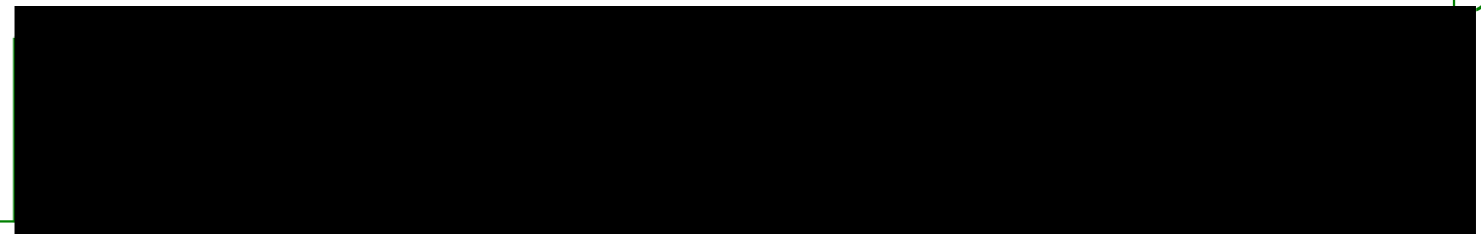
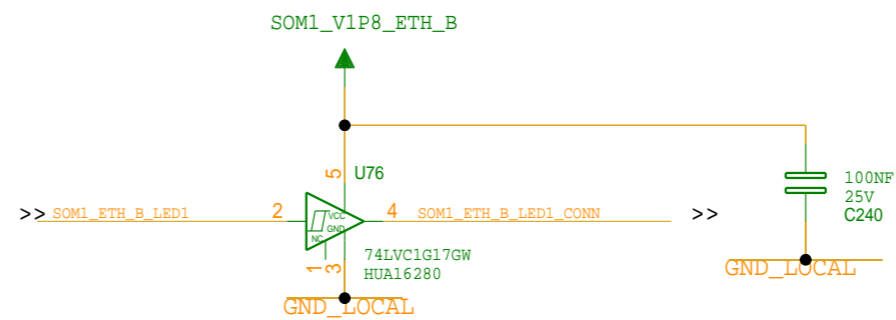
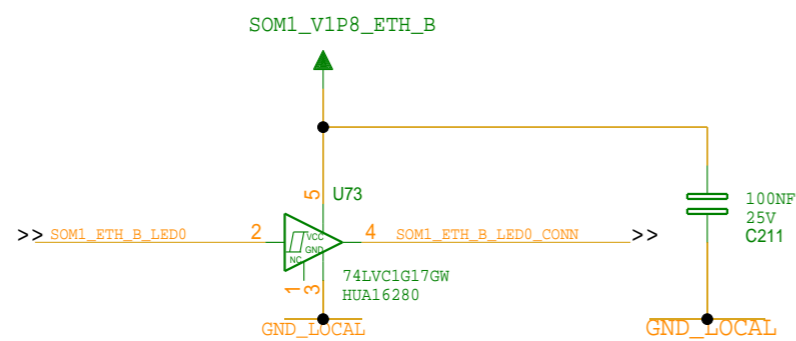
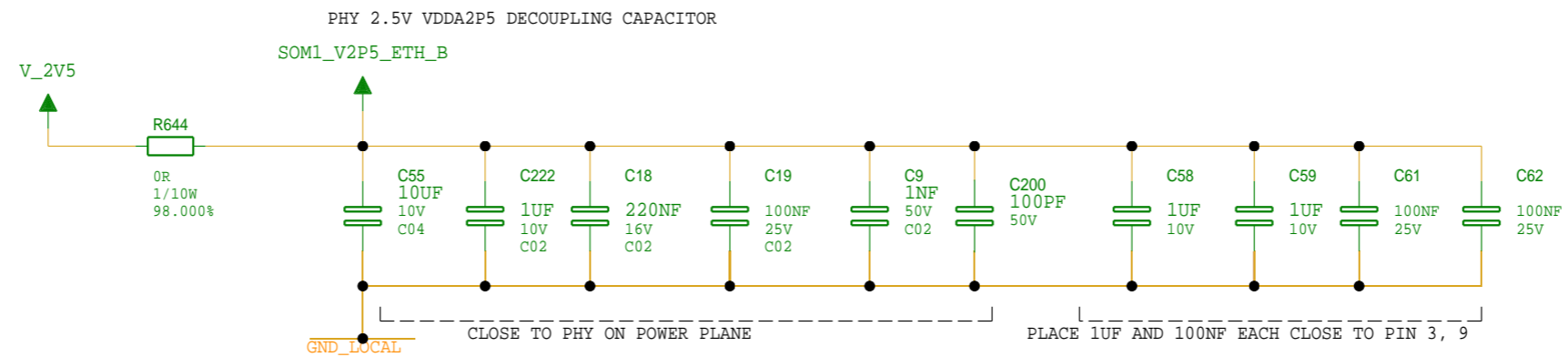
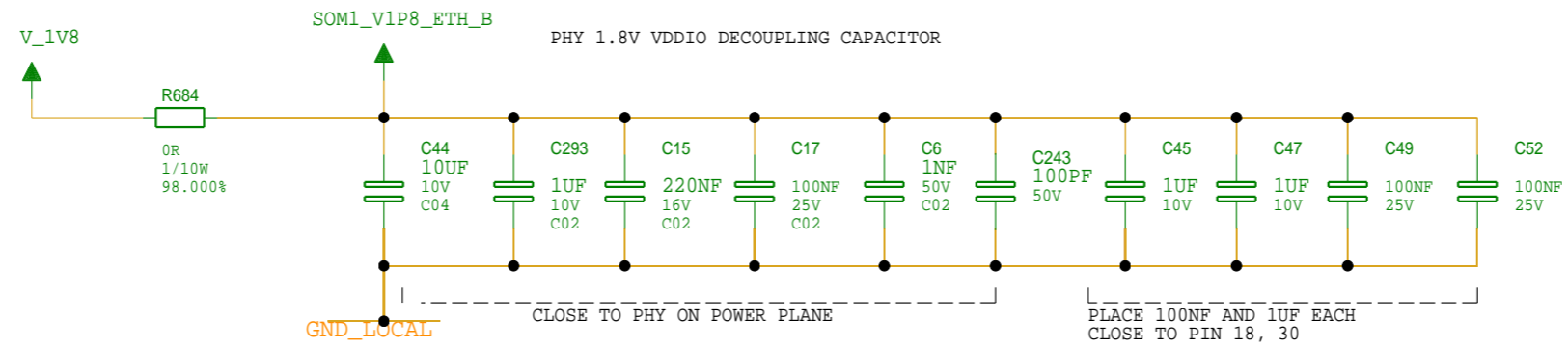
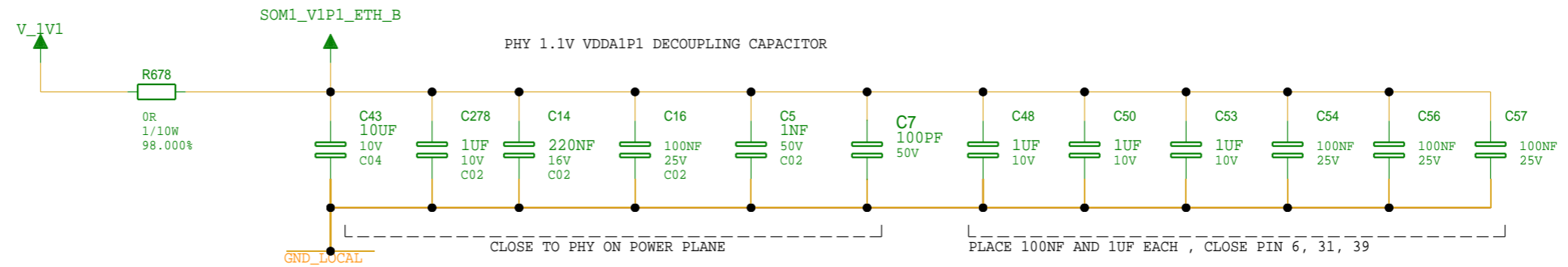
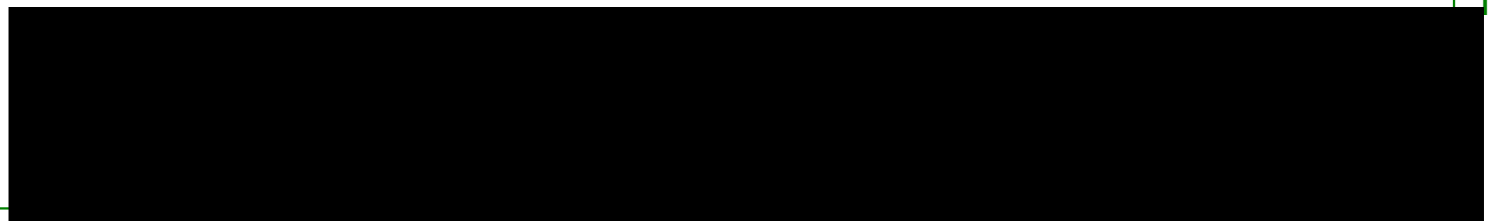
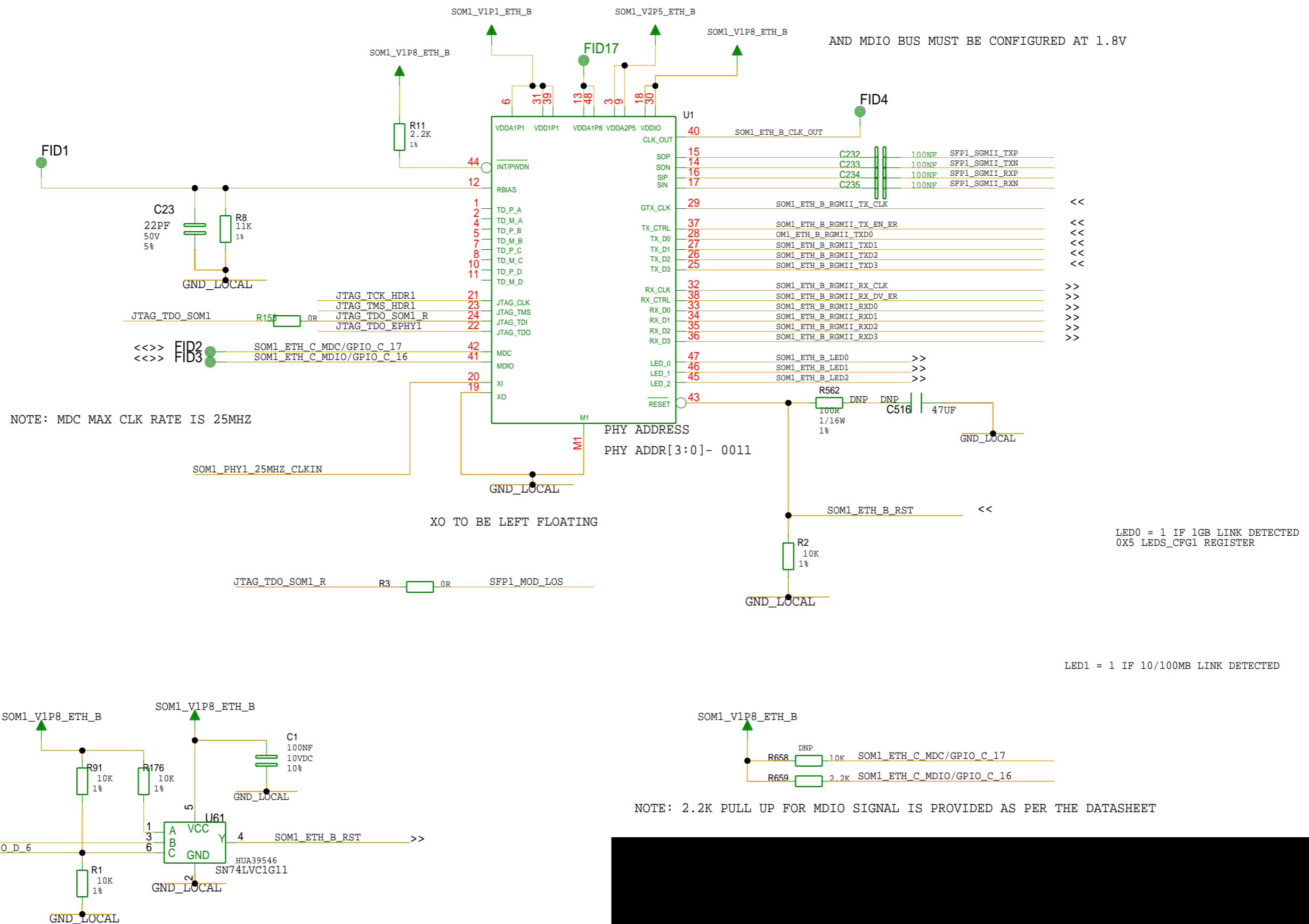
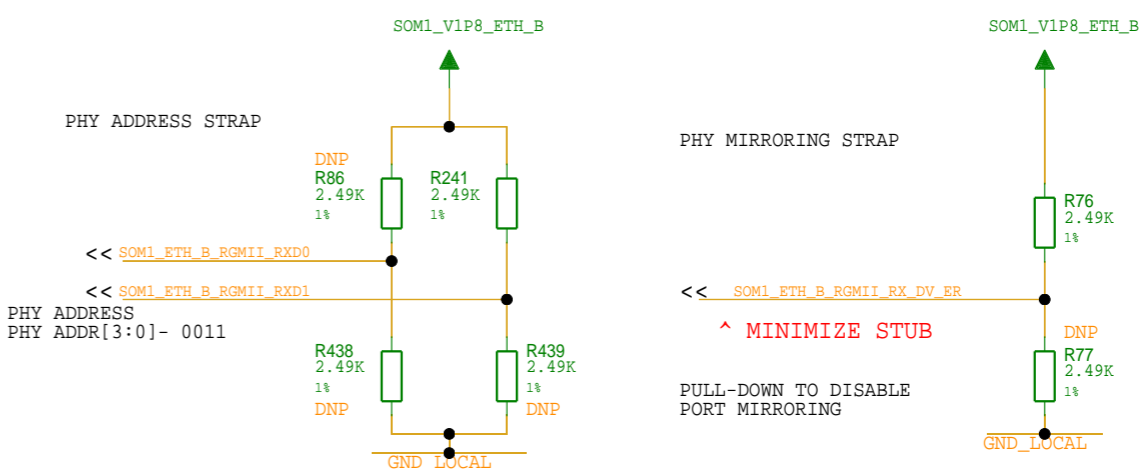
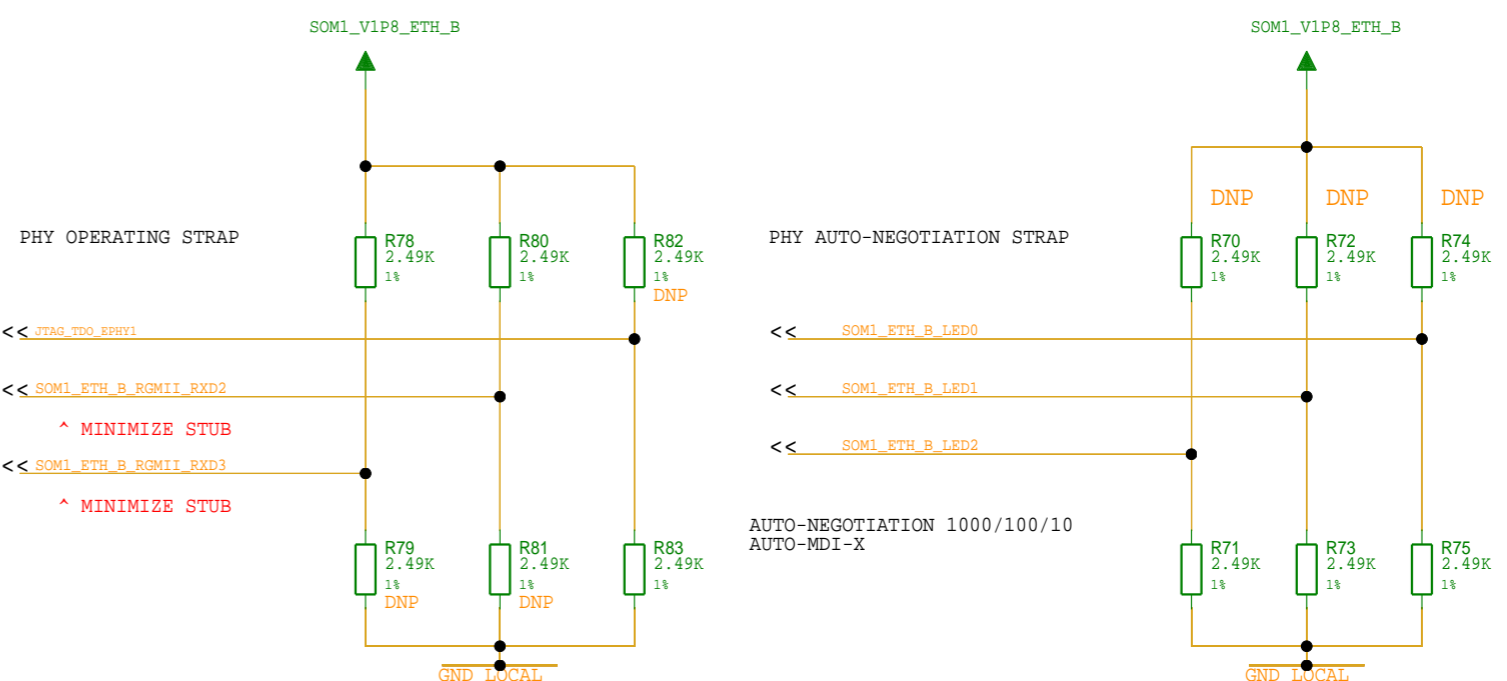
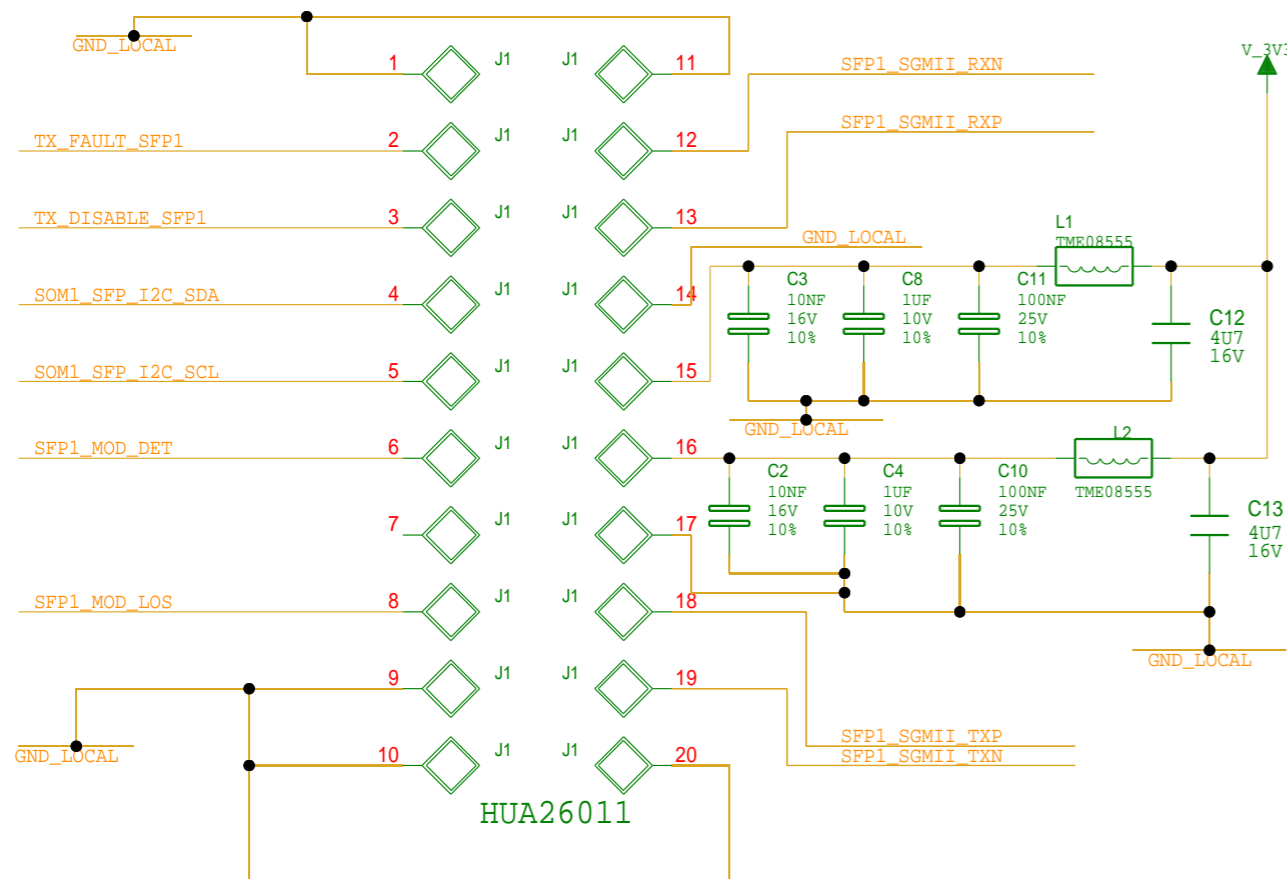


SOM-1 ETHERNET- B DECAPS AND LED BUFFERS



SOM -1 ETHERNET-B





7.5.1.2 Strap for DP83869HM Functional Mode Selection

Table 7-12. Functional Mode Strap Table

PIN NAME	STRAP NAME	PIN #	DEFAULT	OPMO DE[2]	OPMO DE[1]	OPMO DE[0]	FUNCTIONAL MODES
JTAG_TDO/ GPIO_1	OPMODE[0]	22	0	0	0	0	RGMII to Copper (1000Base-T/ 100Base-TX/10Base-Te)
				0	0	1	RGMII to 1000Base-X
RX_D3	OPMODE[1]	36	0	0	1	0	RGMII to 100Base-FX
				0	1	1	RGMII-SGMII Bridge Mode
RX_D2	OPMODE[2]	35	0	1	0	0	1000Base-T to 1000Base-X
				1	0	1	100Base-TX to 100Base-FX
				1	1	0	SGMII to Copper (1000Base-T/ 100Base-TX/10Base-Te)
				1	1	1	JTAG for boundary scan

7.5.1.4 Straps for RGMII/SGMII to Copper

Table 7-14. Copper Ethernet Strap Table

PIN NAME	STRAP NAME	PIN #	DEFAULT	ANEG_DIS	ANEGS_EL_1	ANEGS_EL_0	FUNCTION
LED_0	ANEG_DIS	47	0	0	0	0	Auto-negotiation, 1000/100/10 advertised, Auto MDI-X
				0	0	1	Auto-negotiation, 1000/100 advertised, Auto MDI-X
LED_1	ANEGSEL_0	46	0	0	1	0	Auto-negotiation, 100/10 advertised, Auto-MDI-X
				0	1	1	NA
				1	0	0	NA
LED_2	ANEGSEL_1	45	0	1	1	0	Forced 100M, full duplex, MDI mode
				1	1	1	Forced 100M, full duplex, MDI-X mode
							0
RX_CTRL	MIRROR_EN	38	0			1	Port Mirroring Enabled

Table 7-11. PHY Strap Table

PIN NAME	STRAP NAME	PIN #	DEFAULT	PHY_ADD1		PHY_ADD0	
				MODE 0	PHY_ADD1	PHY_ADD0	MODE 3
RX_D0	PHY_ADD[1:0]	33	00	0	0	0	0
				0	0	1	1
				1	0	0	0
				1	1	0	1
PIN NAME	STRAP NAME	PIN #	DEFAULT	PHY_ADD3		PHY_ADD2	
				MODE 0	PHY_ADD3	PHY_ADD2	MODE 3
RX_D1	PHY_ADD[3:2]	34	00	0	0	0	0
				0	0	1	1
				1	0	0	0
				1	1	0	1

OSM - 1 ETHERNET

