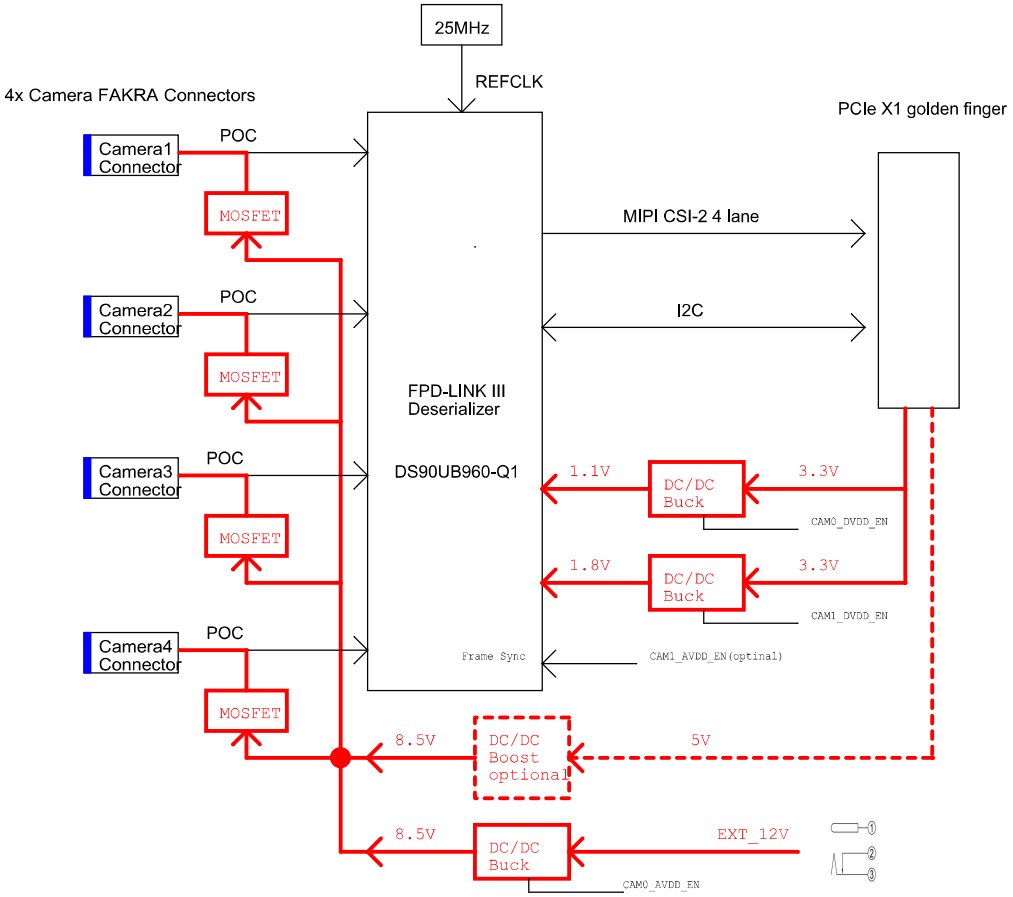
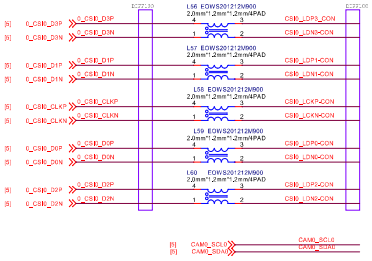
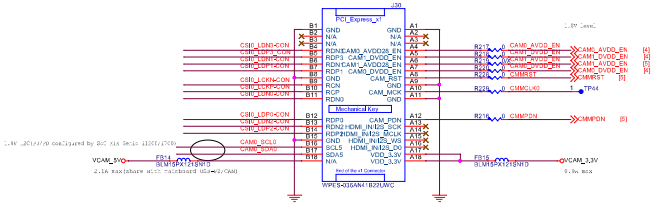
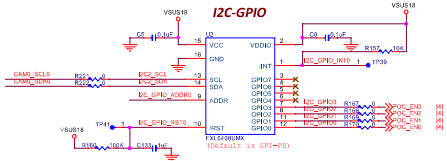
Block Diagram



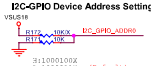
MIPI CSI-2 Golden Finger connector #1







I2C-GPIO Device Address Setting

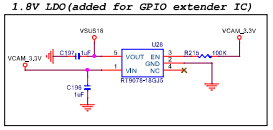


I2C-GPIO LIST

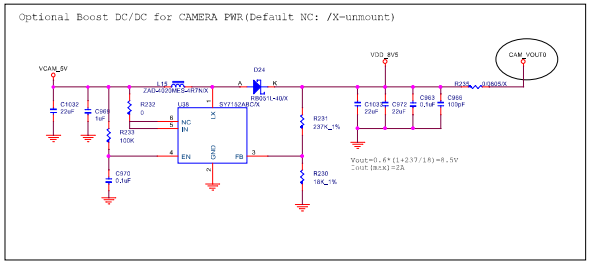
GPIO#	I/O	Function	Note
I2C_GPIO0	I/O	I2C_SDA	Active-High
I2C_GPIO1	I/O	I2C_SCL	Active-High
I2C_GPIO2	I/O	I2C_GPIO2	Active-High
I2C_GPIO3	I/O	I2C_GPIO3	Active-High
I2C_GPIO4	I/O	I2C_GPIO4	Active-High
I2C_GPIO5	I/O	I2C_GPIO5	Active-High
I2C_GPIO6	I/O	I2C_GPIO6	Active-High
I2C_GPIO7	I/O	I2C_GPIO7	Active-High
I2C_GPIO8	I/O	I_CCSBY	Active-Low
I2C_GPIO9	I/O	I_CCSBY	Active-Low
I2C_GPIO10	I/O	I_ERR	Active-Low
I2C_GPIO11	I/O	I_ERR	Active-Low
I2C_GPIO12	I/O	I_LFLT	Active-Low
I2C_GPIO13	I/O	I_LFLT	Active-Low
I2C_GPIO14	I/O	I_LMO	Active-Low
I2C_GPIO15	I/O	I_LMO	Active-Low

LMO Line Memory Overflow Output
LFLT Line-Fault Output
ERR Error Output
CCBSY Control Channel Busy

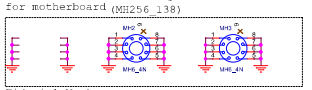
1.8V LDO(added for GPIO extender IC)



Optional Boost DC/DC for CAMERA PWR(Default NC: /X-unmount)

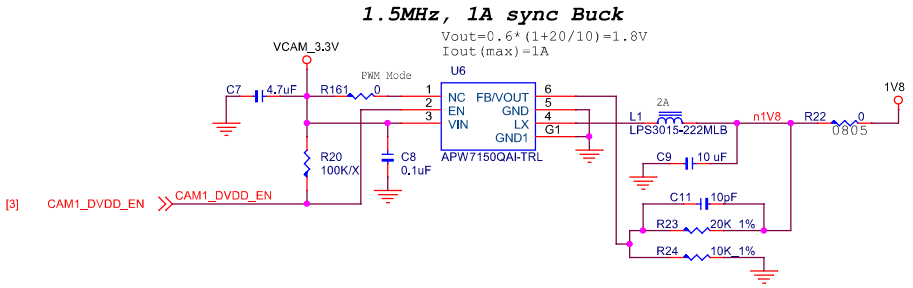


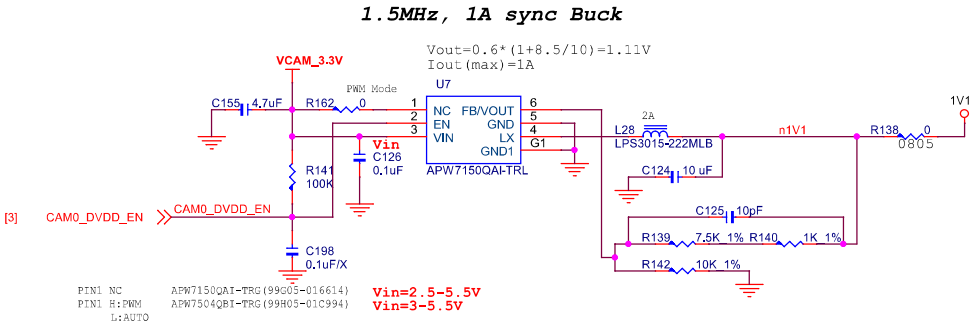
Mechanical Riser Mounting Holes:
for motherboard (MH256_138)

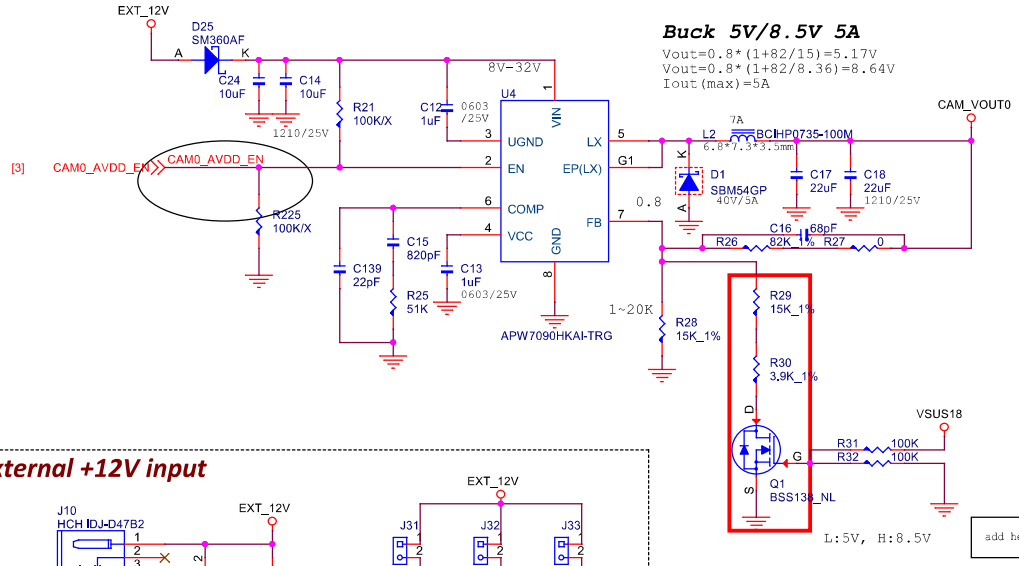


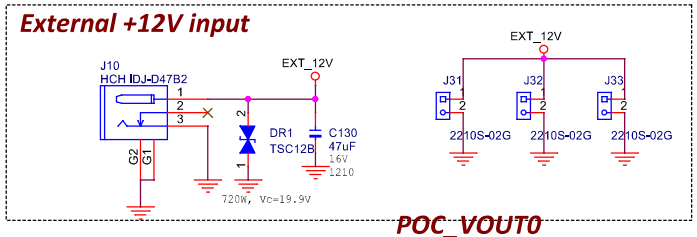
Fiducial Marks:

Power -1









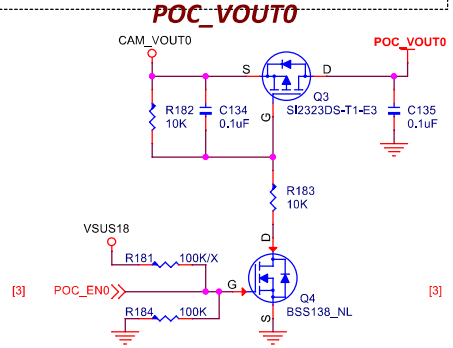
Quad FPD-Link III Deserializer Hub (DS90UB960-Q1)

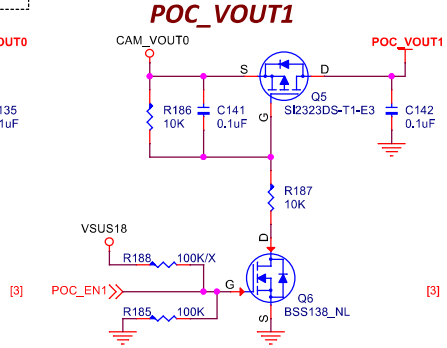
VDD11	1.1V,<310mA
VDD18	1.8V,<340mA
VDDIO	1.8V,<3mA

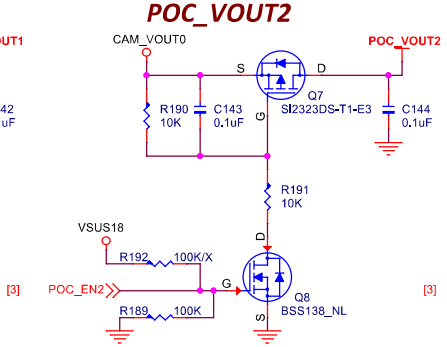
Power On Sequence: VDD18&VDDIO-->VDD11-->PDB

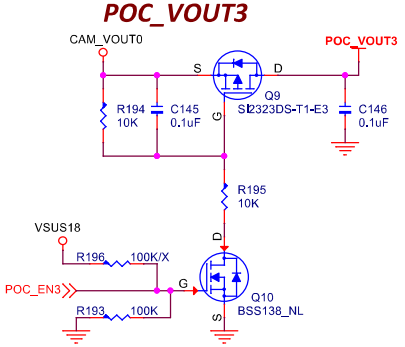
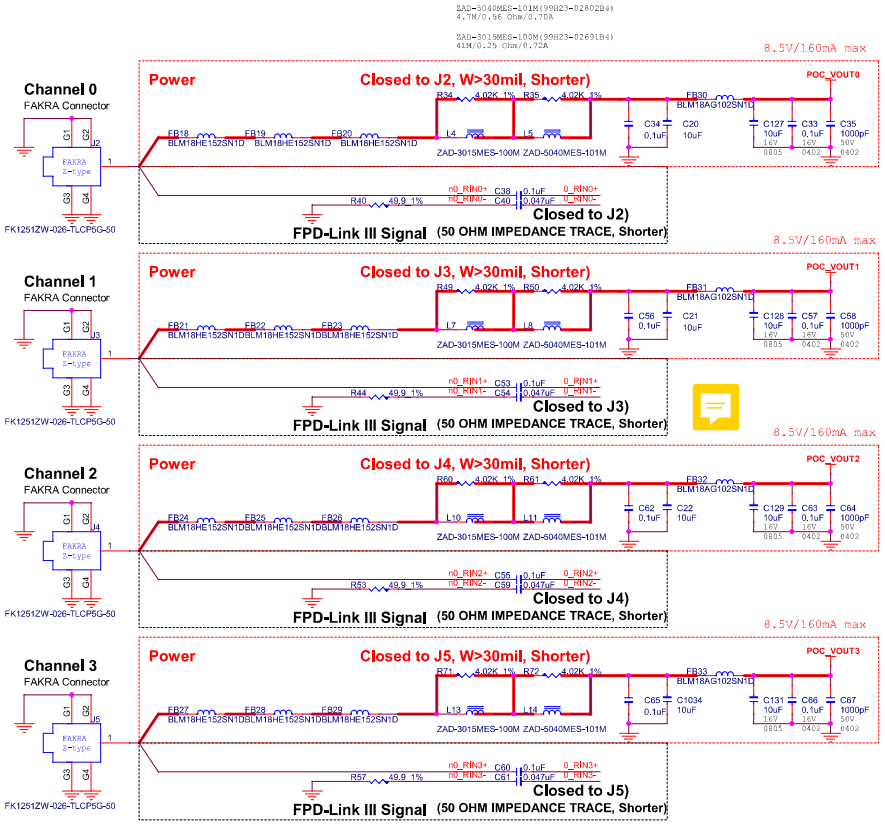
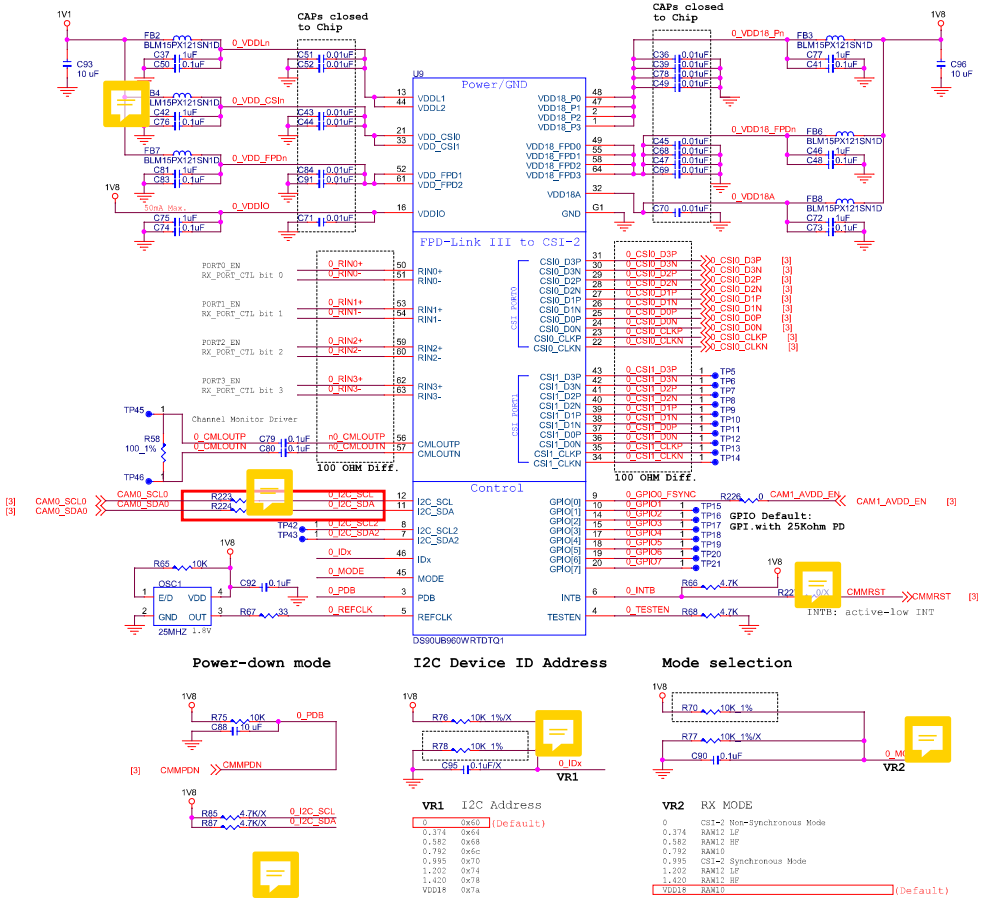
NOTE
CSI-2 TX data rate = 1.664 Gbps(4lane)
4 x FPD-Link III RX inputs (4.16Gbps)
<Non-replicate Mode>
Default registers

CAMERA Module
8.5V/160mA/1pcs (720P)









Quad FPD-Link III Deserializer Hub with MIPI CSI2 output
(DS90UB960-Q1)