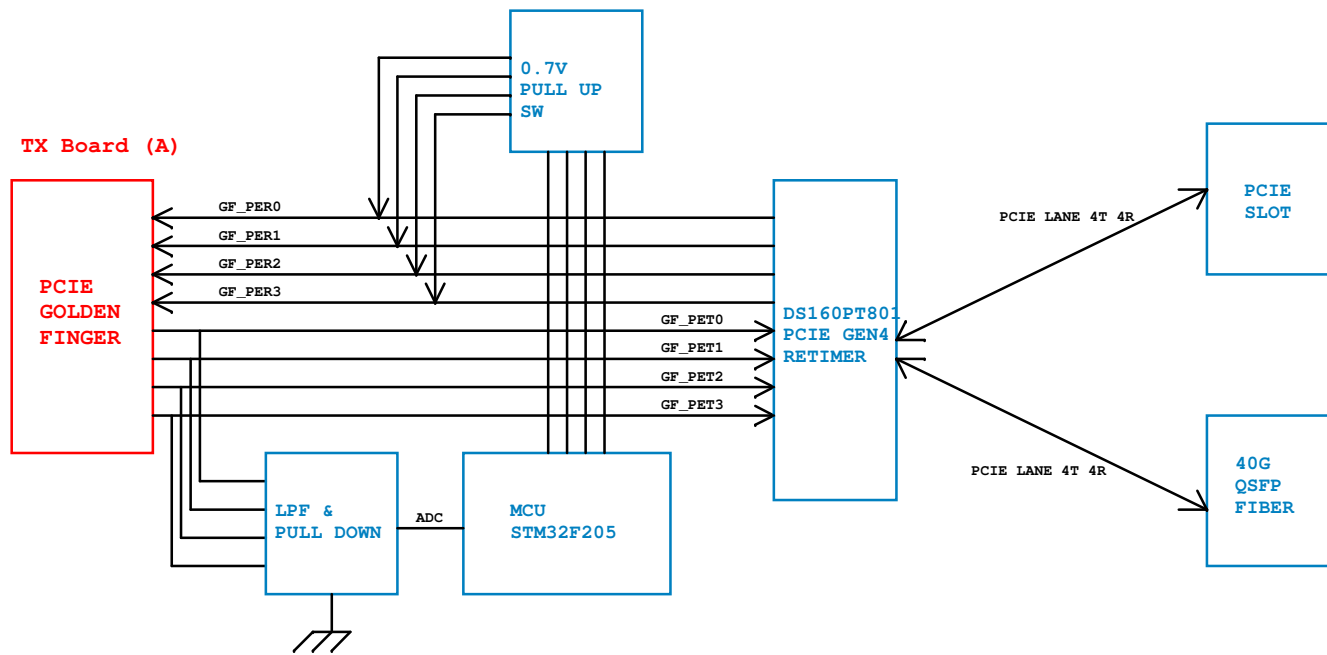
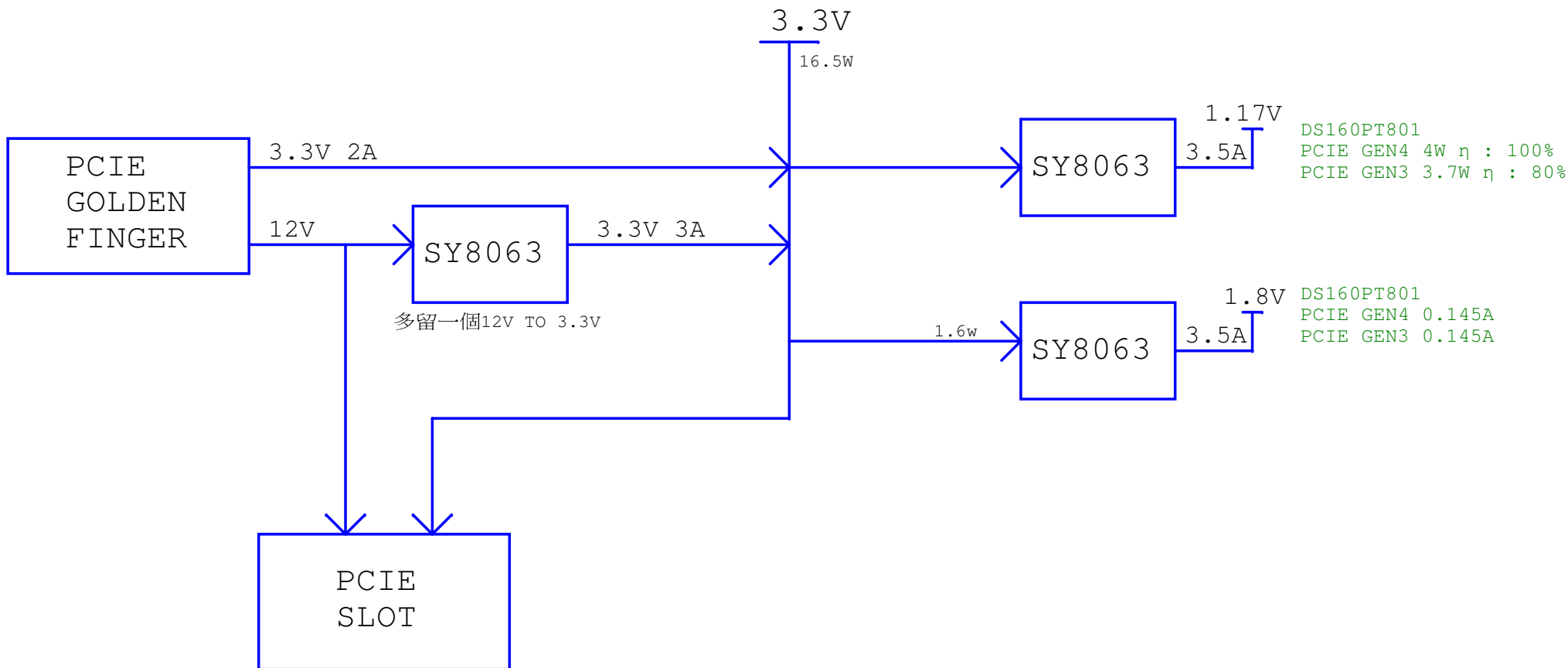
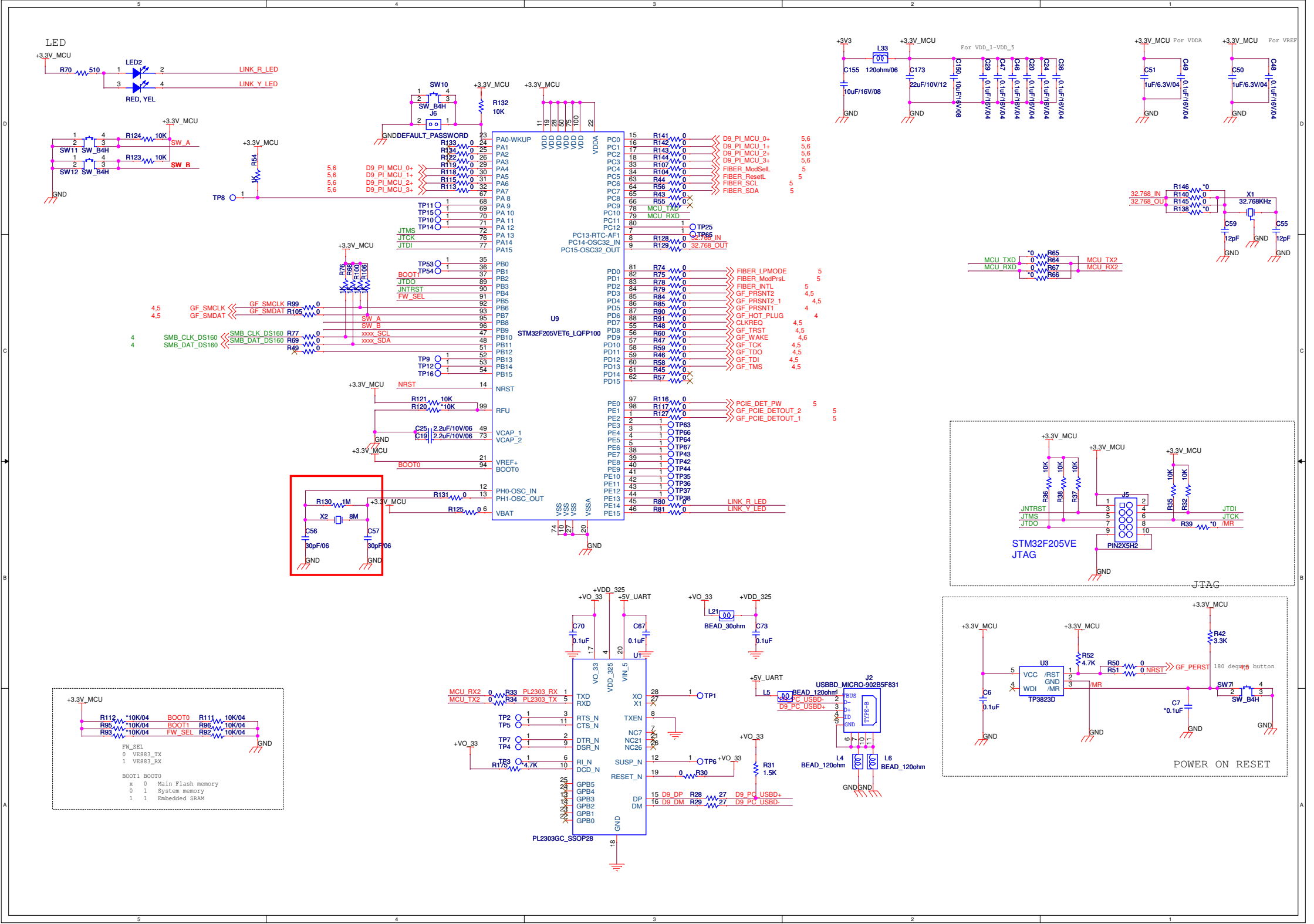
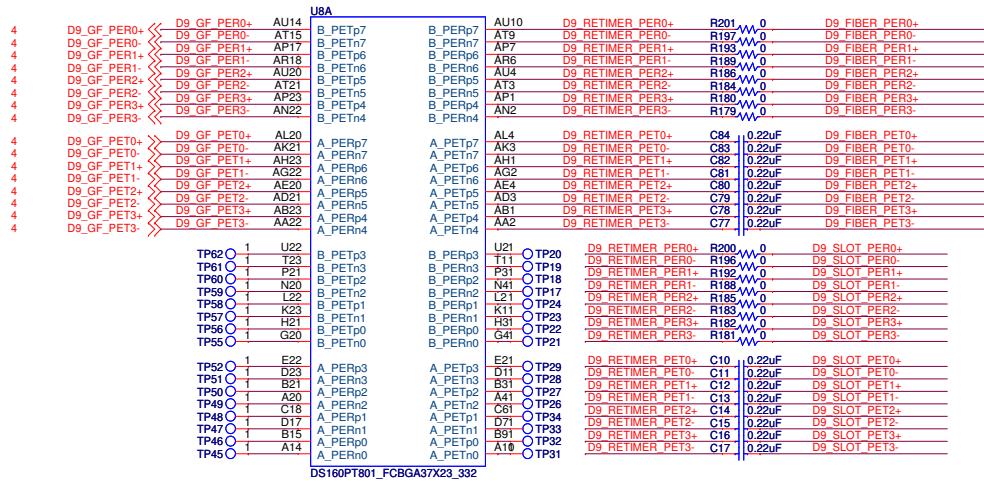




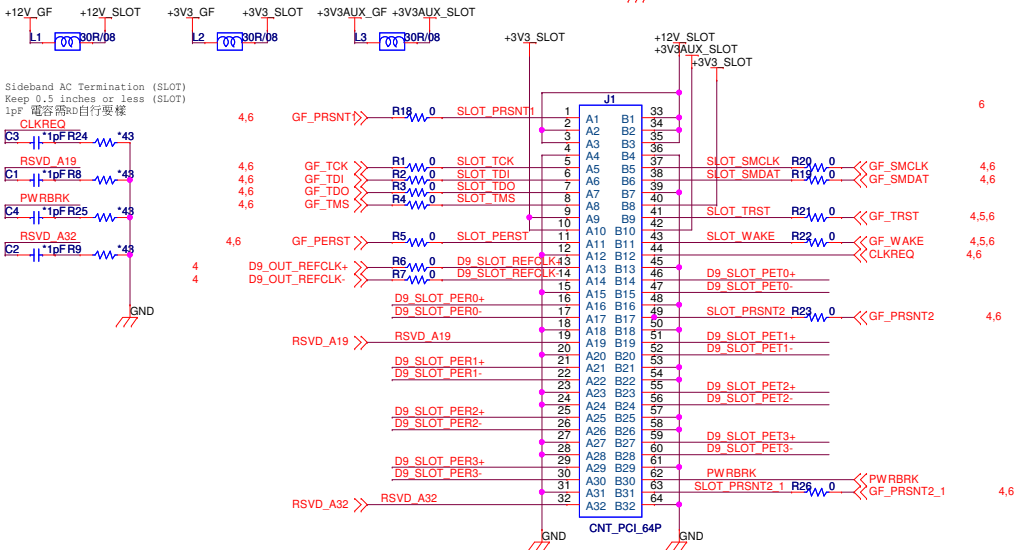
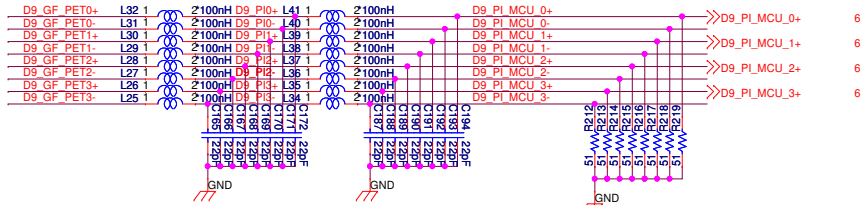


[illegible]



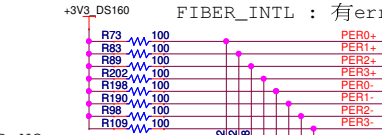
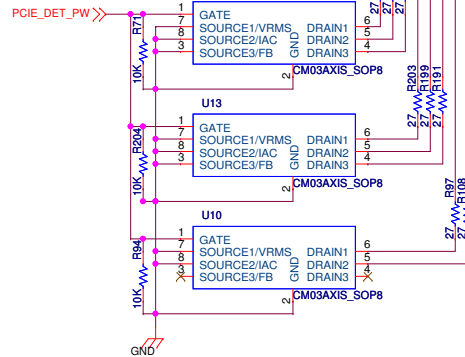


PCIE LC PI low pass filter to MCU



Sideband AC Termination (SLOT)
Keep 0.5 inches or less (SLOT)
1pF 電容需自行安裝

HI : POWER NO
 $3.3 \times (27/100+27) = 0.7V$



FIBER_ModSclL : 拉LOW I2C 才會動作
FIBER_ResetL : LOW RESET
FIBER_LPMODE : PULL UP 省電
FIBER_ModPrsL : 偵測模組插上, 插上模組內部pull low
FIBER_INTL : 有error 會拉low / rx 沒有插上也會拉low

