

BOARD-TO-BOARD CONNECTOR

20/8 Added TP for 5 pins
26/8 Added TP on USB FLT/EN

Verify footprint - so pin 1 is not connecting pin 2 on other side !!!

Power SWITCH



Phono Jack

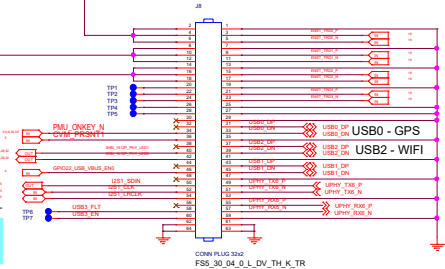


PMU ONKEY N
CVM_PRESNT1

POWER CONTROL

GBE LEDs
WIFI & GPS Vbus enable
MIC I2S
USB3 EN/FLT

Back-door for slw status/control over USB ports



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ASSEMBLY - DESCRIPTION

PCB DETAIL

EXTRUDER

600-82822-BASE-700

PCB REV

600-82822-BASE-700

DATE

12/1/2018

REV

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12/1/2018

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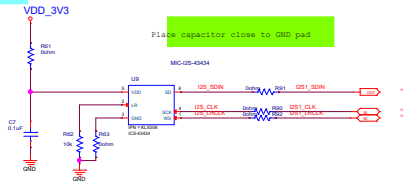
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I2S MICROPHONE

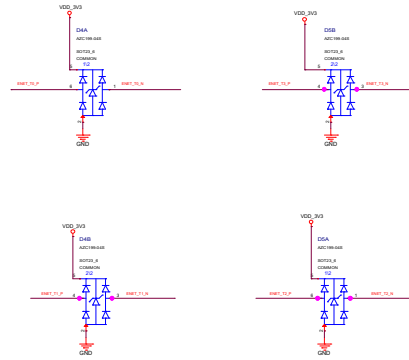
9/7 Igor asked to use other mic, and place with camera
11/7 Agreed on ICS-43434 from TDK
11/8 Added DR on Vdd/Gnd for debug options. (There are DR on signals in CVM page)
20/8 Added series resistors on I2S lines



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CASSEMBLY DESCRIPTION		NVIDIA CORPORATION	
PAGE DETAIL		100-000-0000	
LOOSE and TO Auto Header		600-82822-BASE-700	
		PAGE 18 OF 30	
		DATE 21-DEC-2018	

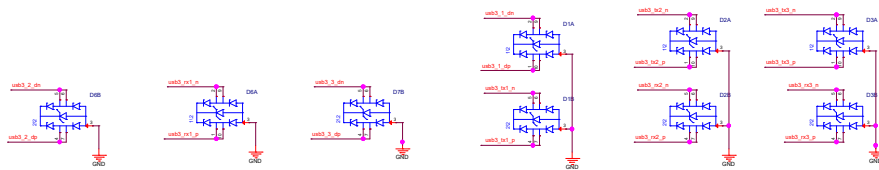
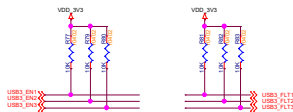
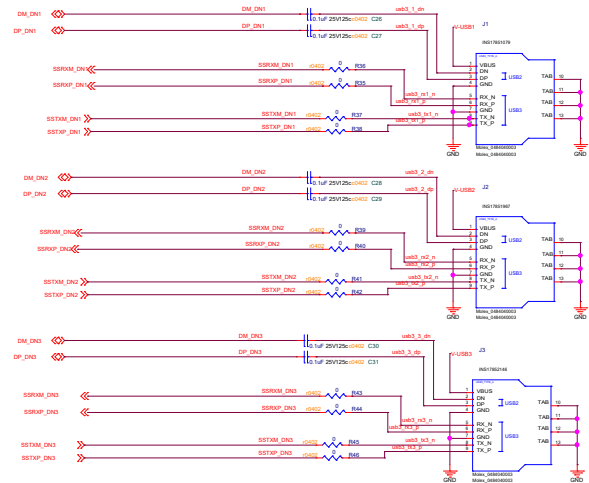
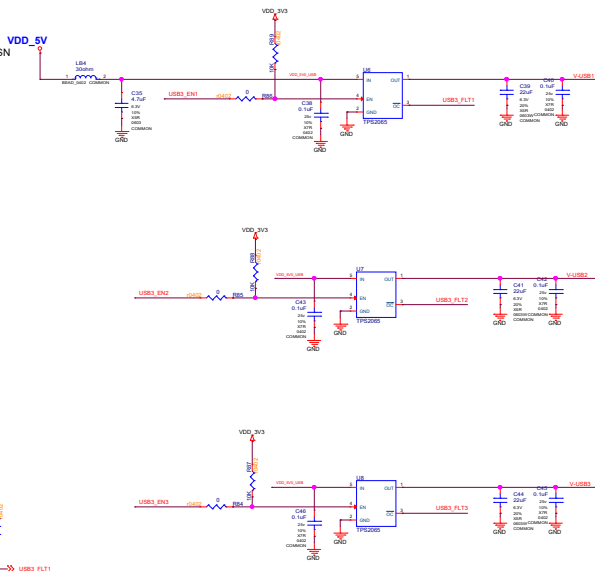
```
replaced capacitors with 100V rated (0603)
in swapt D4 D5
```



NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA			
NV_PN PCB REV BOM REV	600-82822-BASE-700 P2822-803 A	PAGE DATE	10 OF 32 21-AUG-2018

USB3 Hub

- 29/7 Removed FB from connectors - as in new RefDSN
- 4/8 ADD FB to supply - to isolate from other lines
- 4/8 ADD Power switches
- 5/8 Connected ESD (Added 3)
- 14/8 Changed pin, changed symbol - only 2 shields
- 3/9 Swap diodes per layout



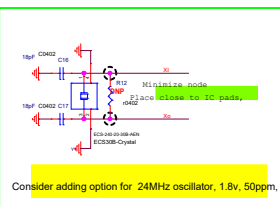
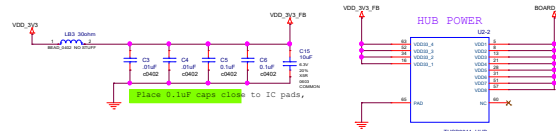
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ASSEMBLY "ASSEMBLY DESCRIPTION"
PAGE DETAIL: USB3 Connector

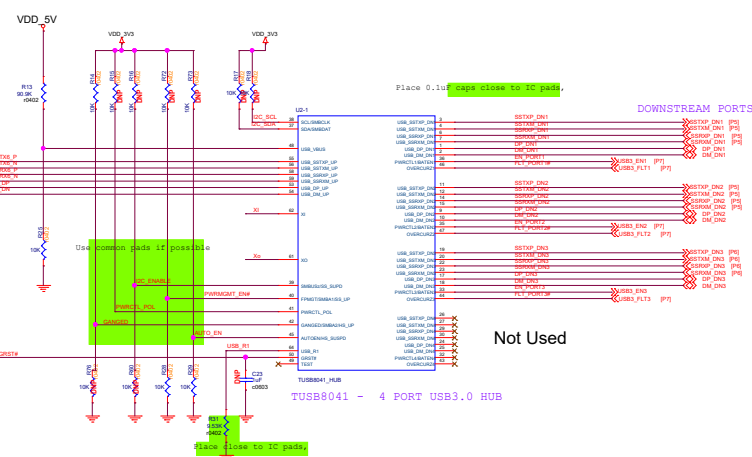
NVIDIA CORPORATION			
2000 SHAW BLVD SANTA CLARA, CA 95050, USA			
REV	600-82822-BASE-700	DATE	10/10/2018
REV	700	DATE	2/1/2019

USB3 Hub

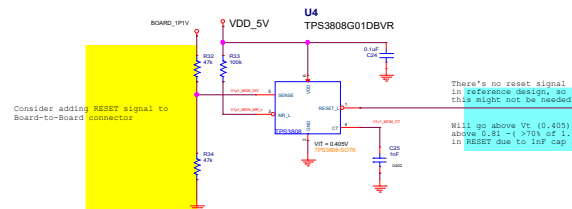
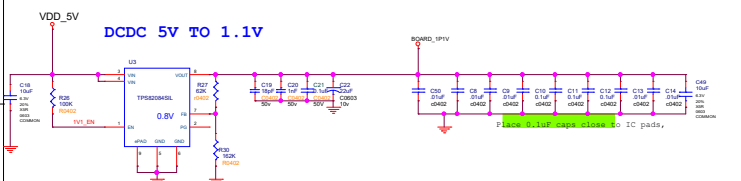
4/8 ADD FB to 3.3V supply - to isolate from other lines
20/8 Removed duplicate PU on EN. Clean-up: moved items etc.



Consider adding option for 24MHz oscillator, 1.8v, 50ppm, <50ps p-p jitter



DOWNSTREAM PORTS



There's no reset signal in reference design, so this might not be needed

Will go above V_t (0.405) when power is above 0.81 - (>70% of 1.1.), and will keep in RESET due to 1nF cap in CT.

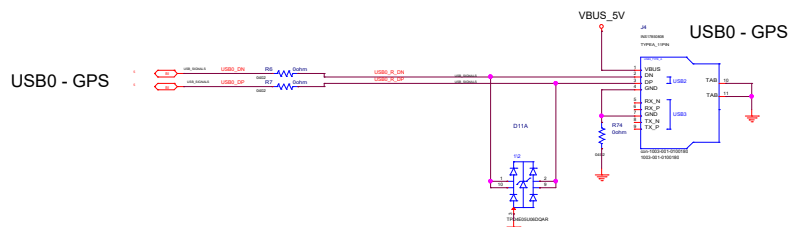
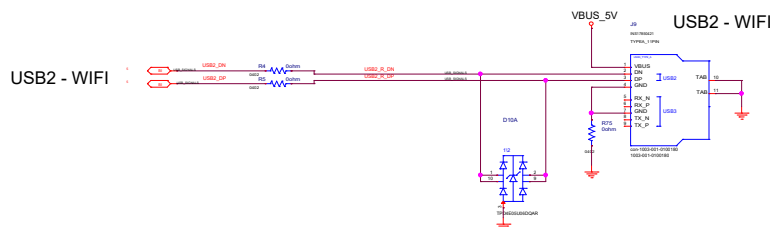
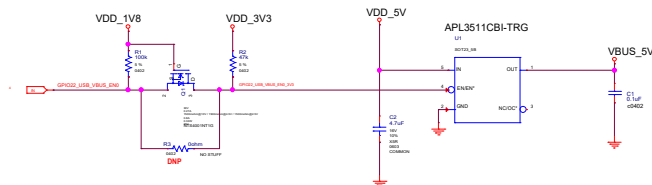
	SIGNAL	DEFINITION
NC	IC_ENABLE	IEEE-13C ENABLED / LOW - REFROM, Can be left H
LOW	PERMIST_EN	LOW-PWR MANAGEMENT ENABLED
NC	PRACT_POL	IEEE-ACTIVE RISE POLARITY
HIGH	GARSED	LOW-INDIVIDUAL PORT CONTROL
LOW	AUTO_EN	LOW-AUTO MODE ENABLED

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3701 SAN TOMAS EXPRESS DRIVE SANTA CLARA, CA 95050, USA			
NV_PN	600-82822-BASE-700		
PCB REV	P2822-003	PAGE	18 OF 32
BOM REV	A	DATE	21-AUG-2018

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USB - WiFi & GPS

- 30/7 Assigned GPIO to Vbus-enable
- 30/7 Defined B2B connectors
- 31/7 Replaced ESD on USB2
- 12/8 Removed CM Chock
- 13/8 Changed FB on connectors (GND) to 0R (per P2597-c02)
- 20/8 Changed ESD to have 2 separate



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2000 SHAW BLVD
SANTA CLARA, CA 95050, USA



Rev. 1.0 600-82822-BASE-700

PCB REV: P2597-001 PAGE: 18 OF 30
DATE: 21-DEC-2018

K-Tigs USB BOARD



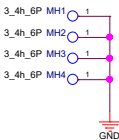
30/7 Copied signals to B2B connector page
8/8 Moved 'Audio' power switch to Ktigs-USB
11/8 Moved MIC to Ktigs-USB
14/8 Review USB-Hub

20/8 Removed 'non-bom' section from this page
8/9 Changes per Arkadi:
Removed Fiducials,
Connected 'GND2' pin of USB connectors J4,J9 to GND
Connected ETH LEDs to VDD_3V (not VSUP3_3)
Added 'D' prefix for D10 and D11
8/9 v05 GBE - only GND - no AGND., J8 ref-des visible
8/9 v06 GBE - Removed resistors/capacitors connected to CGND

Fiducials



Mechanical Holes



11:52:22			
Title		BOARD	
Size	Document Number	Rev	
B	<Doc>	V1	
Date:	Sunday, September 05, 2019	Sheet	1 of 7