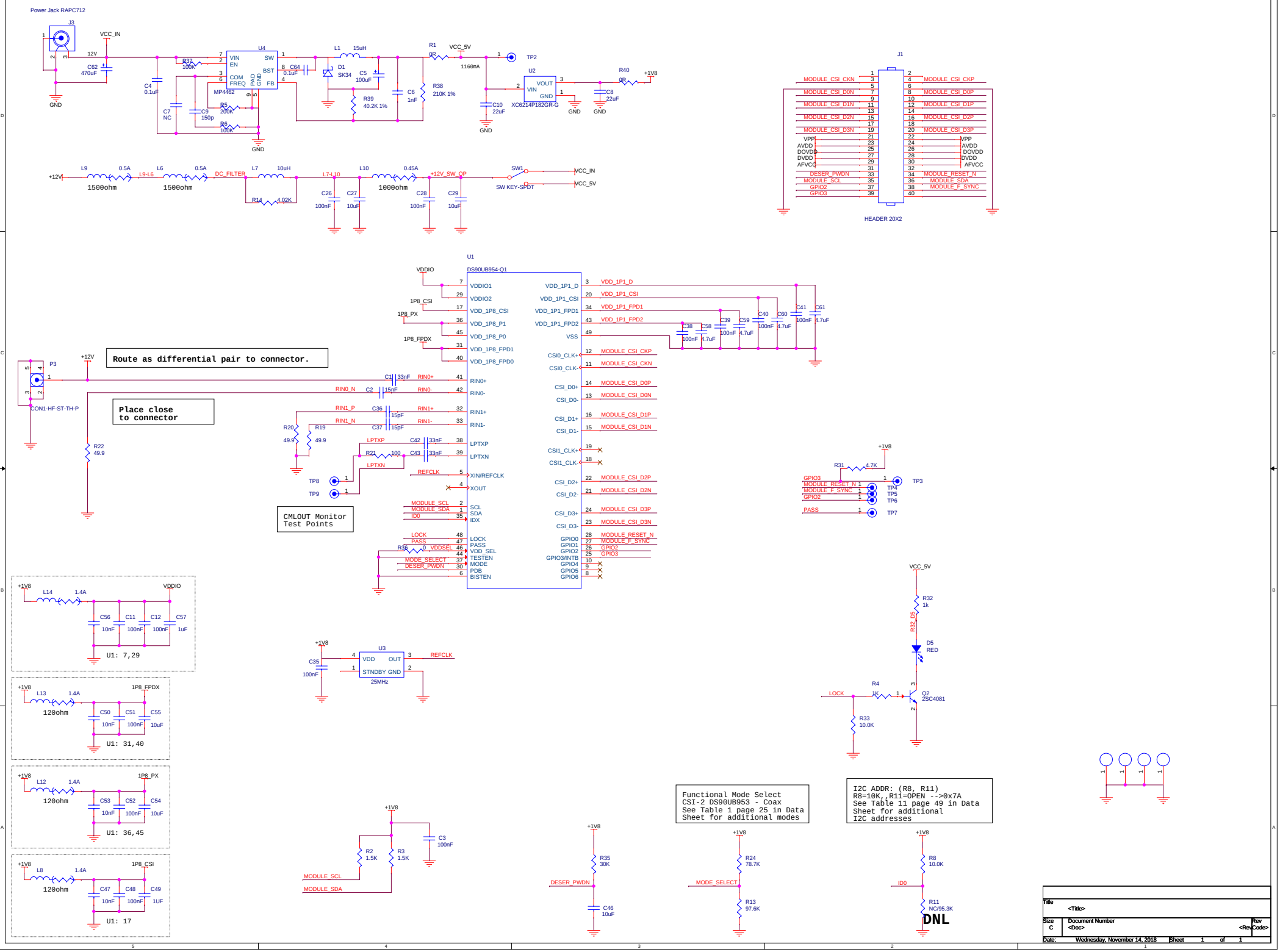
Power Jack RAPC712
J3
VCC_IN
12V
C62 470uF
GND
U4
MP4462
VIN
EN
COM
FREQ
SW
BST
FB
PAD GND
R37 100K
R5 200K
R6 100K
C4 0.1uF
C7 NC
C9 150p
L1 15uH
D1 SK34
C64 0.1uF
C5 100uF
R39 40.2K 1%
C6 1nF
R38 210K 1%
R1 0R
VCC_5V
1160mA
TP2
U2
XC6214P182GR-G
VOUT
VIN
GND
C10 22uF
C8 22uF
R40 0R
+1V8
+12V
L9 1500ohm
0.5A
L9-L6
L6 1500ohm
0.5A
DC_FILTER
L7 10uH
R14 4.02K
L7-L10
L10 1000ohm
0.45A
+12V_SW_OP
C26 100nF
C27 10uF
C28 100nF
C29 10uF
SW1
SW KEY-SPDT
VCC_IN
VCC_5V
J1
HEADER 20X2
MODULE_CSI_CKN
MODULE_CSI_CKP
MODULE_CSI_D0N
MODULE_CSI_D0P
MODULE_CSI_D1N
MODULE_CSI_D1P
MODULE_CSI_D2N
MODULE_CSI_D2P
MODULE_CSI_D3N
MODULE_CSI_D3P
VPP
AVDD
DOVDD
DVDD
AFVCC
DESER_PWDN
MODULE_RESET_N
MODULE_SCL
MODULE_SDA
GPIO2
GPIO3
MODULE_F_SYNC
U1
DS90UB954-Q1
VDDIO
VDDIO1
VDDIO2
VDD_1P8_CSI
VDD_1P8_P1
VDD_1P8_P0
VDD_1P8_FPD1
VDD_1P8_FPD0
1P8_CSI
1P8_PX
1P8_FPDX
VDD_1P1_D
VDD_1P1_CSI
VDD_1P1_FPD1
VDD_1P1_FPD2
VSS
CSI0_CLK+
CSI0_CLK-
CSI_D0+
CSI_D0-
CSI_D1+
CSI_D1-
CSI1_CLK+
CSI1_CLK-
CSI_D2+
CSI_D2-
CSI_D3+
CSI_D3-
GPIO0
GPIO1
GPIO2
GPIO3/INTB
GPIO4
GPIO5
GPIO6
RIN0+
RIN0-
RIN1+
RIN1-
LPTXP
LPTXN
XIN/REFCLK
XOUT
SCL
SDA
IDX
LOCK
PASS
VDD_SEL
TESTEN
MODE
PDB
BISTEN
C38 100nF
C58 4.7uF
C39 100nF
C59 4.7uF
C40 100nF
C60 4.7uF
C41 100nF
C61 4.7uF
P3
CON1-HF-ST-TH-P
+12V
R22 49.9
C1 33nF
C2 15nF
C36 15pF
C37 15pF
R20 49.9
R19 49.9
C42 33nF
C43 33nF
R21 100
TP8
TP9
R36 0
VDDSEL
REFCLK
MODULE_SCL
MODULE_SDA
ID0
LOCK
PASS
MODE_SELECT
DESER_PWDN
R31 4.7K
TP3
TP4
TP5
TP6
TP7
U3
VDD
OUT
STNDBY
GND
25MHz
C35 100nF
VCC_5V
R32 1k
R32_D5
D5 RED
R4 1K
Q2 2SC4081
R33 10.0K
+1V8
L14 1.4A
C56 10nF
C11 100nF
C12 100nF
C57 1uF
U1: 7,29
L13 1.4A
120ohm
C50 10nF
C51 100nF
C55 10uF
U1: 31,40
L12 1.4A
120ohm
C53 10nF
C52 100nF
C54 10uF
U1: 36,45
L8 1.4A
120ohm
C47 10nF
C48 100nF
C49 1UF
U1: 17
R2 1.5K
R3 1.5K
C3 100nF
R35 30K
C46 10uF
R24 78.7K
R13 97.6K
R8 10.0K
R11 NC/95.3K
DNL
Route as differential pair to connector.
Place close to connector
CMLOUT Monitor Test Points
Functional Mode Select
CSI-2 DS90UB953 - Coax
See Table 1 page 25 in Data Sheet for additional modes
I2C ADDR: (R8, R11)
R8=10K,,R11=OPEN -->0x7A
See Table 11 page 49 in Data Sheet for additional I2C addresses
Title
<Title>
Size C
Document Number
<Doc>
Rev
<RevCode>
Date: Wednesday, November 14, 2018
Sheet 1 of 1