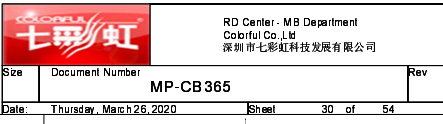
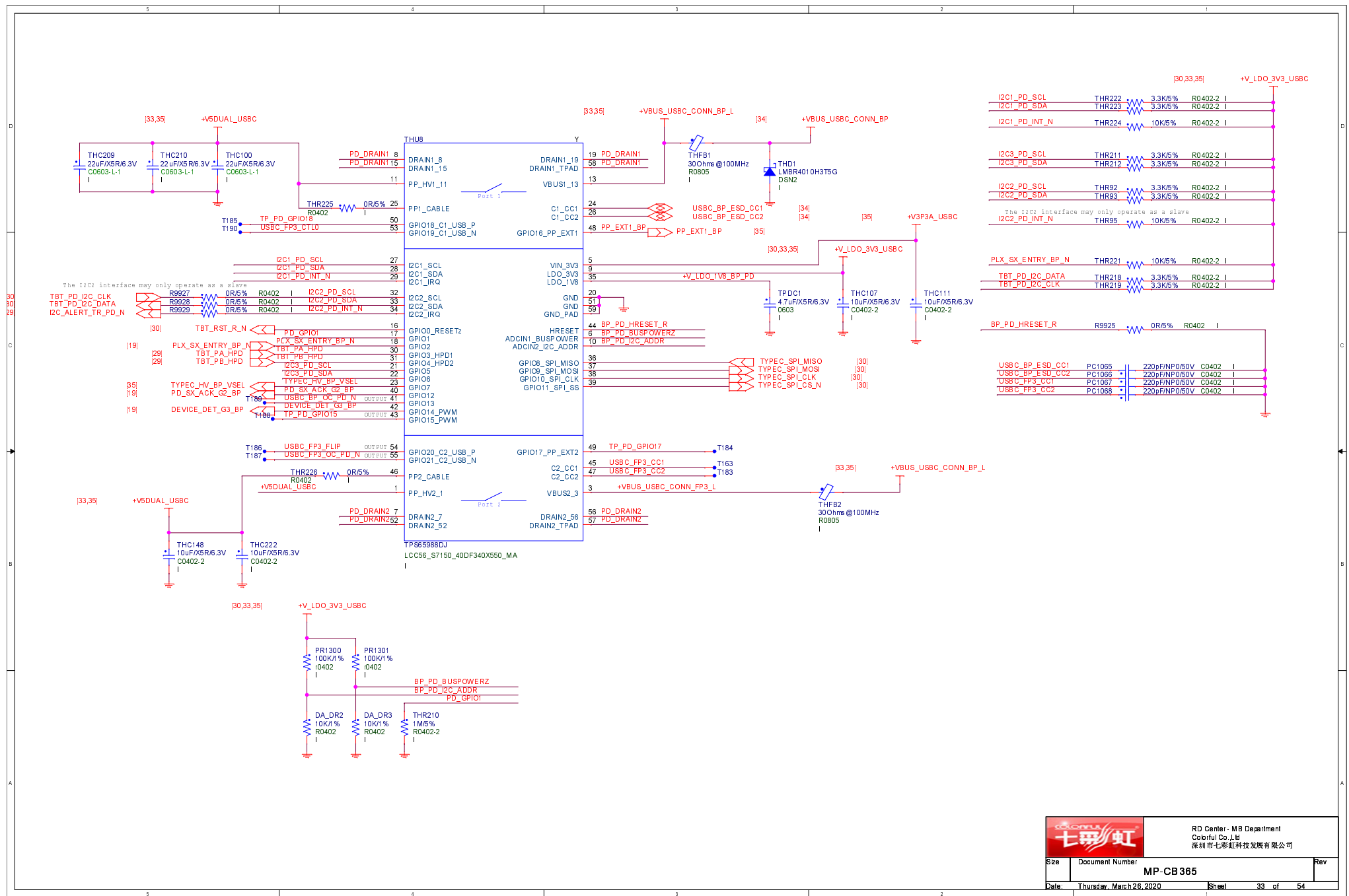
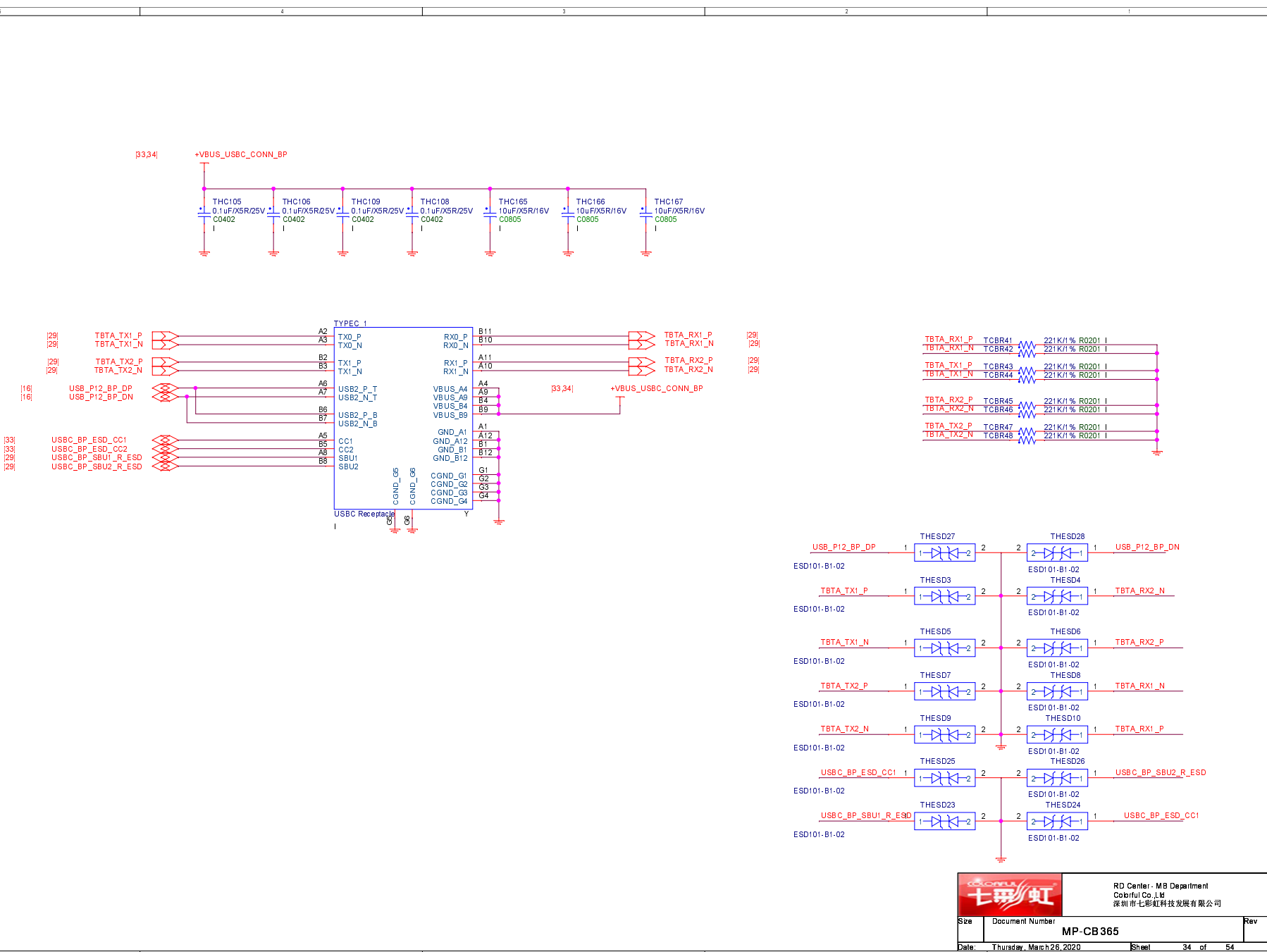


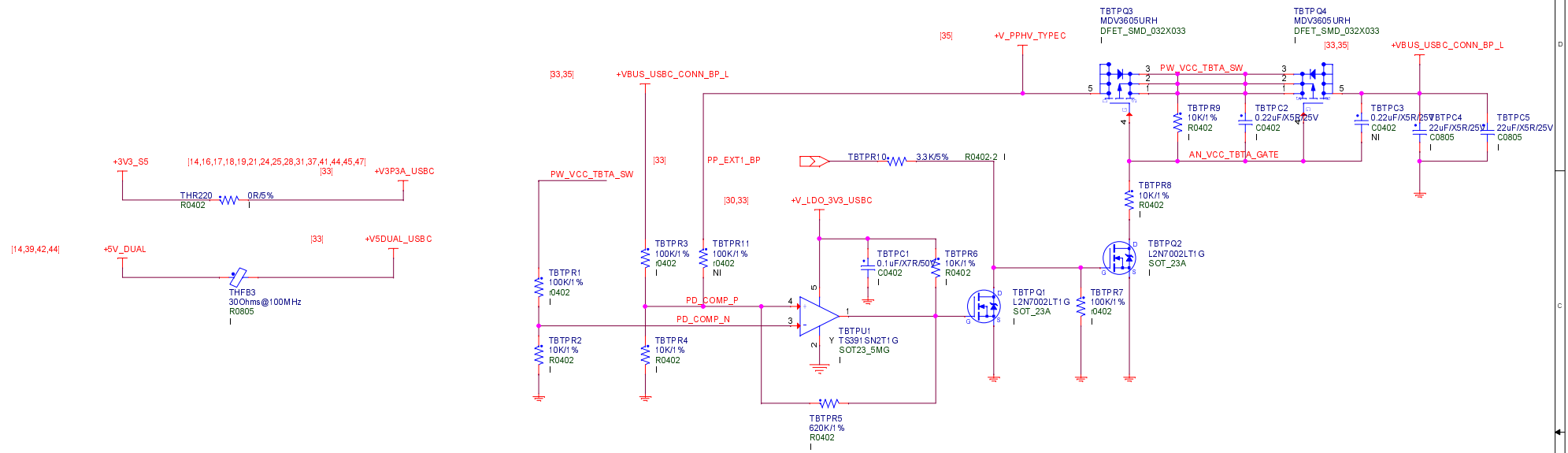
Base on INTEL TR AIC reference SCH 1.0 (2017/3/23)



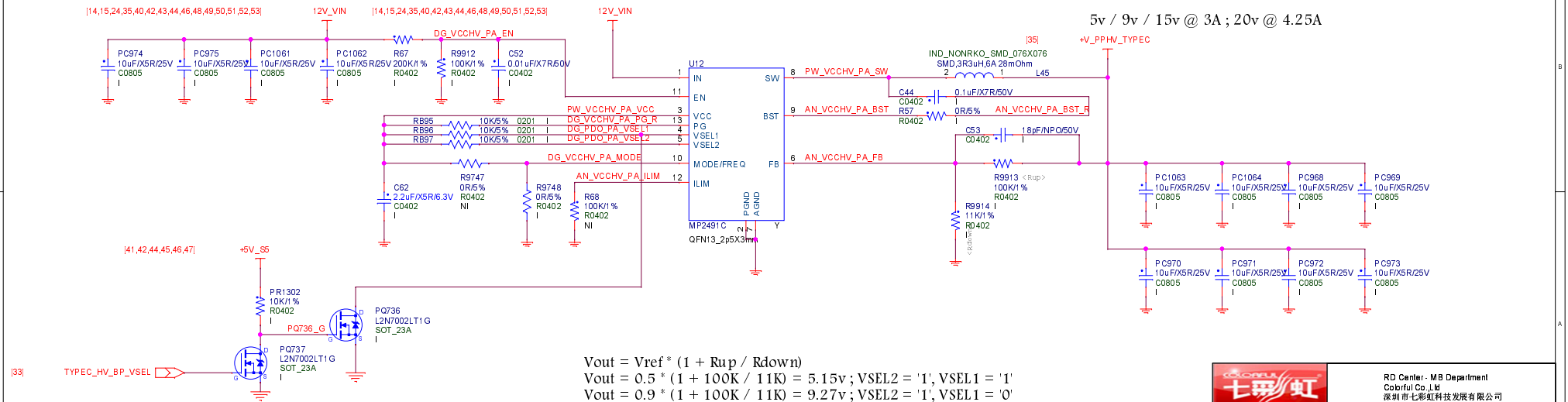




Sink Path From Port A
* Based on TPS65988 EVM



VCCHV_PA Buck Converter



Vout = Vref * (1 + Rup / Rdown)
Vout = 0.5 * (1 + 100K / 11K) = 5.15v; VSEL2 = '1', VSEL1 = '1'
Vout = 0.9 * (1 + 100K / 11K) = 9.27v; VSEL2 = '1', VSEL1 = '0'
Vout = 1.5 * (1 + 100K / 11K) = 15.46v; VSEL2 = '0', VSEL1 = '1'
Vout = 2.0 * (1 + 100K / 11K) = 20.61v; VSEL2 = '0', VSEL1 = '0'