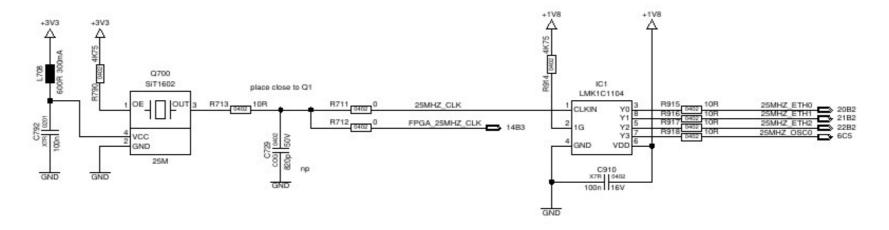
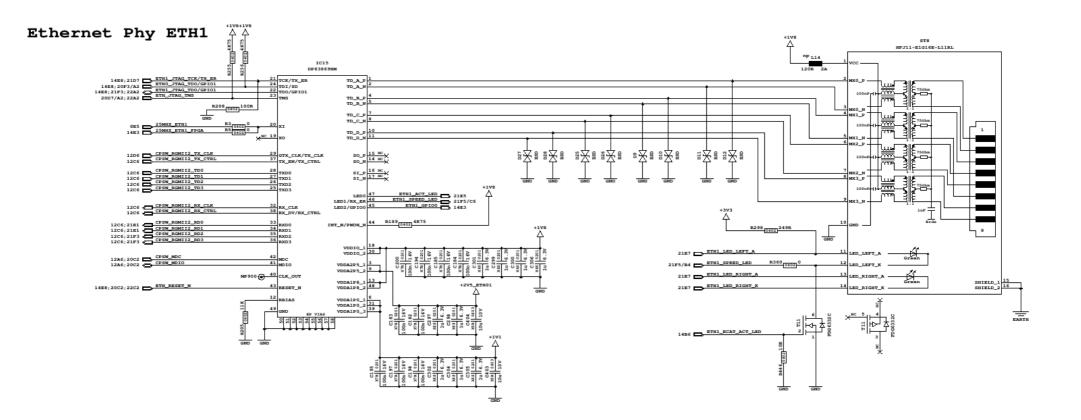
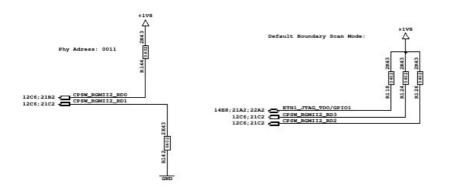
25MHz Clocksource







ETH1_JTAG_TDO/GPIO1 is pulled low from FPGA before ETH_RESET_N is deassert.

So we should be in SGMII to Copper Mode

The RGMII Interface is connected to TI Processor AM64xx. The MDIO Interface has Pull Up Resistors to 1V8 Resistor R5 is not populated