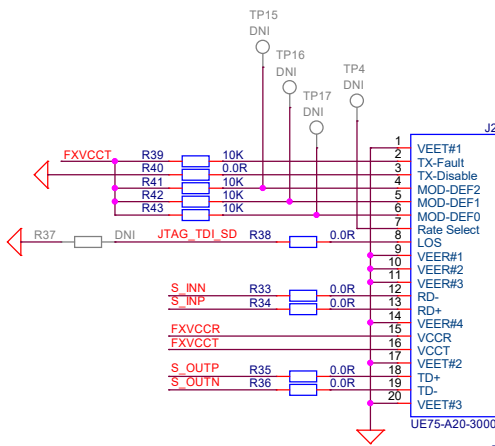
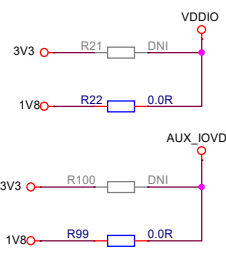
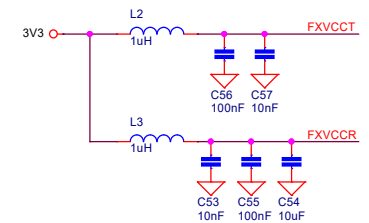
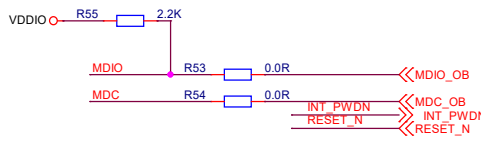
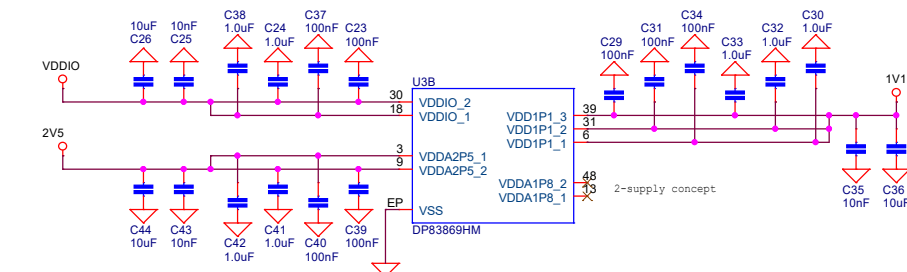
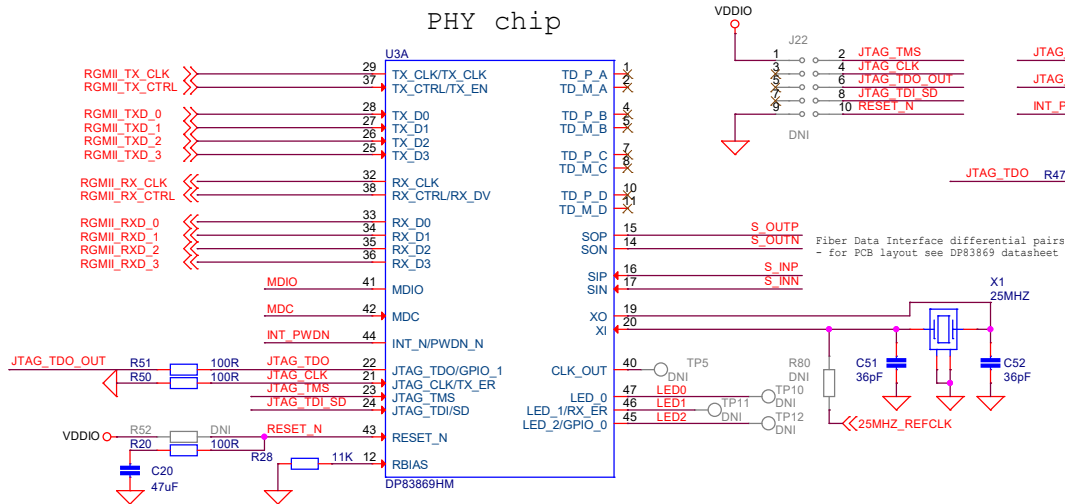
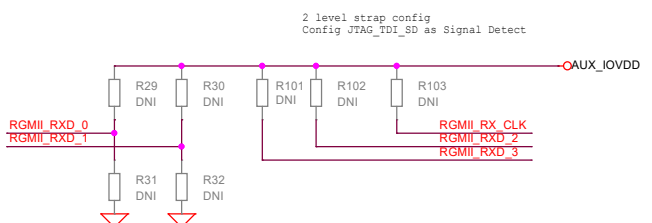
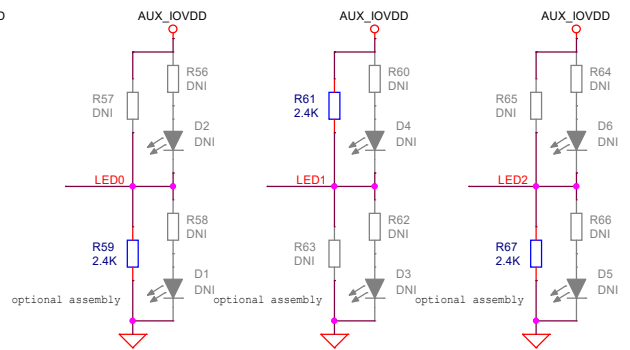




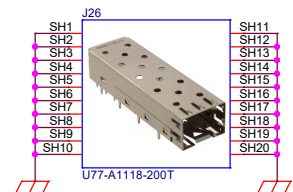
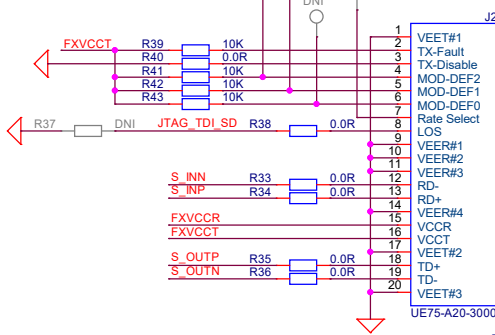
Place LEDs at BOTTOM



4 level strap config

PHY ADDR 0

RXD_0, RXD_1 = 00 (OPEN)



CAGE galvanically connected to the shielding via gasket springs

V01 Assembly

consilia_		Consilia s.r.o.	
Designator:	RGMII to SFP & PHY		Project:
Document Number:	PCBA-02-03-01 ETH SFP		<Project>
Date: 2020-02-30	Dept: HW	Name: <Editor>	Size: Revision:
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			Sheet: 2 of 4