In the Pin Functions you will see the por state of every pin. And UART\_TX say's UART\_RX @ POR  
So if i understand it right: Primary TX to Secondary RX and Secondary TX to Primary RX?

**Yes that connection is Correct, Im sorry to say this is an error. I will be informing the datasheet team for updating asap.**

- To be sure: If I give every [TPS65982](http://www.ti.com/product/TPS65982) the it's own flash, (we are looking into 7 UCB-C connectoren) I need 7 different I2C addresses. So i need to use the i2c\_address pin.  
If I understand it right: It will not deliver any problems if I put the [TPS65982](http://www.ti.com/product/TPS65982) for example in 'UART Slave 5' to get address 0x03 on i2c because the MISO pin will be high @ boot? So it will still load  the config from the flash? Or it must always be in "SPI Owner, UART Master 0 (Primary)" when the [TPS65982](http://www.ti.com/product/TPS65982) has its own flash.

**The 7 (virtual pin strapping) possibilities you are referring to are for the primary to talk to 7 different i2c address’s. If you want to flash 7 devices at the same time then you will need 4 primaries and 3 secondaries.**

- If i am not using daisy chain, what to do with the UART\_TX and RX?  EVM leaving them floating, datasheet say's @ TX: 'Connect UART\_RX to UART\_TX when not connected to another [*TPS65982*](http://www.ti.com/product/TPS65982)'  and @ RX 'Connect UART\_RX to UART\_TX when not connected to another [*TPS65982*](http://www.ti.com/product/TPS65982) ***and ground pin through a 100-kΩ resistance.***'  So short RX and TX en connect it thro ugh a 100k to ground?

**This connect is Correct, I believe you should follow the datasheet to make the part as robust as possible. I will ask the datasheet team, can you leave them floating. I will update you with the response from them.**